**SPEC® CPU2017 Integer Rate Result**  

**Dell Inc.**  
PowerEdge T340 (Intel Xeon E-2174G, 3.80GHz)  

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
<th>SPECrate2017_int_peak</th>
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<tbody>
<tr>
<td>30.6</td>
<td>31.6</td>
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**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Jan-2019  
**Hardware Availability:** Dec-2018  
**Software Availability:** Apr-2018

![Graph showing SPECrate2017_int_base and SPECrate2017_int_peak](chart.png)

**Hardware**

- **CPU Name:** Intel Xeon E-2174G  
- **Max MHz.:** 4700  
- **Nominal:** 3800  
- **Enabled:** 4 cores, 1 chip, 2 threads/core  
- **Orderable:** 1 chip  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **Cache L2:** 256 KB I+D on chip per core  
- **Cache L3:** 8 MB I+D on chip per chip  
- **Memory:** 64 GB (4 x 16 GB 2Rx8 PC4-2666V-R)  
- **Storage:** 1 x 960 GB SATA SSD  
- **Other:** None

**Software**

- **OS:** SUSE Linux Enterprise Server 12 SP3  
- **Compiler:** C/C++: Version 18.0.2.20180210 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.2.20180210 of Intel Fortran Compiler for Linux  
- **Parallel:** No  
- **Firmware:** Version 1.0.1 released Oct-2018  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other:** jemalloc memory allocator v5.0.1

---

**Copies**

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**SPEC®**

Standard Performance Evaluation Corporation (info@spec.org)  
https://www.spec.org/
### SPEC CPU2017 Integer Rate Result

#### Dell Inc.

PowerEdge T340 (Intel Xeon E-2174G, 3.80GHz)

**SPEC CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**SPECratio2017_int_base** = 30.6  
**SPECratio2017_int_peak** = 31.6

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### Results Table

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</tbody>
</table>

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32;/home/cpu2017/lib/intel64;/home/cpu2017/je5.0.1-32;/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:

(Continued on next page)
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PowerEdge T340 (Intel Xeon E-2174G, 3.80GHz)  

SPEC CPU2017 Integer Rate Result

![spec](https://www.spec.org)

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Dell Inc.

3.80GHz)

PowerEdge T340 (Intel Xeon E-2174G, 3.80GHz)

SPECrate2017_int_base = 30.6

SPECrate2017_int_peak = 31.6

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Jan-2019
Hardware Availability: Dec-2018
Software Availability: Apr-2018

General Notes (Continued)

numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5

Platform Notes

BIOS settings:
Virtualization Technology disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Logical Processor enabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-icjc Wed Jan 2 06:24:50 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name: Intel(R) Xeon(R) E-2174G CPU @ 3.80GHz
  1 "physical id"s (chips)
  8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores: 4
siblings: 8
physical 0: cores 0 1 2 3

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 8
On-line CPU(s) list: 0-7
Thread(s) per core: 2
Core(s) per socket: 4
Socket(s): 1

(Continued on next page)
## Platform Notes (Continued)

```plaintext
NUMA node(s):          1
Vendor ID:             GenuineIntel
CPU family:            6
Model:                 158
Model name:            Intel(R) Xeon(R) E-2174G CPU @ 3.80GHz
Stepping:              10
CPU MHz:               4431.750
CPU max MHz:           4700.0000
CPU min MHz:           800.0000
BogoMIPS:              7583.98
Virtualization:        VT-x
L1d cache:             32K
L1i cache:             32K
L2 cache:              256K
L3 cache:              8192K
NUMA node0 CPU(s):     0-7
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts dtherm hwp hwp_act_window hwp_epp intel_pt rsb_cttsxw spec_ctrl stibp retline kaiser tpr_shadow vmx flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm mpx rdseed adx smap clflushopt xsaveopt xsavec xgetbv1
```

```
/proc/cpuinfo cache data
cache size : 8192 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```plaintext
available: 1 nodes (0)
node 0 cpus: 0 1 2 3 4 5 6 7
node 0 size: 64277 MB
node 0 free: 63804 MB
node distances:
  node 0
    0: 10
```

From /proc/meminfo

```plaintext
MemTotal:       65820248 kB
HugePages_Total:       0
Hugepagesize:       2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP3
```

(Continued on next page)
Dell Inc.
PowerEdge T340 (Intel Xeon E-2174G, 3.80GHz)  SPECrate2017_int_base = 30.6
SPECrate2017_int_peak = 31.6

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Platform Notes (Continued)

From /etc/*release* /etc/*version*
SuSE-release:
   SUSE Linux Enterprise Server 12 (x86_64)
   VERSION = 12
   PATCHLEVEL = 3
   # This file is deprecated and will be removed in a future service pack or release.
   # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP3"
      VERSION_ID="12.3"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
   Linux linux-icjc 4.4.126-94.22-default #1 SMP Wed Apr 11 07:45:03 UTC 2018 (9649989)
   x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 3 Jan 2 06:24 last=5

SPEC is set to: /home/cpu2017
   Filesystem  Type  Size  Used Avail Use% Mounted on
   /dev/sda2    xfs  301G  16G  285G   6% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   BIOS Dell Inc. 1.0.1 10/19/2018
   Memory:
      3x 00AD00000A02 HMA82GU7CJR8N-VK 16 GB 2 rank 2666
      1x 00AD00000A07 HMA82GU7CJR8N-VK 16 GB 2 rank 2666

(End of data from sysinfo program)
Dell Inc.
PowerEdge T340 (Intel Xeon E-2174G, 3.80GHz)

SPECrated2017_int_base = 30.6
SPECrated2017_int_peak = 31.6

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Compiler Version Notes

==============================================================================
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
  557.xz_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
CC   500.perlbench_r(peak) 502.gcc_r(peak) 505.mcf_r(peak) 525.x264_r(peak)
  557.xz_r(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
  541.leela_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
  541.leela_r(peak)
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icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
FC  548.exchange2_r(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
FC  548.exchange2_r(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Dell Inc.
PowerEdge T340 (Intel Xeon E-2174G, 3.80GHz)

| SPECrate2017_int_base | 30.6 |
| SPECrate2017_int_peak | 31.6 |

| CPU2017 License: | 55 |
| Test Sponsor: | Dell Inc. |
| Tested by: | Dell Inc. |

**Base Compiler Invocation**

C benchmarks:
```
icc -m64 -std=c11
```

C++ benchmarks:
```
icpc -m64
```

Fortran benchmarks:
```
ifort -m64
```

**Base Portability Flags**

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:
```
-W1, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

C++ benchmarks:
```
-W1, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks:
```
-W1, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-L/usr/local/je5.0.1-64/lib -ljemalloc
```
Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64 -std=c11
```

```
502.gcc_r:icc -m32 -std=c11 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
523.xalancbmk_r:icpc -m32 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin
```

Fortran benchmarks:

```
ifort -m64
```

Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-lljemalloc
```

```
502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -lljemalloc
```

```
505.mcf_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -lljemalloc
```

(Continued on next page)
### Dell Inc.

**PowerEdge T340 (Intel Xeon E-2174G, 3.80GHz)**

<table>
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<th>30.6</th>
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</thead>
<tbody>
<tr>
<td>SPECrate2017_int_peak</td>
<td>31.6</td>
</tr>
</tbody>
</table>

- **CPU2017 License**: 55
- **Test Sponsor**: Dell Inc.
- **Tested by**: Dell Inc.

#### Peak Optimization Flags (Continued)

525.x264_r: `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3 -fno-alias -L/usr/local/je5.0.1-64/lib -ljemalloc`

557.xz_r: Same as 505.mcf_r

**C++ benchmarks**:

520.omnetpp_r: `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

523.xalancbmk_r: `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-32/lib -ljemalloc`

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

**Fortran benchmarks**:

- `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc`

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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