## SPEC® CPU2017 Integer Rate Result

**Inspur Corporation**

**Inspur NF5180M5 (Intel Xeon Silver 4116)**

**SPECrate2017_int_base = 109**

**SPECrate2017_int_peak = 116**

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>3358</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Inspur Corporation</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Inspur Corporation</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name: Intel Xeon Silver 4116</td>
<td>OS: SUSE Linux Enterprise Server 12 SP2 4.17.9-69-default</td>
</tr>
<tr>
<td>Max MHz.: 3000</td>
<td>Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td>Nominal: 2100</td>
<td>Parallel: No</td>
</tr>
<tr>
<td>Enabled: 24 cores, 2 chips, 2 threads/core</td>
<td>Firmware: Version 4.0.4 released Jul-2018</td>
</tr>
<tr>
<td>Orderable: 1,2 chips</td>
<td>File System: xfs</td>
</tr>
<tr>
<td>Cache L1: 32 KB I + 32 KB D on chip per core</td>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>L2: 1 MB I+D on chip per core</td>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>L3: 16.5 MB I+D on chip per chip</td>
<td>Peak Pointers: 32/64-bit</td>
</tr>
<tr>
<td>Other: None</td>
<td>Other: jemalloc: jemalloc memory allocator library V5.0.1</td>
</tr>
<tr>
<td>Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)</td>
<td></td>
</tr>
<tr>
<td>Storage: 1 x 200 GB SATA SSD</td>
<td></td>
</tr>
<tr>
<td>Other: None</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate2017_int_base (109)</th>
<th>SPECrate2017_int_peak (116)</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>102</td>
<td></td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>96.5</td>
<td></td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>71.2</td>
<td></td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>71.3</td>
<td></td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>111</td>
<td></td>
</tr>
<tr>
<td>525.x264_r</td>
<td>93.8</td>
<td></td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>93.9</td>
<td></td>
</tr>
<tr>
<td>541.leela_r</td>
<td>86.2</td>
<td></td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>86.9</td>
<td></td>
</tr>
<tr>
<td>557.xz_r</td>
<td>80.8</td>
<td></td>
</tr>
</tbody>
</table>

Test Date: Dec-2018
Hardware Availability: Jul-2018
Software Availability: Jul-2018
## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>48</td>
<td>934</td>
<td>81.8</td>
<td>937</td>
<td>81.5</td>
<td>938</td>
<td>81.5</td>
<td>937</td>
<td>81.5</td>
<td>938</td>
<td>81.5</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>48</td>
<td>702</td>
<td>96.9</td>
<td>704</td>
<td>96.5</td>
<td>706</td>
<td>96.3</td>
<td>704</td>
<td>96.5</td>
<td>706</td>
<td>96.3</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>48</td>
<td>574</td>
<td>135</td>
<td>572</td>
<td>136</td>
<td>562</td>
<td>138</td>
<td>572</td>
<td>136</td>
<td>562</td>
<td>138</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>48</td>
<td>881</td>
<td>71.5</td>
<td>886</td>
<td>71.1</td>
<td>884</td>
<td>71.2</td>
<td>885</td>
<td>71.2</td>
<td>883</td>
<td>71.3</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>48</td>
<td>458</td>
<td>111</td>
<td>459</td>
<td>110</td>
<td>458</td>
<td>111</td>
<td>457</td>
<td>110</td>
<td>456</td>
<td>110</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>48</td>
<td>409</td>
<td>206</td>
<td>406</td>
<td>207</td>
<td>407</td>
<td>207</td>
<td>408</td>
<td>207</td>
<td>407</td>
<td>207</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>48</td>
<td>588</td>
<td>93.6</td>
<td>587</td>
<td>93.9</td>
<td>586</td>
<td>93.9</td>
<td>586</td>
<td>93.9</td>
<td>585</td>
<td>94.1</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>48</td>
<td>921</td>
<td>86.3</td>
<td>922</td>
<td>86.2</td>
<td>927</td>
<td>85.8</td>
<td>910</td>
<td>87.3</td>
<td>915</td>
<td>86.9</td>
</tr>
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<td>48</td>
<td>618</td>
<td>203</td>
<td>619</td>
<td>203</td>
<td>619</td>
<td>203</td>
<td>620</td>
<td>203</td>
<td>620</td>
<td>203</td>
</tr>
<tr>
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<td>48</td>
<td>641</td>
<td>80.8</td>
<td>641</td>
<td>80.8</td>
<td>642</td>
<td>80.8</td>
<td>642</td>
<td>80.8</td>
<td>642</td>
<td>80.8</td>
</tr>
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### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
The OS kernel (ver. 4.17.9) is a part of Inspur's Performance Boost Suite which could be provide to customers who have special requirement. It's maintained and deployed by Inspur field engineer. Please see http://en.inspur.com/en/2402530/2402532/2402644/2402655/2402659/2404072/index.html or inquire lcsd@inspur.com for further information.

### General Notes

Environment variables set by runcpu before the start of the run:  
LD_LIBRARY_PATH = "/home/CPU2017/lib/ia32:/home/CPU2017/lib/intel64:/home/CPU2017/je5.0.1-32:/home/CPU2017/je5.0.1-64"  
Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3 > /proc/sys/vm/drop_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>
Insapur Corporation  

Inspur NF5180M5 (Intel Xeon Silver 4116)  

**SPECrate2017_int_base = 109**  
**SPECrate2017_int_peak = 116**

---

**CPU2017 License:** 3358  
**Test Date:** Dec-2018

**Test Sponsor:** Inspur Corporation  
**Hardware Availability:** Jul-2018

**Tested by:** Inspur Corporation  
**Software Availability:** Jul-2018

---

**General Notes (Continued)**

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;  
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;  

---

**Platform Notes**

BIOS and OS configuration:  
SCALING_GOVERNOR set to Performance  
Hardware Prefetch set to Disable  
VT Support set to Disable  
C1E Support set to Disable  
IMC (Integrated memory controller) Interleaving set to 1-way  
Sub NUMA Cluster (SNC) set to Enable  
Sysinfo program /home/CPU2017/bin/sysinfo  
Rev: 15797 of 2017-06-14 96c45e4568ad54c135fd618b0c91c0f  
running on linux-vzir Sat Dec 29 03:04:40 2018

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo  
  model name : Intel(R) Xeon(R) Silver 4116 CPU @ 2.10GHz  
  2 "physical id"s (chips)  
  48 "processors"  
  cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
  cpu cores : 12  
  siblings : 24  
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13  
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13

From lscpu:  
  Architecture: x86_64  
  CPU op-mode(s): 32-bit, 64-bit

(Continued on next page)
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Platform Notes (Continued)

Byte Order: Little Endian
CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 2
Core(s) per socket: 12
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4116 CPU @ 2.10GHz
Stepping: 4
CPU MHz: 2400.124
CPU max MHz: 3000.0000
CPU min MHz: 800.0000
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 16896K
NUMA node0 CPU(s): 0-11,24-35
NUMA node1 CPU(s): 12-23,36-47

Flags:

fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
 lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
 aperfmpref pni pclmulqdq dtex64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
 pcid dca ss64_1 ss4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
 rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single pti
 intel_p8in mba tpr_shadow vanni flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle
 avx2 smep bmi2 ermi invpcid rtm cmp mpx rdt_a avx512f avx512d rdseed adx smad
 clflushopt clwb intel_pt avx512cd avx512bw avx512v l xsaveopt xsavexc xgetbv1 xsaves
 cqm_llc cqm_occup_llc cqm_mmb_total cqm_mmb_local dtherm ida arat pln pts hwp
 hwp_act_window hwp_epp hwp_pkg_req pku ospk

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 24 25 26 27 28 29 30 31 32 33 34 35
node 0 size: 385548 MB
node 0 free: 372023 MB
node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23 36 37 38 39 40 41 42 43 44 45 46 47
node 1 size: 386921 MB
node 1 free: 375411 MB
Inspur Corporation

Inspur NF5180M5 (Intel Xeon Silver 4116)

SPEC CPU2017 Integer Rate Result

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Platform Notes (Continued)

node distances:
node 0 1
 0: 10 21
 1: 21 10

From /proc/meminfo
MemTotal: 791009496 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2

From /etc/*release*/etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 2
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.

os-release:
  NAME="SLES"
  VERSION="12-SP2"
  VERSION_ID="12.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux linux-vzir 4.17.9-69-default #1 SMP Wed Jul 25 10:40:26 CST 2018 x86_64 x86_64
x86_64 GNU/Linux

run-level 3 Dec 28 15:53 last=5

SPEC is set to: /home/CPU2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 145G 104G 41G 73% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Inspur 4.0.4 07/19/2018
Memory:
  24x Samsung M393A4K40CB2-CTD 32 GB 2 rank 2666, configured at 2400

(Continued on next page)
Inspur Corporation
Inspur NF5180M5 (Intel Xeon Silver 4116)

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Software Availability: Jul-2018

Platform Notes (Continued)
(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
    525.x264_r(base, peak) 557.xz_r(base, peak)

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC  500.perlbench_r(peak) 502.gcc_r(peak)

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
    541.leela_r(base)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
    541.leela_r(peak)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC  548.exchange2_r(base, peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
SPEC CPU2017 Integer Rate Result

Inspur Corporation
Inspur NF5180M5 (Intel Xeon Silver 4116)

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Base Compiler Invocation

C benchmarks:
  icc

C++ benchmarks:
  icpc

Fortran benchmarks:
  ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
  -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
  -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
  -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
  -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
  -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
  -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
  -L/usr/local/je5.0.1-64/lib -ljemalloc
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### Inspur Corporation

**Inspur NF5180M5 (Intel Xeon Silver 4116)**

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</tr>
</tbody>
</table>

---

### Base Other Flags

**C benchmarks:**
- `-m64`  `-std=c11`

**C++ benchmarks:**
- `-m64`

**Fortran benchmarks:**
- `-m64`

---

### Peak Compiler Invocation

**C benchmarks:**
- `icc`

**C++ benchmarks:**
- `icpc`

**Fortran benchmarks:**
- `ifort`

---

### Peak Portability Flags

- `500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64`
- `502.gcc_r: -D_FILE_OFFSET_BITS=64`
- `505.mcf_r: -DSPEC_LP64`
- `520.omnetpp_r: -DSPEC_LP64`
- `523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX`
- `525.x264_r: -DSPEC_LP64`
- `531.deepsjeng_r: -DSPEC_LP64`
- `541.leela_r: -DSPEC_LP64`
- `548.exchange2_r: -DSPEC_LP64`
- `557.xz_r: -DSPEC_LP64`

---

### Peak Optimization Flags

**C benchmarks:**

- `500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib`

(Continued on next page)
Peak Optimization Flags (Continued)

500.perlbench_r (continued):
-lljemalloc

502.gcc_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-LL/usr/local/je5.0.1-32/lib -lljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -LL/usr/local/je5.0.1-64/lib
-lljemalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -fno-alias
-LL/usr/local/je5.0.1-64/lib -lljemalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-LL/usr/local/je5.0.1-64/lib -lljemalloc

523.xalancbmk_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-LL/usr/local/je5.0.1-32/lib -lljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-LL/usr/local/je5.0.1-64/lib -lljemalloc

Peak Other Flags

C benchmarks (except as noted below):
-m64 -std=c11

(Continued on next page)
### SPEC CPU2017 Integer Rate Result

**Inspur Corporation**

**Inspur NF5180M5 (Intel Xeon Silver 4116)**

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#### Peak Other Flags (Continued)

- 502.gcc_r: `-m32 -std=c11`
- C++ benchmarks (except as noted below):
  - `-m64`
- 523.xalancbmk_r: `-m32`
- Fortran benchmarks:
  - `-m64`

The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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