## Dell Inc. PowerEdge R240 (Intel Xeon E-2126G)

<table>
<thead>
<tr>
<th>Test Sponsor: Dell Inc.</th>
<th>Test Date: Jan-2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tested by: Dell Inc.</td>
<td>Hardware Availability: Dec-2018</td>
</tr>
<tr>
<td>SPECrate2017_fp_base = 37.3</td>
<td>Software Availability: Apr-2018</td>
</tr>
<tr>
<td>SPECrate2017_fp_peak = 36.2</td>
<td></td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>CPU Name: Intel Xeon E-2126G</th>
<th>OS: SUSE Linux Enterprise Server 12 SP3 4.4.126-94.22-default</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz.: 4500</td>
<td>Compiler: C/C++: Version 18.0.2.20180210 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.2.20180210 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td>Nominal: 3300</td>
<td>Parallel: No</td>
</tr>
<tr>
<td>Enabled: 6 cores, 1 chip</td>
<td>Firmware: Version 1.0.1 released Oct-2018</td>
</tr>
<tr>
<td>Orderable: 1 chips</td>
<td>File System: xfs</td>
</tr>
<tr>
<td>Cache L1: 32 KB I + 32 KB D on chip per core</td>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>L2: 256 KB I+D on chip per core</td>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>L3: 12 MB I+D on chip per chip</td>
<td>Peak Pointers: 64-bit</td>
</tr>
<tr>
<td>Other: None</td>
<td>Other: None</td>
</tr>
<tr>
<td>Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2666V-R)</td>
<td></td>
</tr>
<tr>
<td>Storage: 1 x 960 GB SATA SSD</td>
<td></td>
</tr>
<tr>
<td>Other: None</td>
<td></td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base (37.3)</th>
</tr>
</thead>
</table>

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<tr>
<th>SPECrate2017_fp_peak (36.2)</th>
</tr>
</thead>
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### Additional Information

- **Test Date**: Jan-2019
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- **Software Availability**: Apr-2018
SPEC CPU2017 Floating Point Rate Result

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Dell Inc.
PowerEdge R240 (Intel Xeon E-2126G)

SPECrate2017_fp_base = 37.3
SPECrate2017_fp_peak = 36.2

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>6</td>
<td>809</td>
<td>74.4</td>
<td>809</td>
<td>74.4</td>
<td>809</td>
<td>74.4</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>6</td>
<td>213</td>
<td>35.7</td>
<td>213</td>
<td>35.6</td>
<td>214</td>
<td>35.6</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>6</td>
<td>187</td>
<td>30.5</td>
<td>184</td>
<td>30.9</td>
<td>187</td>
<td>30.5</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>6</td>
<td>711</td>
<td>22.1</td>
<td>725</td>
<td>21.7</td>
<td>728</td>
<td>21.6</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>6</td>
<td>282</td>
<td>49.7</td>
<td>282</td>
<td>49.7</td>
<td>284</td>
<td>49.4</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>6</td>
<td>357</td>
<td>17.7</td>
<td>357</td>
<td>17.7</td>
<td>357</td>
<td>17.7</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>6</td>
<td>349</td>
<td>38.5</td>
<td>350</td>
<td>38.4</td>
<td>349</td>
<td>38.5</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>6</td>
<td>218</td>
<td>41.9</td>
<td>218</td>
<td>41.9</td>
<td>218</td>
<td>41.9</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>6</td>
<td>230</td>
<td>45.6</td>
<td>229</td>
<td>45.8</td>
<td>231</td>
<td>45.5</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>6</td>
<td>140</td>
<td>106</td>
<td>137</td>
<td>109</td>
<td>136</td>
<td>110</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>6</td>
<td>160</td>
<td>63.0</td>
<td>160</td>
<td>63.0</td>
<td>160</td>
<td>63.0</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>6</td>
<td>1030</td>
<td>22.7</td>
<td>1031</td>
<td>22.7</td>
<td>1031</td>
<td>22.7</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>6</td>
<td>617</td>
<td>15.5</td>
<td>611</td>
<td>15.6</td>
<td>612</td>
<td>15.6</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

General Notes
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

Dell Inc.
PowerEdge R240 (Intel Xeon E-2126G)

SPECrate2017_fp_base = 37.3
SPECrate2017_fp_peak = 36.2

CPU2017 License: 55
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General Notes (Continued)

Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Platform Notes

BIOS settings:
Virtualization Technology disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-gw0u Wed Jan 30 13:31:33 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) E-2126G CPU @ 3.30GHz
  1 "physical id"s (chips)
  6 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 6
  siblings : 6
  physical 0: cores 0 1 2 3 4 5

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 6
On-line CPU(s) list: 0-5
Thread(s) per core: 1
Core(s) per socket: 6
Socket(s): 1
NUMA node(s): 1

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result
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Platform Notes (Continued)

Vendor ID: GenuineIntel
CPU family: 6
Model: 158
Model name: Intel(R) Xeon(R) E-2126G CPU @ 3.30GHz
Stepping: 10
CPU MHz: 3421.320
CPU max MHz: 4500.0000
CPU min MHz: 800.0000
BogoMIPS: 6623.98
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 256K
L3 cache: 12288K
NUMA node0 CPU(s): 0-5
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
 pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
 lm constant_tsc art arch_perfmon pebs bts rep_good ntopology nonstop_tsc
 aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
 fma cx16 xtrr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
 xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
dtherm hwlp act_window hwlp epp intel_pt rsb ctsxsw spec_ctrl stibp retpoline
kaiser tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep
bmi2 erms invpcid rtm mpx rdseed adx smap clflushopt xsaveopt xsavec xgetbv1

/proc/cpuinfo cache data
cache size: 12288 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 1 nodes (0)
node 0 cpus: 0 1 2 3 4 5
node 0 size: 64276 MB
node 0 free: 63811 MB
node distances:
node 0
0: 10

From /proc/meminfo
MemTotal: 65819520 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP3

From /etc/*release* /etc/*version*

(Continued on next page)
## Dell Inc.

### PowerEdge R240 (Intel Xeon E-2126G)

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**Test Sponsor:** Dell Inc.  
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**Hardware Availability:** Dec-2018  
**Software Availability:** Apr-2018

### Platform Notes (Continued)

- **SuSE-release:**
  - SUSE Linux Enterprise Server 12 (x86_64)
  - VERSION = 12
  - PATCHLEVEL = 3
  - # This file is deprecated and will be removed in a future service pack or release.
  - # Please check /etc/os-release for details about this release.

- **os-release:**
  - NAME="SLES"
  - VERSION="12-SP3"
  - VERSION_ID="12.3"
  - PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
  - ID="sles"
  - ANSI_COLOR="0;32"
  - CPE_NAME="cpe:/o:suse:sles:12:sp3"

- **uname -a:**
  - Linux linux-gw0u 4.4.126-94.22-default #1 SMP Wed Apr 11 07:45:03 UTC 2018 (9649989)
  - x86_64 x86_64 x86_64 GNU/Linux

- **Kernel self-reported vulnerability status:**
  - CVE-2017-5754 (Meltdown): Mitigation: PTI
  - CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
  - CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

- **run-level 3 Jan 30 09:00 last=5**

- **SPEC is set to: /home/cpu2017**

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda2</td>
<td>xfs</td>
<td>301G</td>
<td>16G</td>
<td>285G</td>
<td>6%</td>
<td>/</td>
</tr>
</tbody>
</table>

- **Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMI BIOS" standard.**

  - **BIOS Dell Inc. 1.0.1 10/19/2018**
  - **Memory:**
    - 3x 00AD00000A02 HMA82GU7CJR8N-VK 16 GB 2 rank 2666
    - 1x 00AD00000A07 HMA82GU7CJR8N-VK 16 GB 2 rank 2666

- **(End of data from sysinfo program)**

### Compiler Version Notes

```
CC 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
```

(Continued on next page)
Compiler Version Notes (Continued)

icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CC  519.lbm_r(peak) 538.imagick_r(peak) 544.nab_r(peak)
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==============================================================================
CXXC 508.namd_r(base) 510.parest_r(base)

icpc (ICC) 18.0.2 20180210
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FC  507.cactuBSSN_r(base)
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Compiler Version Notes (Continued)

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icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.2 20180210
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(Continued on next page)
## SPEC CPU2017 Floating Point Rate Result

### Dell Inc. PowerEdge R240 (Intel Xeon E-2126G)

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</tr>
</thead>
<tbody>
<tr>
<td>36.2</td>
<td>37.3</td>
</tr>
</tbody>
</table>

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### Compiler Version Notes (Continued)

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icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

### Base Compiler Invocation

- **C benchmarks:**
  - `icc -m64 -std=c11`

- **C++ benchmarks:**
  - `icpc -m64`

- **Fortran benchmarks:**
  - `ifort -m64`

- **Benchmarks using both Fortran and C:**
  - `ifort -m64 icc -m64 -std=c11`

- **Benchmarks using both C and C++:**
  - `icpc -m64 icc -m64 -std=c11`

- **Benchmarks using Fortran, C, and C++:**
  - `icpc -m64 icc -m64 -std=c11 ifort -m64`

### Base Portability Flags

- 503.bwaves_r: -DSPEC_LP64  
- 507.cactuBSSN_r: -DSPEC_LP64  
- 508.namd_r: -DSPEC_LP64  
- 510.parest_r: -DSPEC_LP64  
- 511.povray_r: -DSPEC_LP64  
- 519.libm_r: -DSPEC_LP64  
- 521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian  
- 526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char  
- 527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG  
- 538.imagick_r: -DSPEC_LP64  
- 544.nab_r: -DSPEC_LP64  
- 549.fotonik3d_r: -DSPEC_LP64  
- 554.roms_r: -DSPEC_LP64
Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64
SPEC CPU2017 Floating Point Rate Result

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Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both C and C++:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
## SPEC CPU2017 Floating Point Rate Result

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<td>36.2</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55  
**Test Date:** Jan-2019  
**Test Sponsor:** Dell Inc.  
**Hardware Availability:** Dec-2018  
**Tested by:** Dell Inc.  
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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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