**SPEC® CPU2017 Floating Point Speed Result**

**Dell Inc.**

**PowerEdge T140 (Intel Xeon E-2124)**

<table>
<thead>
<tr>
<th><strong>CPU2017 License:</strong></th>
<th>55</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Test Sponsor:</strong></td>
<td>Dell Inc.</td>
</tr>
<tr>
<td><strong>Tested by:</strong></td>
<td>Dell Inc.</td>
</tr>
<tr>
<td><strong>Test Date:</strong></td>
<td>Feb-2019</td>
</tr>
<tr>
<td><strong>Hardware Availability:</strong></td>
<td>Dec-2018</td>
</tr>
<tr>
<td><strong>Software Availability:</strong></td>
<td>Apr-2018</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>4</td>
<td>23.7</td>
<td>22.4</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>CPU Usage</th>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>4</td>
<td>38.0</td>
<td>79.9</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>4</td>
<td>37.8</td>
<td>74.9</td>
</tr>
<tr>
<td>619.ibm_s</td>
<td>4</td>
<td>30.5</td>
<td>62.3</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>4</td>
<td>17.3</td>
<td>32.3</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>4</td>
<td>16.8</td>
<td>30.5</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>4</td>
<td>28.6</td>
<td>59.8</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>4</td>
<td>18.4</td>
<td>33.7</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>4</td>
<td>17.9</td>
<td>31.7</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>4</td>
<td>15.5</td>
<td>28.0</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>4</td>
<td>15.8</td>
<td>29.8</td>
</tr>
</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon E-2124
- **Max MHz:** 4300
- **Nominal:** 3300
- **Enabled:** 4 cores, 1 chip
- **Orderable:** 1 chip
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 256 KB I+D on chip per core
- **L3:** 8 MB I+D on chip per chip
- **Other:** None
- **Memory:** 64 GB (4 x 16 GB 2Rx8 PC4-2666V-R)
- **Storage:** 1 x 960 GB SATA SSD
- **Other:** None

**Software**

- **OS:** SUSE Linux Enterprise Server 12 SP3
  - 4.4.126-94.22-default
- **Compiler:** C/C++: Version 18.0.2.20180210 of Intel C/C++ Compiler for Linux;
  Fortran: Version 18.0.2.20180210 of Intel Fortran Compiler for Linux
- **Parallel:** Yes
- **Firmware:** Version 1.0.1 released Oct-2018
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** None
Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Base</th>
<th></th>
<th></th>
<th></th>
<th></th>
<th>Peak</th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Seconds</td>
<td>Ratio</td>
<td>Seconds</td>
<td>Ratio</td>
<td>Seconds</td>
<td>Ratio</td>
<td>Seconds</td>
<td>Ratio</td>
<td>Seconds</td>
<td>Ratio</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>603.bwaves_s</td>
<td>4</td>
<td>738</td>
<td>80.0</td>
<td>737</td>
<td>80.0</td>
<td>739</td>
<td>79.8</td>
<td>4</td>
<td>739</td>
<td>79.9</td>
<td>739</td>
<td>79.8</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>4</td>
<td>439</td>
<td>37.9</td>
<td>438</td>
<td>38.0</td>
<td>439</td>
<td>38.0</td>
<td>4</td>
<td>441</td>
<td>37.8</td>
<td>441</td>
<td>37.8</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>4</td>
<td>737</td>
<td>7.11</td>
<td>737</td>
<td>7.11</td>
<td>737</td>
<td>7.11</td>
<td>4</td>
<td>738</td>
<td>7.10</td>
<td>738</td>
<td>7.10</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>4</td>
<td>433</td>
<td>30.5</td>
<td>430</td>
<td>30.7</td>
<td>435</td>
<td>30.4</td>
<td>4</td>
<td>408</td>
<td>32.4</td>
<td>411</td>
<td>32.2</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>4</td>
<td>511</td>
<td>17.3</td>
<td>511</td>
<td>17.3</td>
<td>511</td>
<td>17.4</td>
<td>4</td>
<td>526</td>
<td>16.8</td>
<td>526</td>
<td>16.8</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>4</td>
<td>415</td>
<td>28.6</td>
<td>415</td>
<td>28.6</td>
<td>415</td>
<td>28.6</td>
<td>4</td>
<td>398</td>
<td>29.8</td>
<td>396</td>
<td>30.0</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>4</td>
<td>782</td>
<td>18.5</td>
<td>783</td>
<td>18.4</td>
<td>783</td>
<td>18.4</td>
<td>4</td>
<td>1446</td>
<td>9.98</td>
<td>1446</td>
<td>9.98</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>4</td>
<td>519</td>
<td>33.7</td>
<td>519</td>
<td>33.7</td>
<td>519</td>
<td>33.7</td>
<td>4</td>
<td>551</td>
<td>31.7</td>
<td>551</td>
<td>31.7</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>4</td>
<td>508</td>
<td>17.9</td>
<td>508</td>
<td>17.9</td>
<td>508</td>
<td>17.9</td>
<td>4</td>
<td>507</td>
<td>18.0</td>
<td>508</td>
<td>18.0</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>4</td>
<td>1016</td>
<td>15.5</td>
<td>1017</td>
<td>15.5</td>
<td>1018</td>
<td>15.5</td>
<td>4</td>
<td>996</td>
<td>15.8</td>
<td>995</td>
<td>15.8</td>
</tr>
</tbody>
</table>

SPECspeed2017_fp_base = 23.7
SPECspeed2017_fp_peak = 22.4

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,compact"
- LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
- OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3> /proc/sys/vm/drop_caches

Platform Notes

BIOS settings:
- Virtualization Technology disabled
- System Profile set to Custom

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

Dell Inc.
PowerEdge T140 (Intel Xeon E-2124)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>23.7</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_fp_peak</td>
<td>22.4</td>
</tr>
</tbody>
</table>

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

**Platform Notes (Continued)**

CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-gdas Fri Feb 22 19:09:25 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) E-2124 CPU @ 3.30GHz
  1 "physical id"s (chips)
  4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 4
physical 0: cores 0 1 2 3

From lscpu:
Architecture:       x86_64
CPU op-mode(s):     32-bit, 64-bit
Byte Order:         Little Endian
CPU(s):             4
On-line CPU(s) list: 0-3
Thread(s) per core: 1
Core(s) per socket: 4
Socket(s):          1
NUMA node(s):       1
Vendor ID:          GenuineIntel
CPU family:         6
Model:              158
Model name:         Intel(R) Xeon(R) E-2124 CPU @ 3.30GHz
Stepping:           10
CPU MHz:            4182.345
CPU max MHz:        4300.0000
CPU min MHz:        800.0000
BogoMIPS:           6623.96
Virtualization:     VT-x
L1d cache:          32K

(Continued on next page)
Dell Inc.  

PowerEdge T140 (Intel Xeon E-2124)

**SPEC CPU2017 Floating Point Speed Result**

---

**Copyright 2017-2019 Standard Performance Evaluation Corporation**

---

### SPECspeed2017_fp_base = 23.7

### SPECspeed2017_fp_peak = 22.4

---

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Feb-2019  
**Hardware Availability:** Dec-2018  
**Software Availability:** Apr-2018

---

**Platform Notes (Continued)**

- L1i cache: 32K
- L2 cache: 256K
- L3 cache: 8192K
- NUMA node0 CPU(s): 0-3

**Flags:**  
- fpu
- vme
- de
- pse
- tsc
- msr
- mce
- cx8
- apic
- sep
- mtrr
- pae
- cmov
- pat
- pse36
- clflush
- dts
- acpi
- mmx
- fxsr
- sse
- ss
- ht
- tm
- pbe
- syscall
- nx
- pdpe1gb
- rdtsscp
- lm
- constant_tsc
- arch
- perfmon
- pebs
- bts
- rep_good
- nopl
- xtopology
- nonstop_tsc
- aperf
- mpn
- eagerfpu
- pni
- pclmulqdq
- dtes64
- monitor
- ds_cpl
- vmx
- smx
- est
- tm2
- ssse3
- sdbg
- fma
- cx16
- xtrp
- pdcm
- pcid
- sse4_1
- sse4_2
- x2apic
- movbe
- popcnt
- tsc_deadline_timer
- aes
- xsave
- avx
- f16c
- rdrand
- lahf_lm
- abm
- 3dnowprefetch
- ida
- arat
- epb
- invpcid_single
- pln
- pts
- dtherm
- hw
- act_window
- hwp
- epp
- intel_pt
- rsb_cxsw
- spec_ctrl
- stibp
- retpoline
- kaiser
- tpr_shadow
- vmni
- flexpriority
- ept
- vpid
- fsgsbase
- tsc_adjust
- bm1
- hle
- avx2
- smep
- bmi2
- erms
- invpcid
- rtm
- mpx
- rdseed
- adx
- smap
- clflushopt
- xsaveopt
- xsavec
- xgetbv1

---

From `numactl --hardware`  
**WARNING:** a numactl 'node' might or might not correspond to a physical chip.  
- available: 1 nodes (0)  
- node 0 cpus: 0 1 2 3  
- node 0 size: 64278 MB  
- node 0 free: 55739 MB  
- node distances:  
  - node 0  
    - 0: 10

---

From `/proc/meminfo`  
- MemTotal: 65820840 kB  
- HugePages_Total: 0  
- Hugepagesize: 2048 kB

---

From `/usr/bin/lsb_release -d`  
**SUSE Linux Enterprise Server 12 SP3**

---

From `/etc/*release`  
**SuSE-release:**  
- SUSE Linux Enterprise Server 12 (x86_64)  
- VERSION = 12  
- PATCHLEVEL = 3  
- # This file is deprecated and will be removed in a future service pack or release.  
- # Please check /etc/os-release for details about this release.  
- os-release:  
  - NAME="SLES"  
  - VERSION="12-SP3"  
  - VERSION_ID="12.3"  
  - PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"

---

(Continued on next page)
Dell Inc.
PowerEdge T140 (Intel Xeon E-2124)

SPEC CPU2017 Floating Point Speed Result

SPECspeed2017_fp_base = 23.7
SPECspeed2017_fp_peak = 22.4

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Feb-2019
Hardware Availability: Dec-2018
Software Availability: Apr-2018

Platform Notes (Continued)

ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
Linux linux-gdas 4.4.126-94.22-default #1 SMP Wed Apr 11 07:45:03 UTC 2018 (9649989)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 3 Feb 22 14:00 last=5

SPEC is set to: /home/cpu2017
Filesystem  Type  Size  Used Avail Use% Mounted on
/dev/sda3  xfs   605G   22G  583G   4% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.0.1 10/19/2018
Memory:
  3x 00AD00000A02 HMA82GU7CJR8N-VK 16 GB 2 rank 2666
  1x 00AD00000A07 HMA82GU7CJR8N-VK 16 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC  619.lbm_s(peak) 638.imagick_s(peak) 644.nab_s(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

(Continued on next page)
Dell Inc.

PowerEdge T140 (Intel Xeon E-2124)

SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

SPECspeed2017_fp_base = 23.7
SPECspeed2017_fp_peak = 22.4

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Feb-2019
Hardware Availability: Dec-2018
Software Availability: Apr-2018

Compiler Version Notes (Continued)

==============================================================================
FC 607.cactuBSSN_s(base)
==============================================================================
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
FC 607.cactuBSSN_s(peak)
==============================================================================
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
==============================================================================
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
FC 603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)
==============================================================================
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CC 621.wrf_s(base) 627.cam4_s(base) 628.pop2_s(base)
==============================================================================
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
(Continued on next page)
spec

SPEC CPU2017 Floating Point Speed Result
Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge T140 (Intel Xeon E-2124) SPECspeed2017_fp_base = 23.7
SPECspeed2017_fp_peak = 22.4

<table>
<thead>
<tr>
<th>CPU2017 License: 55</th>
<th>Test Date: Feb-2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Dell Inc.</td>
<td>Hardware Availability: Dec-2018</td>
</tr>
<tr>
<td>Tested by: Dell Inc.</td>
<td>Software Availability: Apr-2018</td>
</tr>
</tbody>
</table>

Compiler Version Notes (Continued)

```
CC    621.wrf_s(peak) 627.cam4_s(peak) 628.pop2_s(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
```

Base Compiler Invocation

```
C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64
```

Base Portability Flags

```
603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
 -assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
```

Base Optimization Flags

```
C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
```

(Continued on next page)
Dell Inc.

PowerEdge T140 (Intel Xeon E-2124)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>23.7</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_fp_peak</td>
<td>22.4</td>
</tr>
</tbody>
</table>

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

<table>
<thead>
<tr>
<th>Test Date:</th>
<th>Feb-2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Dec-2018</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Apr-2018</td>
</tr>
</tbody>
</table>

**Base Optimization Flags (Continued)**

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs

**Peak Compiler Invocation**

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

**Peak Portability Flags**

Same as Base Portability Flags

**Peak Optimization Flags**

C benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

Dell Inc.
PowerEdge T140 (Intel Xeon E-2124)

| SPECspeed2017_fp_base | 23.7 |
| SPECspeed2017_fp_peak | 22.4 |

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Peak Optimization Flags (Continued)

C benchmarks (continued):
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -02 -xCORE-AVX2 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -02 -xCORE-AVX2 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -02 -xCORE-AVX2 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-02-22 20:09:25-0500.
Originally published on 2019-03-19.