**SPEC® CPU2017 Integer Speed Result**

**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)  
ProLiant DL580 Gen10  
(3.60 GHz, Intel Xeon Gold 6244)

<table>
<thead>
<tr>
<th>SPECspeak2017_int_base</th>
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CPU2017 License: 3  
Test Sponsor: HPE  
Tested by: HPE  
Test Date: Mar-2019  
Hardware Availability: Apr-2019  
Software Availability: Feb-2019

### Threads

<table>
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<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Speed</th>
</tr>
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<tbody>
<tr>
<td>600.perlbench_s</td>
<td>16</td>
<td>7.60</td>
</tr>
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<td>16</td>
<td>10.1</td>
</tr>
<tr>
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<td>13.6</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>16</td>
<td>8.31</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>16</td>
<td>13.6</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>16</td>
<td>15.3</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>16</td>
<td>6.05</td>
</tr>
<tr>
<td>641.leea_s</td>
<td>16</td>
<td>5.35</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>16</td>
<td>15.8</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>16</td>
<td>22.6</td>
</tr>
</tbody>
</table>

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### Hardware

- **CPU Name:** Intel Xeon Gold 6244  
- **Max MHz.:** 4400  
- **Nominal:** 3600  
- **Enabled:** 16 cores, 2 chips  
- **Orderable:** 1, 2, 3, 4 chip(s)  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 24.75 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)  
- **Storage:** 1 x 600 GB SATA SSD, RAID 0  
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 15 (x86_64)  
  
  Kernel 4.12.14-23-default  
- **Compiler:**  
  C/C++: Version 19.0.2.187 of Intel C/C++  
  Compiler Build 20190131 for Linux;  
  Fortran: Version 19.0.2.187 of Intel Fortran  
  Compiler Build 20190131 for Linux  
- **Parallel:** Yes  
- **Firmware:** HPE BIOS Version U34 02/02/2019 released Apr-2019  
- **File System:** btrfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** Not Applicable  
- **Other:** jemalloc memory allocator V5.0.1
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>600.perlbench_s</td>
<td>16</td>
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<tr>
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<tr>
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<td>625.x264_s</td>
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<td>631.deepsjeng_s</td>
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<td>237</td>
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<td>237</td>
<td>6.05</td>
<td>237</td>
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<tr>
<td>641.leela_s</td>
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<td>319</td>
<td>5.36</td>
<td>319</td>
<td>5.35</td>
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<tr>
<td>648.exchange2_s</td>
<td>16</td>
<td>186</td>
<td>15.8</td>
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<td>273</td>
<td>22.6</td>
<td>273</td>
<td>22.6</td>
<td>274</td>
</tr>
</tbody>
</table>

SPECspeed2017_int_base = 10.8
SPECspeed2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017_u2/lib/ia32:/home/cpu2017_u2/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017_u2/je5.0.1-32:/home/cpu2017_u2/je5.0.1-64"
OMP_STACKSIZE = "192M"
Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Platform Notes

BIOS Configuration:
- Hyper-Threading set to Disabled
- Thermal Configuration set to Maximum Cooling
- Memory Patrol Scrubbing set to Disabled
- LLC Prefetch set to Enabled
- LLC Dead Line Allocation set to Disabled
- Enhanced Processor Performance set to Enabled
- Workload Profile set to General Peak Frequency Compute
- Energy/Performance Bias set to Maximum Performance
- Workload Profile set to Custom
- Uncore Frequency Scaling set to Auto
- Numa Group Size Optimization set to Flat
- Advanced Memory Protection set to AdvancedECC

Sysinfo program /home/cpu2017_u2/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-lj2l Thu Mar 14 20:24:56 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) Gold 6244 CPU @ 3.60GHz
- 2 "physical id"s (chips)
- 16 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 8
  - siblings : 8
  - physical 0: cores 2 4 8 9 18 19 20 25
  - physical 1: cores 2 3 4 8 9 11 18 25

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 16
- On-line CPU(s) list: 0-15
- Thread(s) per core: 1
- Core(s) per socket: 8
- Socket(s): 2
- NUMA node(s): 2
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Gold 6244 CPU @ 3.60GHz
- Stepping: 7

(Continued on next page)
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**SPECspeed2017_int_base = 10.8**

**SPECspeed2017_int_peak = Not Run**

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### Platform Notes (Continued)

- **CPU MHz:** 3600.000
- **BogoMIPS:** 7200.00
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 1024K
- **L3 cache:** 25344K
- **NUMA node0 CPU(s):** 0-7
- **NUMA node1 CPU(s):** 8-15
- **Flags:** fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_pptin mba tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaves xsaveopt xsavec xsaves cgcc_llc cgcc_occup_llc cgcc_mbm_total cgcc_mbm_local ibpb ibrs stibp dtherm ida arat pin pts pku ospke avx512_vnni arch_capabilities ssbd

From /proc/cpuinfo cache data
- **cache size:** 25344 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
- **available:** 2 nodes (0-1)
- **node 0 cpus:** 0 1 2 3 4 5 6 7
- **node 0 size:** 193119 MB
- **node 0 free:** 192763 MB
- **node 1 cpus:** 8 9 10 11 12 13 14 15
- **node 1 size:** 193504 MB
- **node 1 free:** 193108 MB
- **node distances:**
  - **node 0 1**
  - **0:** 10 21
  - **1:** 21 10

From /proc/meminfo
- **MemTotal:** 395903464 KB
- **HugePages_Total:** 0
- **Hugepagesize:** 2048 KB

From /etc/*release* /etc/*version* os-release:
- **NAME="SLES"
  VERSION="15"

(Continued on next page)
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Platform Notes (Continued)

VERSION_ID="15"
PRETTY_NAME="SUSE Linux Enterprise Server 15"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME=cpe:/o:suse:sles:15"

uname -a:
Linux linux-lj2l 4.12.14-23-default #1 SMP Tue May 29 21:04:44 UTC 2018 (cd0437b)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 Mar 14 20:22

SPEC is set to: /home/cpu2017_u2
  Filesystem Type  Size  Used Avail Use% Mounted on
  /dev/sdb3  xfs  141G  95G  47G  67% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U34 02/02/2019
Memory:
  36x UNKNOWN NOT AVAILABLE
  12x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base)
  657.xz_s(base)
==============================================================================

(Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.2.187 Build 20190117
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(Continued on next page)
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Compiler Version Notes (Continued)

CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
641.leela_s(base)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

FC 648.exchange2_s(base)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
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## Base Optimization Flags

**C benchmarks:**  
-`-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
-`-qopt-mem-layout-trans=4 -gopenmp -DSPEC_OPENMP`
-`-L/home/cpu2017_u2/je5.0.1-64/ -ljemalloc`

**C++ benchmarks:**  
-`-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
-`-qopt-mem-layout-trans=4`
-`-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64 -lqkmalloc`

**Fortran benchmarks:**  
-`-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4`
-`-nostandard-realloc-lhs`

The flags files that were used to format this result can be browsed at:
- [http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revA.html](http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revA.html)

You can also download the XML flags sources by saving the following links:
- [http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revA.xml](http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revA.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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