## Dell Inc.

### PowerEdge T340 (Intel Pentium Gold G5500)

<table>
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<tr>
<th>Spec Speed2017_fp_base</th>
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<td>Spec Speed2017_fp_peak</td>
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**CPU2017 License:** 55  
**Test Date:** Mar-2019  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Hardware Availability:** Dec-2018  
**Software Availability:** Oct-2018

### Threads

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<td></td>
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<td>11.1</td>
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</table>

### Hardware

- **CPU Name:** Intel Pentium Gold G5500  
- **Max MHz.:** 3800  
- **Nominal:** 3800  
- **Enabled:** 2 cores, 1 chip, 2 threads/core  
- **Orderable:** 1 chip  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 256 KB I+D on chip per core  
- **L3:** 4 MB I+D on chip per core  
- **Other:** None  
- **Memory:** 64 GB (4 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)  
- **Storage:** 1 x 960 GB SATA SSD  
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 12 SP3  
  - **kernel:** 4.4.126-94.22-default  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++  
  - **Compiler for Linux:** Fortran: Version 18.0.0.128 of Intel Fortran  
- **Parallel:** Yes  
- **Firmware:** Version 1.0.1 released Oct-2018  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** None
Dell Inc.
PowerEdge T340 (Intel Pentium Gold G5500)

SPECspeed2017_fp_base = 14.2
SPECspeed2017_fp_peak = 12.9

Results Table

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</table>

SPECspeed2017_fp_base = 14.2
SPECspeed2017_fp_peak = 12.9

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

Platform Notes

BIOS settings:
Virtualization Technology disabled
System Profile set to Custom

(Continued on next page)
Dell Inc.  
PowerEdge T340 (Intel Pentium Gold G5500)  

| SPECspeed2017_fp_base = 14.2 |
| SPECspeed2017_fp_peak = 12.9 |

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.  

**Platform Notes (Continued)**

- CPU Performance set to Maximum Performance
- C States set to Autonomous
- C1E disabled
- Uncore Frequency set to Dynamic
- Energy Efficiency Policy set to Performance
- Memory Patrol Scrub disabled
- CPU Interconnect Bus Link Power Management disabled
- PCI ASPM L1 Link Power Management disabled
- Sysinfo program /home/cpu2017/bin/sysinfo
- Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
- running on linux-icjc Tue Mar 26 15:47:19 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

- From /proc/cpuinfo
  - model name : Intel(R) Pentium(R) Gold G5500 CPU @ 3.80GHz
  - 1 "physical id"s (chips)
  - 4 "processors"
  - cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    - cpu cores : 2
    - siblings : 4
    - physical 0: cores 0 1

- From lscpu:
  - Architecture: x86_64
  - CPU op-mode(s): 32-bit, 64-bit
  - Byte Order: Little Endian
  - CPU(s): 4
  - On-line CPU(s) list: 0-3
  - Thread(s) per core: 2
  - Core(s) per socket: 2
  - Socket(s): 1
  - NUMA node(s): 1
  - Vendor ID: GenuineIntel
  - CPU family: 6
  - Model: 158
  - Model name: Intel(R) Pentium(R) Gold G5500 CPU @ 3.80GHz
  - Stepping: 11
  - CPU MHz: 3800.031
  - CPU max MHz: 3800.0000
  - CPU min MHz: 800.0000
  - BogoMIPS: 7583.80
  - Virtualization: VT-x
  - L1d cache: 32K

(Continued on next page)
Dell Inc.  
PowerEdge T340 (Intel Pentium Gold G5500)  

SPEC CPU2017 Floating Point Speed Result  

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SPECspeed2017_fp_peak = 12.9  
SPECspeed2017_fp_base = 14.2

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.  
Test Date: Mar-2019  
Hardware Availability: Dec-2018  
Software Availability: Oct-2018

Platform Notes (Continued)

L1i cache: 32K  
L2 cache: 256K  
L3 cache: 4096K  
NUMA node0 CPU(s): 0-3  
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfusion eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 sdbg cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave xepp intel_pt rsb_cts cspec_ctrl stibp retpoline kaiser tpr_shadow vmx flexpriority vmpid fsgsb base tsc_adjust smep erms invpcid mpx rdseed smap clflushopt xsaveopt xsave c xgetbv1

/proc/cpuinfo cache data  
cache size: 4096 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.  
available: 1 nodes (0)  
node 0 cpus: 0 1 2 3  
node 0 size: 64278 MB  
node 0 free: 55739 MB  
node distances:  
node 0 0: 10

From /proc/meminfo  
MemTotal: 65820840 kB  
HugePages_Total: 0  
Hugepagesize: 2048 kB

From /usr/bin/lsb_release -d  
SUSE Linux Enterprise Server 12 SP3

From /etc/*release* /etc/*version*  
SuSE-release:  
SUSE Linux Enterprise Server 12 (x86_64)  
VERSION = 12  
PATCHLEVEL = 3  
# This file is deprecated and will be removed in a future service pack or release.  
# Please check /etc/os-release for details about this release.  
os-release:  
NAME="SLES"  
VERSION="12-SP3"  
VERSION_ID="12.3"  
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"

(Continued on next page)
## SPEC CPU2017 Floating Point Speed Result

**Dell Inc.**

**PowerEdge T340 (Intel Pentium Gold G5500)**

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### SPECspeed2017_fp_base = 14.2

### SPECspeed2017_fp_peak = 12.9

**Platform Notes (Continued)**

```plaintext
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
  Linux linux-icjc 4.4.126-94.22-default #1 SMP Wed Apr 11 07:45:03 UTC 2018 (9649989)
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

- CVE-2017-5754 (Meltdown): Mitigation: PTI
- CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
- CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 3 Mar 26 09:18 last=5

SPEC is set to: /home/cpu2017

Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda2      xfs   301G   22G  279G   8% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS Dell Inc. 1.0.1 10/19/2018
  Memory:
    3x 00AD00000A02 HMA82GU7CJR8N-VK 16 GB 2 rank 2666, configured at 2400
    1x 00AD00000A06 HMA82GU7CJR8N-VK 16 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)
```

### Compiler Version Notes

```
Compiler Version Notes

==============================================================================
CC   619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC   619.lbm_s(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

(Continued on next page)
```
Dell Inc.          SPECspeed2017_fp_base = 14.2
PowerEdge T340 (Intel Pentium Gold G5500)       SPECspeed2017_fp_peak = 12.9

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.
Test Date:  Mar-2019
Hardware Availability: Dec-2018
Software Availability: Oct-2018

Compiler Version Notes (Continued)

==============================================================================
FC  607.cactuBSSN_s(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
FC   607.cactuBSSN_s(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
FC   603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
FC   603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
CC  621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
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### Compiler Version Notes (Continued)

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### Base Compiler Invocation

- **C benchmarks:**
  - icc

- **Fortran benchmarks:**
  - ifort

- **Benchmarks using both Fortran and C:**
  - ifort icc

- **Benchmarks using Fortran, C, and C++:**
  - icpc icc ifort

### Base Portability Flags

- 603.bwaves.s: -DSPEC_LP64
- 607.cactuBSSN.s: -DSPEC_LP64
- 619.lbm.s: -DSPEC_LP64
- 621.wrf.s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl
- 627.cam4.s: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 628.pop2.s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl
- 638.imagick.s: -DSPEC_LP64
- 644.nab.s: -DSPEC_LP64
- 649.fotonik3d.s: -DSPEC_LP64
- 654.roms.s: -DSPEC_LP64

### Base Optimization Flags

- **C benchmarks:**
  - -xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
  - -qopt-mem-layout=trans=3 -qopenmp -DSPEC_OPENMP

(Continued on next page)
### Base Optimization Flags (Continued)

Fortran benchmarks:
- `-DSPEC_OPENMP`  
- `-xSSE4.2`  
- `-ipo -O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-qopenmp`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`

Benchmarks using both Fortran and C:
- `-xSSE4.2`  
- `-ipo -O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-qopenmp -DSPEC_OPENMP`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`

Benchmarks using Fortran, C, and C++:
- `-xSSE4.2`  
- `-ipo -O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-qopenmp -DSPEC_OPENMP`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`

### Base Other Flags

C benchmarks:
- `-m64 -std=c11`

Fortran benchmarks:
- `-m64`

Benchmarks using both Fortran and C:
- `-m64 -std=c11`

Benchmarks using Fortran, C, and C++:
- `-m64 -std=c11`

### Peak Compiler Invocation

C benchmarks:
- `icc`

Fortran benchmarks:
- `ifort`

Benchmarks using both Fortran and C:
- `ifort icc`
Peak Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xSSE4.2
-qopt-prefetch -ipo -O3 -no-prec-div -ffinite-math-only
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP

638.imagick_s: -xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s

Fortran benchmarks:

-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xSSE4.2 -qopt-prefetch -ipo -O3 -no-prec-div
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xSSE4.2
-qopt-prefetch -ipo -O3 -no-prec-div -ffinite-math-only
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

627.cam4_s: -xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

628.pop2_s: Same as 621.wrf_s

(Continued on next page)
## SPEC CPU2017 Floating Point Speed Result

### Dell Inc.

**PowerEdge T340 (Intel Pentium Gold G5500)**

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### CPU2017 License: 55

**Test Sponsor:** Dell Inc.  
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**Test Date:** Mar-2019  
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**Software Availability:** Oct-2018

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### Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
- `-prof-gen(pass 1)`  
- `-prof-use(pass 2)`  
- `-O2`  
- `-xSSE4.2`  
- `-qopt-prefetch`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-DSPEC_SUPPRESS_OPENMP`  
- `-qopenmp`  
- `-DSPEC_OPENMP`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`

### Peak Other Flags

**C benchmarks:**
- `-m64`  
- `-std=c11`

**Fortran benchmarks:**
- `-m64`

**Benchmarks using both Fortran and C:**
- `-m64`  
- `-std=c11`

**Benchmarks using Fortran, C, and C++:**
- `-m64`  
- `-std=c11`

---

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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