## SPEC® CPU2017 Integer Speed Result

### Supermicro
SuperServer 6029U-TR4 (X11DPU, Intel Xeon Gold 6138T)

<table>
<thead>
<tr>
<th>threads</th>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>40</td>
<td>9.52</td>
<td>9.75</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Test Date:** Mar-2019  
**Tested by:** Supermicro  
**Hardware Availability:** Jul-2017  
**Software Availability:** Nov-2018

### Hardware
- **CPU Name:** Intel Xeon Gold 6138T  
- **Max MHz.:** 3700  
- **Nominal:** 2000  
- **Enabled:** 40 cores, 2 chips  
- **Orderable:** 1,2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 27.5 MB I+D on chip per chip  
- **Memory:** 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R)  
- **Storage:** 1 x 200 GB SATA III SSD  
- **Other:** None

### Software
- **OS:** Red Hat Enterprise Linux Server 7.6 (Maipo)  
- **Kernel:** 3.10.0-957.el7.x86_64  
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++  
- **Compiler for Linux:** Fortran: Version 19.0.1.144 of Intel Fortran  
- **Firmware:** Version 3.0b released Mar-2019  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator V5.0.1
## SPEC CPU2017 Integer Speed Result

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SuperServer 6029U-TR4 (X11DPU, Intel Xeon Gold 6138T)

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>40</td>
<td>276</td>
<td>6.43</td>
<td>273</td>
<td>6.50</td>
<td>275</td>
<td>6.47</td>
<td>233</td>
<td>7.63</td>
<td>232</td>
<td>7.66</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>40</td>
<td>390</td>
<td>12.1</td>
<td>389</td>
<td>12.1</td>
<td>392</td>
<td>12.0</td>
<td>388</td>
<td>12.2</td>
<td>390</td>
<td>12.1</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>40</td>
<td>220</td>
<td>7.42</td>
<td>211</td>
<td>7.73</td>
<td>221</td>
<td>7.37</td>
<td>215</td>
<td>7.58</td>
<td>226</td>
<td>7.23</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>40</td>
<td>119</td>
<td>11.9</td>
<td>119</td>
<td>11.9</td>
<td>119</td>
<td>11.9</td>
<td>119</td>
<td>11.9</td>
<td>119</td>
<td>11.9</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>40</td>
<td>128</td>
<td>13.8</td>
<td>128</td>
<td>13.8</td>
<td>128</td>
<td>13.8</td>
<td>128</td>
<td>13.8</td>
<td>128</td>
<td>13.8</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>40</td>
<td>276</td>
<td>5.19</td>
<td>276</td>
<td>5.19</td>
<td>276</td>
<td>5.19</td>
<td>276</td>
<td>5.19</td>
<td>276</td>
<td>5.19</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>40</td>
<td>377</td>
<td>4.53</td>
<td>377</td>
<td>4.53</td>
<td>377</td>
<td>4.53</td>
<td>377</td>
<td>4.53</td>
<td>377</td>
<td>4.53</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>40</td>
<td>222</td>
<td>13.3</td>
<td>221</td>
<td>13.3</td>
<td>221</td>
<td>13.3</td>
<td>221</td>
<td>13.3</td>
<td>222</td>
<td>13.3</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>40</td>
<td>283</td>
<td>21.8</td>
<td>281</td>
<td>22.0</td>
<td>281</td>
<td>22.0</td>
<td>280</td>
<td>22.1</td>
<td>278</td>
<td>22.2</td>
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**SPECspeed2017_int_base = 9.52**  
**SPECspeed2017_int_peak = 9.75**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,scatter"
- LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
- OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

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Tested by: Supermicro

Test Date: Mar-2019
Hardware Availability: Jul-2017
Software Availability: Nov-2018

Platform Notes

BIOS Settings:
Hyper-Threading = Disable
LLC prefetch = Disable
Power Technology = Custom
Power Performance Tuning = BIOS Controls EPB
ENERGY_PERF_BIAS_CFG mode = Performance
Hardware P-state = Out of Band Mode
XPT Prefetch = Disable
Stale AtoS = Disable
LLC dead line alloc = Enable
SDDC Plus One = Disable
ADDDC Sparing = Disable
Patrol Scrub = Disable
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on CPU2017-01 Fri Mar 22 09:53:59 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6138T CPU @ 2.00GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 20
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 40
On-line CPU(s) list: 0-39
Thread(s) per core: 1
Core(s) per socket: 20
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6138T CPU @ 2.00GHz
Stepping: 4

(Continued on next page)
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Platform Notes (Continued)

CPU MHz: 2000.000
BogoMIPS: 4000.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 28160K
NUMA node0 CPU(s): 0-19
NUMA node1 CPU(s): 20-39
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmon perf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtopr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_13 cdp_13 intel_pstate
intel_pt ssbd mba ibrs ibpb stibp cpr_shadow vmpid flexpriority ept vpid fsgsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erva isvcmp ida rdmsk pmr cx16 xtpr pdcm pcid
rcode adx smap clflushopt clwb avx512f avx512dq avx512bw avx512vl xsaveopt xsavec
xgetbv1 cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local dtherm ida arat pln pts hwp_epp
pku ospke spec_ctrl intel_stibp flush_lid

/proc/cpuinfo cache data
  cache size: 28160 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19
  node 0 size: 391838 MB
  node 0 free: 382444 MB
  node 1 cpus: 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39
  node 1 size: 393216 MB
  node 1 free: 384343 MB
  node distances:
  node 0 1
  0: 10 21
  1: 21 10

From /proc/meminfo
  MemTotal: 791182148 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.6 (Maipo)"

(Continued on next page)
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Platform Notes (Continued)

ID="rhel"
ID_LIKE="fedora"
VARIANT="Server"
VARIANT_ID="server"
VERSION_ID="7.6"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.6 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)

uname -a:
Linux CPU2017-01 3.10.0-957.el7.x86_64 #1 SMP Thu Oct 4 20:48:51 UTC 2018 x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS (kernel)

run-level 3 Mar 22 09:49

SPEC is set to: /home/cpu2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 185G 22G 163G 12% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 3.0b 03/04/2019
Memory:
 12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666
 12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base, peak) 657.xz_s(base)
==============================================================================

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.1.144 Build 20181018
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(Continued on next page)
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<th>001176</th>
</tr>
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<td>Test Sponsor</td>
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</tr>
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</tr>
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</tr>
</tbody>
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**Compiler Version Notes (Continued)**

```
CC  600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 657.xz_s(peak)
```

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985–2018 Intel Corporation. All rights reserved.

```
CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
```

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985–2018 Intel Corporation. All rights reserved.

```
CXXC 620.omnetpp_s(peak)
```

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985–2018 Intel Corporation. All rights reserved.

```
FC  648.exchange2_s(base, peak)
```

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985–2018 Intel Corporation. All rights reserved.

**Base Compiler Invocation**

- **C benchmarks:**
  - icc -m64 -std=c11

- **C++ benchmarks:**
  - icpc -m64

- **Fortran benchmarks:**
  - ifort -m64
SPEC CPU2017 Integer Speed Result

Supermicro SuperServer 6029U-TR4 (X11DPU, Intel Xeon Gold 6138T)

SPECspeed2017_int_base = 9.52
SPECspeed2017_int_peak = 9.75

<table>
<thead>
<tr>
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<th>Value</th>
</tr>
</thead>
<tbody>
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<td>001176</td>
</tr>
<tr>
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<td>Supermicro</td>
</tr>
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</tr>
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<td>Mar-2019</td>
</tr>
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</tr>
</tbody>
</table>

Base Portability Flags

- 600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
- 602.gcc_s: -DSPEC_LP64
- 605.mcf_s: -DSPEC_LP64
- 620.omnetpp_s: -DSPEC_LP64
- 623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
- 625.x264_s: -DSPEC_LP64
- 631.deepsjeng_s: -DSPEC_LP64
- 641.leela_s: -DSPEC_LP64
- 648.exchange2_s: -DSPEC_LP64
- 657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
- -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4
- -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
- -lqkmalloc

Fortran benchmarks:
- -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
- -nostandard-realloc-lhs

Peak Compiler Invocation

C benchmarks:
- icc -m64 -std=c11

C++ benchmarks:
- icpc -m64

Fortran benchmarks:
- ifort -m64
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Software Availability: Nov-2018

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: basepeak = yes

657.xz_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

C++ benchmarks:
620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

623.xalancbmk_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

631.deepsjeng_s: Same as 623.xalancbmk_s

(Continued on next page)
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Peak Optimization Flags (Continued)

641.leela_s: Same as 623.xalancbmk_s

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SKL-revD.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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