# SPEC® CPU2017 Floating Point Rate Result

## Dell Inc.

**PowerEdge R640 (Intel Xeon Gold 5217, 3.00GHz)**

| Test Date: | Mar-2019 |
| Test Sponsor: | Dell Inc. |
| Hardware Availability: | Apr-2019 |
| Software Availability: | Jan-2019 |

### CPU2017 License:
- SPECrate2017_fp_base = 114
- SPECrate2017_fp_peak = 117

### Hardware

<table>
<thead>
<tr>
<th>Task</th>
<th>Copies</th>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>32</td>
<td>85.7</td>
<td>85.6</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>32</td>
<td>78.9</td>
<td>79.9</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>32</td>
<td>64.4</td>
<td>64.2</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>32</td>
<td>126</td>
<td>149</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>32</td>
<td>72.0</td>
<td>73.2</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>32</td>
<td>134</td>
<td>134</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>32</td>
<td>109</td>
<td>112</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>32</td>
<td>118</td>
<td>241</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>32</td>
<td>174</td>
<td>176</td>
</tr>
<tr>
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<td>110</td>
<td>241</td>
</tr>
<tr>
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<td>32</td>
<td>56.2</td>
<td>56.2</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>32</td>
<td>57.8</td>
<td>57.8</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>32</td>
<td>320</td>
<td>329</td>
</tr>
</tbody>
</table>

### Software

- **OS:** Ubuntu 18.04.2 LTS
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++ Compiler Build 20181018 for Linux;
  Fortran: Version 19.0.1.144 of Intel Fortran Compiler Build 20181018 for Linux
- **Parallel:** No
- **Firmware:** Version 2.1.6 released Mar-2019
- **File System:** ext4
- **System State:** Run level 5 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** None
- **Memory:** 192 GB (12 x 16 GB 2Rx8 PC4-2933Y-R, running at 2666)
- **Storage:** 1 x 480 GB SATA SSD
- **Other:** None
## Dell Inc.

PowerEdge R640 (Intel Xeon Gold 5217, 3.00GHz)

**SPECrate2017_fp_base = 114**  
**SPECrate2017_fp_peak = 117**

### CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>32</td>
<td>971</td>
<td>330</td>
<td>972</td>
<td>330</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>32</td>
<td>473</td>
<td>85.7</td>
<td>472</td>
<td>85.7</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>32</td>
<td>385</td>
<td>78.9</td>
<td>383</td>
<td>79.3</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>510.parest_r</td>
<td>32</td>
<td>1288</td>
<td>65.0</td>
<td>1299</td>
<td>64.4</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>32</td>
<td>592</td>
<td>126</td>
<td>593</td>
<td>126</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
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<td></td>
<td></td>
<td></td>
<td></td>
</tr>
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<td>537</td>
<td>134</td>
<td>534</td>
<td>134</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
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<td>32</td>
<td>446</td>
<td>109</td>
<td>446</td>
<td>109</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
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<td>32</td>
<td>498</td>
<td>112</td>
<td>494</td>
<td>113</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>531.imagick_r</td>
<td>32</td>
<td>331</td>
<td>241</td>
<td>329</td>
<td>242</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>32</td>
<td>305</td>
<td>177</td>
<td>309</td>
<td>174</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>32</td>
<td>1122</td>
<td>111</td>
<td>1133</td>
<td>110</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>32</td>
<td>905</td>
<td>56.2</td>
<td>905</td>
<td>56.2</td>
<td></td>
<td></td>
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</tbody>
</table>

**SPECrate2017_fp_base = 114**  
**SPECrate2017_fp_peak = 117**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

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CPU2017 License: 55
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General Notes (Continued)

Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Platform Notes

BIOS settings:
ADDDC setting disabled
Sub NUMA Cluster enabled
Virtualization Technology disabled
DCU Streamer Prefetcher enabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Logical Processor enabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on intel-sut Wed Mar 13 21:47:20 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5217 CPU @ 3.00GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32

(Continued on next page)
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Test Date: Mar-2019
Hardware Availability: Apr-2019
Software Availability: Jan-2019

Platform Notes (Continued)

On-line CPU(s) list: 0-31
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5217 CPU @ 3.00GHz
Stepping: 6
CPU MHz: 3398.246
BogoMIPS: 6000.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 11264K
NUMA node0 CPU(s): 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30
NUMA node1 CPU(s): 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref perfctr ndx pdcpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrm pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand
lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cdp_13 invpcid_single ssbd mba ibrs
ibpb stibp ibrs enhanced tpr_shadow vmmi flexpriority ept vpid fsgsbase tsc_adjust
bmi1 hle avx2 smep bmi2 64 ems invpd rtm cqm mpx rdt_a avx512f avx512dq rdseed adx
smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsaves 
ckmm cqm_llc cqm_occult_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts pku
ospke avx512_vnni flush_lld arch_capabilities

/pro/cpupinfo cache data
  cache size: 11264 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30
  node 0 size: 95147 MB
  node 0 free: 94278 MB
  node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31
  node 1 size: 96764 MB
  node 1 free: 95993 MB
  node distances:
    node 0 1
    0: 10 21
    1: 21 10

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Platform Notes (Continued)

From /proc/meminfo
   MemTotal:       196517788 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Ubuntu 18.04.2 LTS

From /etc/*release* /etc/*version*
   debian_version: buster/sid
   os-release:
      NAME="Ubuntu"
      VERSION="18.04.2 LTS (Bionic Beaver)"
      ID=ubuntu
      ID_LIKE=debian
      PRETTY_NAME="Ubuntu 18.04.2 LTS"
      VERSION_ID="18.04"
      HOME_URL="https://www.ubuntu.com/
      SUPPORT_URL="https://help.ubuntu.com/"

uname -a:
   Linux intel-sut 4.15.0-45-generic #48-Ubuntu SMP Tue Jan 29 16:28:13 UTC 2019 x86_64
   x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB

run-level 5 Mar 13 16:06

SPEC is set to: /home/cpu2017
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda2      ext4  439G   27G  390G   7% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   BIOS Dell Inc. 2.1.6 03/03/2019
   Memory:
      10x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666
      2x 00AD063200AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666
      12x Not Specified Not Specified

(Continued on next page)
**Dell Inc.**

**PowerEdge R640 (Intel Xeon Gold 5217, 3.00GHz)**

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**SPEC CPU2017 Floating Point Rate Result**

| SPECrate2017_fp_base = 114 | SPECrate2017_fp_peak = 117 |

(End of data from sysinfo program)

**Platform Notes (Continued)**

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**Compiler Version Notes**

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**CC 519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base, peak)**

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

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**CC 519.lbm_r(peak)**

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

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**CXXC 508.namd_r(base) 510.parest_r(base, peak)**

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

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**CXXC 508.namd_r(peak)**

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

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**CC 511.povray_r(base) 526.blender_r(base, peak)**

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
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Compiler Version Notes (Continued)

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Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
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Compiler Version Notes (Continued)

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel (R) C Intel (R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64

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Base Portability Flags (Continued)

511.povray_r: -DSPEC_LP64
519.ibm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte
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Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
519.ibm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

544.nab_r: Same as 538.imagick_r

C++ benchmarks:
508.namd_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

(Continued on next page)
### Dell Inc.

**PowerEdge R640 (Intel Xeon Gold 5217, 3.00GHz)**

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>55</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Mar-2019</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Apr-2019</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Jan-2019</td>
</tr>
</tbody>
</table>

#### SPEC CPU2017 Floating Point Rate Result

**SPECrate2017_fp_base** = 114  
**SPECrate2017_fp_peak** = 117

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**Peak Optimization Flags (Continued)**

510.parest_r: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4`

Fortran benchmarks:

503.bwaves_r: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs -align array32byte`

549.fotonik3d_r: `Same as 503.bwaves_r`

554.roms_r: `-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs -align array32byte`

Benchmarks using both Fortran and C:

- `-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs -align array32byte`

Benchmarks using both C and C++:

511.povray_r: `-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4`

526.blender_r: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4`

Benchmarks using Fortran, C, and C++:

- `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs -align array32byte`

---

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

**SPEC CPU2017 Floating Point Rate Result**

Dell Inc.  
PowerEdge R640 (Intel Xeon Gold 5217, 3.00GHz)  

| SPECrate2017_fp_peak | 117 |
| SPECrate2017_fp_base | 114 |

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  

| Test Date | Mar-2019 |
| Hardware Availability | Apr-2019 |
| Software Availability | Jan-2019 |

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Tested with SPEC CPU2017 v1.0.5 on 2019-03-13 17:47:20-0400.  