### Lenovo Global Technology

**ThinkSystem SR650 (3.60 GHz, Intel Xeon Gold 6244)**

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
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<tbody>
<tr>
<td>SPECrate2017_int_peak</td>
<td>Not Run</td>
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</table>

#### CPU2017 License:
- Lenovo Global Technology

#### Hardware

- **CPU Name:** Intel Xeon Gold 6244
- **Max MHz.:** 4400
- **Nominal:** 3600
- **Enabled:** 16 cores, 2 chips, 2 threads/core
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 24.75 MB I+D on chip per chip
- **Other:** None
- **Memory:** 384 GB (24 x 16 GB 2Rx4 PC4-2933Y-R)
- **Storage:** 1 x 800 GB SATA SSD
- **Other:** None

#### Software

- **OS:** Red Hat Enterprise Linux Server release 7.6 (Maipo)
- **Kernel:** 3.10.0-957.el7.x86_64
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++ Compiler Build 20181018 for Linux;
  Fortran: Version 19.0.1.144 of Intel Fortran Compiler Build 20181018 for Linux
- **Parallel:** No
- **Firmware:** Lenovo BIOS Version IVE135R 2.10 released Feb-2019
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** Not Applicable
- **Other:** None

#### SPECrate2017_int_base (134)

<table>
<thead>
<tr>
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**Test Sponsor:** Lenovo Global Technology

**Hardware Availability:** Apr-2019

**Software Availability:** Nov-2018

**Test Date:** Apr-2019
## Results Table

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## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0u1/lib/intel164"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

(Continued on next page)
**General Notes (Continued)**

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

---

**Platform Notes**

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Choose Operating Mode set to Custom Mode
C-states set to Legacy
SNC set to Enable
Trusted Execution Technology set to Enable
Stale AtoS set to Enable
LLC dead line alloc set to Disable
Sysinfo program /home/cpu2017-1.0.5-ic19.0u1/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on localhost.localdomain Mon Apr 15 16:44:19 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Gold 6244 CPU @ 3.60GHz
   2  "physical id"s (chips)
   32 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
      cpu cores : 8
      siblings : 16
      physical 0: cores 4 8 17 18 19 24 25 27
      physical 1: cores 2 3 4 8 17 20 24 26

From lscpu:
   Architecture:          x86_64
   CPU op-mode(s):        32-bit, 64-bit
   Byte Order:            Little Endian
   CPU(s):                32
   On-line CPU(s) list:   0-31
   Thread(s) per core:    2
   Core(s) per socket:    8
   Socket(s):             2
   NUMA node(s):          4
   Vendor ID:             GenuineIntel

(Continued on next page)
### Lenovo Global Technology

**ThinkSystem SR650**  
(3.60 GHz, Intel Xeon Gold 6244)

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

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**Test Date:** Apr-2019  
**Hardware Availability:** Apr-2019  
**Software Availability:** Nov-2018

### Platform Notes (Continued)

- CPU family: 6  
- Model: 85  
- Model name: Intel(R) Xeon(R) Gold 6244 CPU @ 3.60GHz  
- Stepping: 6  
- CPU MHz: 3600.000  
- BogoMIPS: 7200.00  
- Virtualization: VT-x  
- L1d cache: 32K  
- L1i cache: 32K  
- L2 cache: 1024K  
- L3 cache: 25344K  
- NUMA node0 CPU(s): 0,2,5,6,17,18,21,22  
- NUMA node1 CPU(s): 1,3,4,7,16,19,20,23  
- NUMA node2 CPU(s): 8,11,12,14,24,27,28,30  
- NUMA node3 CPU(s): 9,10,13,15,17,20,29,31  
- Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov ml constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ept cat_13 cdp_13 intel_pt ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rd_t_a avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512vd avx512vni xsavesopt xsaveopt xsave xsetbv1 cmp liable cmp_occupa cmp_occub cmp_total cmp_mbm cmp_mbm_local cmp_mbm_local  
- /proc/cpuinfo cache data  
- cache size : 25344 KB

From numactl --hardware  
**WARNING:** a numactl 'node' might or might not correspond to a physical chip.  
- available: 4 nodes (0-3)  
- node 0 cpus: 0 2 5 6 17 18 21 22  
- node 0 size: 97976 MB  
- node 0 free: 95453 MB  
- node 1 cpus: 1 3 4 7 16 19 20 23  
- node 1 size: 98304 MB  
- node 1 free: 95868 MB  
- node 2 cpus: 8 11 12 14 24 27 28 30  
- node 2 size: 98304 MB  
- node 2 free: 95986 MB  
- node 3 cpus: 9 10 13 15 25 26 29 31  
- node 3 size: 98304 MB  
- node 3 free: 96010 MB  
- node distances:  
  - node 0 1 2 3  

(Continued on next page)
SPEC CPU2017 Integer Rate Result

Lenovo Global Technology
ThinkSystem SR650
(3.60 GHz, Intel Xeon Gold 6244)

SPECrate2017_int_base = 134
SPECrate2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Platform Notes (Continued)

0:  10  11  21  21
1:  11  10  21  21
2:  21  21  10  11
3:  21  21  11  10

From /proc/meminfo
MemTotal: 395878232 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.6 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VARIANT="Server"
  VARIANT_ID="server"
  VERSION_ID="7.6"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.6 (Maipo)"
  redhat-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)
  system-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)

uname -a:
  Linux localhost.localdomain 3.10.0-957.el7.x86_64 #1 SMP Thu Oct 4 20:48:51 UTC 2018
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS

run-level 3 Apr 15 16:41

SPEC is set to: /home/cpu2017-1.0.5-ic19.0u1
  Filesystem Type Size Used Avail Use% Mounted on
  /dev/sdb2 xfs 689G 18G 672G 3% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMIOS" standard.
  BIOS Lenovo -[IVE135R-2.10]- 02/27/2019
  Memory:
    24x Samsung M393A2K43CB2-CVF 16 GB 2 rank 2933

(Continued on next page)
**SPEC CPU2017 Integer Rate Result**

**Lenovo Global Technology**
ThinkSystem SR650  
(3.60 GHz, Intel Xeon Gold 6244)

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**Platform Notes (Continued)**

(End of data from sysinfo program)

**Compiler Version Notes**

```plaintext
==============================================================================
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
  557.xz_r(base)
==============================================================================
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
  541.leela_r(base)
==============================================================================
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
FC  548.exchange2_r(base)
==============================================================================
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
```

**Base Compiler Invocation**

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64
**Lenovo Global Technology**

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**SPECrate2017_int_base = 134**  
**SPECrate2017_int_peak = Not Run**

## Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc_r: -DSPEC_LP64  
505.mcf_r: -DSPEC_LP64  
520.omnetpp_r: -DSPEC_LP64  
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
525.x264_r: -DSPEC_LP64  
531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64  
548.exchange2_r: -DSPEC_LP64  
557.xz_r: -DSPEC_LP64

## Base Optimization Flags

**C benchmarks:**
- Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
- qopt-mem-layout-trans=4  
- L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64  
- lqkmalloc

**C++ benchmarks:**
- Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
- qopt-mem-layout-trans=4  
- L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64  
- lqkmalloc

**Fortran benchmarks:**
- Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
- qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte  
- L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64  
- lqkmalloc

The flags files that were used to format this result can be browsed at:

http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-A.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-A.xml
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| Test Date: | Apr-2019 |
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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-04-15 04:44:18-0400.  