### SPEC® CPU2017 Floating Point Speed Result

**Supermicro**

SuperWorkstation 5039C-I (X11SCL-F, Intel Celeron G4900)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>11.7</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_fp_peak</td>
<td>11.9</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro  
**Test Date:** Apr-2019  
**Hardware Availability:** Nov-2018  
**Software Availability:** Sep-2018

#### Hardware

**CPU Name:** Intel Celeron G4900  
**Max MHz.:** 3100  
**Nominal:** 3100  
**Enabled:** 2 cores, 1 chip  
**Orderable:** 1 chip  
**Cache L1:** 32 KB I + 32 KB D on chip per core  
**L2:** 256 KB I+D on chip per core  
**L3:** 2 MB I+D on chip per chip  
**Other:** None  
**Memory:** 64 GB (4 x 16 GB 2Rx8 PC4-2666V-E, running at 2400)  
**Storage:** 1 x 200 GB SATA III SSD  
**Other:** None

#### Software

**OS:** SUSE Linux Enterprise Server 12 SP3 (x86_64)  
**Kernel:** 4.4.114-94.11-default  
**Compiler:** C/C++: Version 19.0.0.117 of Intel C/C++ Compiler for Linux; Fortran: Version 19.0.0.117 of Intel Fortran Compiler for Linux  
**Parallel:** Yes  
**Firmware:** Version 1.0a released Feb-2019  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 64-bit  
**Peak Pointers:** 64-bit  
**Other:** None  

---

**Threads**

<table>
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<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed2017_fp_base (11.7)</th>
<th>SPECspeed2017_fp_peak (11.9)</th>
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<tr>
<td>603.bwaves_s</td>
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<td>607.cactuBSSN_s</td>
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<td>627.cam4_s</td>
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<tr>
<td>628.pop2_s</td>
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<tr>
<td>638.imagick_s</td>
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<tr>
<td>644.nab_s</td>
<td>2</td>
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<td>649.fotonik3d_s</td>
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Supermicro

SuperWorkstation 5039C-I (X11SCL-F, Intel Celeron G4900)

SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

SPECspeed2017_fp_base = 11.7
SPECspeed2017_fp_peak = 11.9

Results Table

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<tr>
<th>Benchmark</th>
<th>Threads</th>
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<th>Seconds</th>
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<td>1616</td>
<td>9.74</td>
<td>1611</td>
<td>9.78</td>
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</tbody>
</table>

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

Supermicro
SuperWorkstation 5039C-I (X11SCL-F, Intel Celeron G4900)

| SPECspeed2017_fp_base = 11.7 |
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CPU2017 License: 001176
Test Sponsor: Supermicro
Test Date: Apr-2019
Hardware Availability: Nov-2018
Tested by: Supermicro
Software Availability: Sep-2018

Platform Notes (Continued)

running on linux-65nv Thu Apr 4 09:50:05 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

```
model name : Intel(R) Celeron(R) G4900 CPU @ 3.10GHz
1 "physical id"s (chips)
2 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 2
siblings : 2
physical 0: cores 0 1
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 2
On-line CPU(s) list: 0,1
Thread(s) per core: 1
Core(s) per socket: 2
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 158
Model name: Intel(R) Celeron(R) G4900 CPU @ 3.10GHz
Stepping: 11
CPU MHz: 3100.003
CPU max MHz: 3100.0000
CPU min MHz: 800.0000
BogoMIPS: 6191.96
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 256K
L3 cache: 2048K
NUMA node(s): 0,1
```

Flags:
fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmon perf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 sdbg cx16
xptr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave rdrand
lahf_lm abm 3dnowprefetch arat epb invpcid_single pni pts dtherm hwp hwp_notify

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**Platform Notes (Continued)**

- hwp_act_window hwp_epp intel_pt rsb_ctxsw spec_ctrl retpoleine kaiser tpr_shadow vnmi flexpriority ept vpid fsqsgbase tsc_adjust smep erms invpcid mpx rdseed smap clflushopt xsaveopt xsavec xgetbv1

```
/proc/cpuinfo cache data
 cache size: 2048 KB
```

From `numactl --hardware` WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 1 nodes (0)
node 0 cpus: 0 1
node 0 size: 64334 MB
node 0 free: 45207 MB
node distances:
  node 0
    0:  10
```

From `/proc/meminfo`

```
MemTotal: 65878272 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

From `/etc/*release*` /etc/*version*

```
SuSE-release: SUSE Linux Enterprise Server 12 (x86_64)
 VERSION = 12
 PATCHLEVEL = 3
 # This file is deprecated and will be removed in a future service pack or release.
 # Please check /etc/os-release for details about this release.
 os-release:
  NAME="SLES"
  VERSION="12-SP3"
  VERSION_ID="12.3"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

```
uname -a:
 Linux linux-65nv 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
 x86_64 x86_64 x86_64 GNU/Linux
```

- **Kernel self-reported vulnerability status:**

  - CVE-2017-5754 (Meltdown): Mitigation: PTI
  - CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers
  - CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

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| SPECspeed2017_fp_base = 11.7 |
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CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

| Test Date: Apr-2019 |
| Hardware Availability: Nov-2018 |
| Software Availability: Sep-2018 |

Platform Notes (Continued)

run-level 3 Apr 3 10:40
SPEC is set to: /home/cpu2017

<table>
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<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda3</td>
<td>xfs</td>
<td>145G</td>
<td>26G</td>
<td>119G</td>
<td>18%</td>
<td>/home</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0a 02/14/2019
Memory:
4x Micron 18ADF2G72AZ-2G6H1R 16 GB 2 rank 2667, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| CC 619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak) |
==============================================================================
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
CC 619.lbm_s(peak)
==============================================================================
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
FC 607.cactuBSSN_s(base, peak)
==============================================================================
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.0.117 Build 20180804

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SuperWorkstation 5039C-I (X11SCL-F, Intel Celeron G4900)

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**SPECspeed2017_fp_base** = 11.7

**SPECspeed2017_fp_peak** = 11.9

**Compiler Version Notes (Continued)**

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

```
FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base, peak)
```

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

```
FC 603.bwaves_s(peak) 649.fotonik3d_s(peak)
```

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

```
CC 621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
```

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

```
CC 621.wrf_s(peak) 628.pop2_s(peak)
```

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```
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.0.117 Build 20180804
```

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

```
CC 621.wrf_s(peak) 628.pop2_s(peak)
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Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

```
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.0.117 Build 20180804
```

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

---

**Base Compiler Invocation**

C benchmarks:

```
icc -m64 -std=c11
```

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**Base Compiler Invocation (Continued)**

Fortran benchmarks:
ifort  -m64

Benchmarks using both Fortran and C:
ifort  -m64 icc  -m64  -std=c11

Benchmarks using Fortran, C, and C++:
icpc  -m64 icc  -m64  -std=c11 ifort  -m64

---

**Base Portability Flags**

603.bwaves.s: -DSPEC_LP64
607.cactuBSSN.s: -DSPEC_LP64
619.lbm.s: -DSPEC_LP64
621.wrf.s: -DSPEC_LP64  -DSPEC_CASE_FLAG  -convert big_endian
627.cam4.s: -DSPEC_LP64  -DSPEC_CASE_FLAG
628.pop2.s: -DSPEC_LP64  -DSPEC_CASE_FLAG  -convert big_endian  
-assume byterecl
638.imagick.s: -DSPEC_LP64
644.nab.s: -DSPEC_LP64
649.fotonik3d.s: -DSPEC_LP64
654.roms.s: -DSPEC_LP64

---

**Base Optimization Flags**

C benchmarks:
-xSSE4.2  -ipo  -O3  -no-prec-div  -qopt-prefetch  -ffinite-math-only  
-qopt-mem-layout-trans=3  -qopenmp  -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP  -xSSE4.2  -ipo  -O3  -no-prec-div  -qopt-prefetch  
-ffinite-math-only  -qopt-mem-layout-trans=3  -qopenmp

Benchmarks using both Fortran and C:
-xSSE4.2  -ipo  -O3  -no-prec-div  -qopt-prefetch  -ffinite-math-only  
-qopt-mem-layout-trans=3  -qopenmp  -DSPEC_OPENMP  
-nostandard-realloc-lhs  -align array32byte

Benchmarks using Fortran, C, and C++:
-xSSE4.2  -ipo  -O3  -no-prec-div  -qopt-prefetch  -ffinite-math-only  
-qopt-mem-layout-trans=3  -qopenmp  -DSPEC_OPENMP

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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):
-nostandard-realloc-lhs -align array32byte

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
619.lbm_s: basepeak = yes
638.imagick_s: -xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP
644.nab_s: Same as 638.imagick_s

Fortran benchmarks:
603.bwaves_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xSSE4.2 -qopt-prefetch -ipo -O3
-no-prec-div -ffinite-math-only -qopt-mem-layout-trans=3
-qopenmp -nostandard-realloc-lhs -align array32byte

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Supermicro
SuperWorkstation 5039C-I (X11SCL-F , Intel Celeron G4900)

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<tbody>
<tr>
<td>11.7</td>
<td>11.9</td>
</tr>
</tbody>
</table>

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Peak Optimization Flags (Continued)

649.fotonik3d_s: basepeak = yes
654.roms_s: -DSPEC_OPENMP -xSSE4.2 -ipo -O3 -no-prec-div
-ipo -O3 -no-prec-div -ffinite-math-only -qopt-mem-layout-trans=3
-qopenmp -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xSSE4.2
-ipo -O3 -no-prec-div -ffinite-math-only
-ipo -O3 -no-prec-div -ffinite-math-only
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte
627.cam4_s: basepeak = yes
628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:
-xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-ipo -O3 -no-prec-div -ffinite-math-only
-ipo -O3 -no-prec-div -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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