### SPEC® CPU2017 Integer Speed Result

**Huawei**

**Huawei CH225 V5 (Intel Xeon Gold 5218N)**

<table>
<thead>
<tr>
<th>Test Sponsor</th>
<th>Huawei</th>
<th>Hardware Availability</th>
<th>Apr-2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tested by</td>
<td>Huawei</td>
<td>Software Availability</td>
<td>Dec-2018</td>
</tr>
<tr>
<td>CPU2017 License</td>
<td>3175</td>
<td>Test Date:</td>
<td>Apr-2019</td>
</tr>
<tr>
<td>SPECspeed2017_int_base</td>
<td>9.74</td>
<td></td>
<td></td>
</tr>
<tr>
<td>SPECspeed2017_int_peak</td>
<td>Not Run</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

#### Hardware

- **CPU Name:** Intel Xeon Gold 5218N
- **Max MHz.:** 3700
- **Nominal:** 2300
- **Enabled:** 32 cores, 2 chips
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **Cache L2:** 1 MB I+D on chip per core
- **Cache L3:** 22 MB I+D on chip per chip
- **Other:** None
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2933Y-R, running at 2666)
- **Storage:** 1 x 1200 GB SAS, 10000 RPM
- **Other:** None

#### Software

- **OS:** SUSE Linux Enterprise Server 12 SP4 (x86_64)
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++ Compiler Build 20181018 for Linux; Fortran: Version 19.0.1.144 of Intel Fortran Compiler Build 20181018 for Linux
- **Parallel:** Yes
- **Firmware:** Version 6.52 Released Mar-2019
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** Not Applicable
- **Other:** jemalloc memory allocator V5.0.1

#### SPECspeed2017_int_base

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed2017_int_base (9.74)</th>
</tr>
</thead>
<tbody>
<tr>
<td>4</td>
<td>6.80</td>
</tr>
<tr>
<td>8</td>
<td>10.0</td>
</tr>
<tr>
<td>12</td>
<td>12.7</td>
</tr>
<tr>
<td>16</td>
<td>7.44</td>
</tr>
<tr>
<td>32</td>
<td>11.6</td>
</tr>
<tr>
<td>64</td>
<td>5.48</td>
</tr>
<tr>
<td>128</td>
<td>4.78</td>
</tr>
<tr>
<td>256</td>
<td>14.1</td>
</tr>
<tr>
<td>512</td>
<td>22.1</td>
</tr>
</tbody>
</table>

---

**Benchmark Results**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Result (Threads)</th>
<th>Result (16 threads)</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench</td>
<td>10.0</td>
<td>Not Run</td>
</tr>
<tr>
<td>gcc</td>
<td>12.7</td>
<td></td>
</tr>
<tr>
<td>mcf</td>
<td>7.44</td>
<td></td>
</tr>
<tr>
<td>omnetpp</td>
<td>12.6</td>
<td></td>
</tr>
<tr>
<td>xalancbmk</td>
<td>11.6</td>
<td></td>
</tr>
<tr>
<td>x264</td>
<td>5.48</td>
<td></td>
</tr>
<tr>
<td>xalancbmk</td>
<td>14.1</td>
<td></td>
</tr>
<tr>
<td>x264</td>
<td>22.1</td>
<td></td>
</tr>
<tr>
<td>deepsjeng</td>
<td>10.0</td>
<td></td>
</tr>
<tr>
<td>leela</td>
<td>12.7</td>
<td></td>
</tr>
<tr>
<td>exchange2</td>
<td>6.80</td>
<td></td>
</tr>
<tr>
<td>xz</td>
<td>11.6</td>
<td></td>
</tr>
<tr>
<td>total</td>
<td>9.74</td>
<td></td>
</tr>
</tbody>
</table>

**Notes:**

- **Hardware Confidence:** 5
- **Software Confidence:** 5

---

**Additional Information:**

Huawei CH225 V5 (Intel Xeon Gold 5218N) detailed specifications and performance results as provided by SPEC.
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**Huawei CH225 V5 (Intel Xeon Gold 5218N)**

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**SPECspeed2017_int_base = 9.74**

**SPECspeed2017_int_peak = Not Run**

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>32</td>
<td>262</td>
<td>6.78</td>
<td>261</td>
<td>6.81</td>
<td>261</td>
<td>6.80</td>
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<tr>
<td>602.gcc_s</td>
<td>32</td>
<td>398</td>
<td>10.0</td>
<td>394</td>
<td>10.1</td>
<td>405</td>
<td>9.84</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>32</td>
<td>375</td>
<td>12.6</td>
<td>370</td>
<td>12.8</td>
<td>371</td>
<td>12.7</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>32</td>
<td>220</td>
<td>7.41</td>
<td>219</td>
<td>7.44</td>
<td>219</td>
<td>7.44</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>32</td>
<td>113</td>
<td>12.6</td>
<td>112</td>
<td>12.6</td>
<td>114</td>
<td>12.4</td>
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<tr>
<td>625.x264_s</td>
<td>32</td>
<td>151</td>
<td>11.6</td>
<td>151</td>
<td>11.7</td>
<td>152</td>
<td>11.6</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>32</td>
<td>262</td>
<td>5.48</td>
<td>261</td>
<td>5.48</td>
<td>261</td>
<td>5.49</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>32</td>
<td>357</td>
<td>4.78</td>
<td>357</td>
<td>4.78</td>
<td>357</td>
<td>4.77</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>32</td>
<td>209</td>
<td>14.0</td>
<td>208</td>
<td>14.1</td>
<td>208</td>
<td>14.1</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>32</td>
<td>279</td>
<td>22.1</td>
<td>279</td>
<td>22.1</td>
<td>280</td>
<td>22.1</td>
</tr>
</tbody>
</table>

**SPECspeed2017_int_base = 9.74**

**SPECspeed2017_int_peak = Not Run**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:

- KMP_AFFINITY = "granularity=fine,compact,1,0"
- OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

SPEC CPU2017 Integer Speed Result

Huawei

Huawei CH225 V5 (Intel Xeon Gold 5218N)

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

SPECspeed2017_int_base = 9.74
SPECspeed2017_int_peak = Not Run

Test Date: Apr-2019
Hardware Availability: Apr-2019
Software Availability: Dec-2018

BIOS configuration:
Power Policy Set to Load Balance
Hyper-Threading Set to Disable
XPT Prefetch Set to Enabled
Sysinfo program /spec2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on sles12sp4 Fri Apr 12 02:39:49 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 5218N CPU @ 2.30GHz
  2 "physical id" s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                32
On-line CPU(s) list:   0-31
Thread(s) per core:    1
Core(s) per socket:    16
Socket(s):             2
NUMA node(s):          2
Vendor ID:             GenuineIntel
CPU family:            6
Model:                 85
Model name:            Intel(R) Xeon(R) Gold 5218N CPU @ 2.30GHz
Stepping:              6
CPU MHz:               2300.000
CPU max MHz:           3900.0000
CPU min MHz:           1000.0000
BogoMIPS:              4600.00
Virtualization:        VT-x
L1d cache:             32K
L1i cache:             32K
L2 cache:              1024K
L3 cache:              22528K

(Continued on next page)
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SPEC CPU2017 Integer Speed Result

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Platform Notes (Continued)

NUMA node0 CPU(s): 0-15
NUMA node1 CPU(s): 16-31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrp pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid
fsysbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f
avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
xsaveopt xsaveopt xsavec xgetbv1 xsave csq_llc cqm_occu_llc cqm_mbms_total cqm_mbms_local
dtss counter ida arat pln pts pku ospke avx512_vnni flush_l1d arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
node 0 size: 191933 MB
node 0 free: 191120 MB
node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
node 1 size: 193252 MB
node 1 free: 190843 MB
node distances:
node 0 1
0: 10 21
1: 21 10

From /proc/meminfo
MemTotal: 394430292 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 4
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP4"
VERSION_ID="12.4"

(Continued on next page)
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**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei

**Test Date:** Apr-2019  
**Hardware Availability:** Apr-2019  
**Software Availability:** Dec-2018

### Platform Notes (Continued)

```plaintext
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP4"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME=cpe:/o:suse:sles:12:sp4"

uname -a:
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

r run-level 3 Apr 11 07:57

SPEC is set to: /spec2017

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda3</td>
<td>xfs</td>
<td>700G</td>
<td>32G</td>
<td>669G</td>
<td>5%</td>
<td>/</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 6.52 03/16/2019
Memory:
  24x Samsung M393A2K43CB2-CVF 16 GB 2 rank 2933, configured at 2666

(End of data from sysinfo program)
```

### Compiler Version Notes

```
==============================================================================
CC  600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base)
  657.xz_s(base)
==============================================================================
```

**Intel(R) C**

```
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, 
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
==============================================================================
CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
  641.leela_s(base)
==============================================================================
```

(Continued on next page)
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**Compiler Version Notes (Continued)**

---

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

---

PC 648.exchange2_s(base)

---

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

---

**Base Compiler Invocation**

C benchmarks:

icc -m64 -std=c11

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

**Base Portability Flags**

600.perlbench_s: -DSPEC_LP64  -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64  -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
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### Base Optimization Flags

#### C benchmarks:
- `-Wl,-z,muldefs`  
- `-xCORE-AVX512`  
- `-ipo -O3 -no-prec-div`  
- `-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP`  
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

#### C++ benchmarks:
- `-Wl,-z,muldefs`  
- `-xCORE-AVX512`  
- `-ipo -O3 -no-prec-div`  
- `-qopt-mem-layout-trans=4`  
- `-lqkmalloc`

#### Fortran benchmarks:
- `-xCORE-AVX512`  
- `-ipo -O3 -no-prec-div`  
- `-qopt-mem-layout-trans=4`  
- `-nostandard-realloc-lhs`

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


---

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-04-12 02:39:48-0400.  