Huawei

Huawei 1288H V5 (Intel Xeon Silver 4208)

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
<th>SPECrate2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>83.1</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name:</td>
<td>OS:</td>
</tr>
<tr>
<td>Intel Xeon Silver</td>
<td>SUSE Linux Enterprise Server 12 SP4 (x86_64)</td>
</tr>
<tr>
<td>4208</td>
<td>4.12.14-94.41-default</td>
</tr>
<tr>
<td>Max MHz.:</td>
<td>Compiler:</td>
</tr>
<tr>
<td>3200</td>
<td>C/C++: Version 19.0.1.144 of Intel C/C++</td>
</tr>
<tr>
<td>Nominal:</td>
<td>Compiler Build 20181018 for Linux:</td>
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<tr>
<td>2100</td>
<td>Fortran: Version 19.0.1.144 of Intel Fortran</td>
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<tr>
<td>Enabled:</td>
<td>Compiler Build 20181018 for Linux</td>
</tr>
<tr>
<td>16 cores, 2 chips, 2 threads/core</td>
<td>Parallel: No</td>
</tr>
<tr>
<td>Orderable:</td>
<td>Firmware:</td>
</tr>
<tr>
<td>1.2 chips</td>
<td>Version 6.52 Released Mar-2019</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>File System:</td>
</tr>
<tr>
<td>32 KB I + 32 KB D on chip per core</td>
<td>xfs</td>
</tr>
<tr>
<td>L2:</td>
<td>System State:</td>
</tr>
<tr>
<td>1 MB I+D on chip per core</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>L3:</td>
<td>Base Pointers:</td>
</tr>
<tr>
<td>11 MB I+D on chip per chip</td>
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</tr>
<tr>
<td>Other:</td>
<td>Peak Pointers:</td>
</tr>
<tr>
<td>None</td>
<td>Not Applicable</td>
</tr>
<tr>
<td>Memory:</td>
<td>Other:</td>
</tr>
<tr>
<td>384 GB (24 x 16 GB 2Rx8 PC4-2933Y-R, running at 2400)</td>
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</tr>
<tr>
<td>Storage:</td>
<td>Hardware</td>
</tr>
<tr>
<td>1 x 1200 GB SAS, 10000 RPM</td>
<td>CPU Name:</td>
</tr>
<tr>
<td></td>
<td>Intel Xeon Silver 4208</td>
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<tr>
<td>Tested by:</td>
<td>Hardware Availability: Apr-2019</td>
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<td>Huawei</td>
<td>Software Availability: Dec-2018</td>
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Test Date: Mar-2019
Hardware Availability: Apr-2019
Software Availability: Dec-2018

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<thead>
<tr>
<th>Copies</th>
<th>SPECrate2017_int_base</th>
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<tbody>
<tr>
<td>500.perlbench_r 32</td>
<td>63.3</td>
</tr>
<tr>
<td>502.gcc_r 32</td>
<td>72.4</td>
</tr>
<tr>
<td>505.mcf_r 32</td>
<td>59.1</td>
</tr>
<tr>
<td>520.omnetpp_r 32</td>
<td>116</td>
</tr>
<tr>
<td>523.xalancbmk_r 32</td>
<td>102</td>
</tr>
<tr>
<td>525.x264_r 32</td>
<td>146</td>
</tr>
<tr>
<td>531.deepsjeng_r 32</td>
<td>68.2</td>
</tr>
<tr>
<td>541.leela_r 32</td>
<td>61.7</td>
</tr>
<tr>
<td>548.exchange2_r 32</td>
<td>143</td>
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<tr>
<td>557.xz_r 32</td>
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SPECrater2017_int_base (83.1)
Results Table

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<tr>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>803</td>
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<td><strong>625</strong></td>
<td><strong>72.4</strong></td>
<td>633</td>
<td>71.6</td>
<td>625</td>
<td>72.6</td>
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<tr>
<td>505.mcf_r</td>
<td>32</td>
<td>447</td>
<td>116</td>
<td>444</td>
<td>116</td>
<td><strong>445</strong></td>
<td><strong>116</strong></td>
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<tr>
<td>520.omnetpp_r</td>
<td>32</td>
<td>713</td>
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<td><strong>59.1</strong></td>
<td>708</td>
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<td>523.xalancbmk_r</td>
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<td><strong>331</strong></td>
<td><strong>102</strong></td>
<td>331</td>
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<tr>
<td>525.x264_r</td>
<td>32</td>
<td>387</td>
<td>145</td>
<td><strong>385</strong></td>
<td><strong>146</strong></td>
<td>383</td>
<td>146</td>
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<tr>
<td>531.deepsjeng_r</td>
<td>32</td>
<td><strong>538</strong></td>
<td><strong>68.2</strong></td>
<td>538</td>
<td>68.2</td>
<td>538</td>
<td>68.2</td>
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<tr>
<td>541.leela_r</td>
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<td><strong>860</strong></td>
<td><strong>61.7</strong></td>
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<td>61.6</td>
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<tr>
<td>548.exchange2_r</td>
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<td>586</td>
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<td><strong>143</strong></td>
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<tr>
<td>557.xz_r</td>
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<td>56.1</td>
<td><strong>616</strong></td>
<td><strong>56.1</strong></td>
</tr>
</tbody>
</table>

**SPECrate2017_int_base = 83.1**

**SPECrate2017_int_peak = Not Run**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

(Continued on next page)
Huawei

Huawei 1288H V5 (Intel Xeon Silver 4208)

| SPECrate2017_int_base = 83.1 |
| SPECrate2017_int_peak = Not Run |

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Mar-2019
Hardware Availability: Apr-2019
Software Availability: Dec-2018

General Notes (Continued)

is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Power Policy Set to Performance
XPT Prefetch Set to Enabled
Sysinfo program /spec2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on sles12sp4 Fri Mar 29 12:20:37 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Silver 4208 CPU @ 2.10GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4208 CPU @ 2.10GHz
Stepping: 6
CPU MHz: 2100.000
CPU max MHz: 3200.0000
CPU min MHz: 800.0000
BogoMIPS: 4200.00
Virtualization: VT-x

(Continued on next page)
Huawei 1288H V5 (Intel Xeon Silver 4208)

Huawei

SPECrate2017_int_base = 83.1
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CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Mar-2019
Tested by: Huawei
Hardware Availability: Apr-2019
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Platform Notes (Continued)

L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 11264K
NUMA node0 CPU(s): 0-7,16-23
NUMA node1 CPU(s): 8-15,24-31
Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 ds CPL vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat L3 cdg L3 invpcid_single ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 xsave vctors cmq_llc cmq_occup_llc cmq_mbb_total cmq_mbb_local dtherm ida arat pfn ts pku ospke avx512_vnni flush_l1d arch_capabilities

/proc/cpuinfo cache data
cache size : 11264 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
node 0 size: 191904 MB
node 0 free: 191276 MB
node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
node 1 size: 193280 MB
node 1 free: 192802 MB
node distances:
node 0 1
0: 10 21
1: 21 10

From /proc/meminfo
MemTotal: 394429268 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 4

# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

(Continued on next page)
Huawei
Huawei 1288H V5 (Intel Xeon Silver 4208)

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Platform Notes (Continued)

os-release:
  NAME="SLES"
  VERSION="12-SP4"
  VERSION_ID="12.4"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP4"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp4"

uname -a:
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 Mar 29 12:17

SPEC is set to: /spec2017
  Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda3      xfs   700G   15G  686G   3% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 6.52 03/16/2019
Memory:
  24x Samsung M393A2K43CB2-CVF 16 GB 2 rank 2933, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base) |
| 557.xz_r(base)                                                             |
==============================================================================

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
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(Continued on next page)
 SPEC CPU2017 Integer Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Huawei

Huawei 1288H V5 (Intel Xeon Silver 4208)

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CPU2017 License: 3175  Test Date: Mar-2019
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Compiler Version Notes (Continued)

CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
541.leela_r(base)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
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FC 548.exchange2_r(base)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
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Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64  -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64  -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

CPU2017 License: 3175  Test Date: Mar-2019
Test Sponsor: Huawei  Hardware Availability: Apr-2019
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Compiler Version Notes (Continued)

CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
541.leela_r(base)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
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FC 548.exchange2_r(base)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64  -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64  -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
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</table>

#### Base Optimization Flags

**C benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=4`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64`
- `-lqkmalloc`

**C++ benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=4`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64`
- `-lqkmalloc`

**Fortran benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64`
- `-lqkmalloc`

The flags files that were used to format this result can be browsed at:

You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-03-29 12:20:36-0400.