## Lenovo Global Technology

### ThinkSystem SR590

**2.50 GHz, Intel Xeon Gold 5215L**

**SPECrate2017_fp_base = 125**

**SPECrate2017_fp_peak = Not Run**

| Copies | 0  | 15.0 | 30.0 | 45.0 | 60.0 | 75.0 | 90.0 | 105 | 120 | 135 | 150 | 165 | 180 | 195 | 210 | 225 | 240 | 255 | 270 | 285 | 300 | 315 | 330 | 345 | 360 | 375 | 390 | 405 | 420 | 435 | 450 |
|---------|----|------|------|------|------|------|------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 503.bwaves_r | 40 |      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 507.cactuBSSN_r | 40 |      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 508.namd_r | 40 |      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 510.parest_r | 40 |      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 511.povray_r | 40 |      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 519.lbm_r | 40 |      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 521.wrf_r | 40 |      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 526.blender_r | 40 |      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 527.cam4_r | 40 |      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 538.imagick_r | 40 |      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 544.nab_r | 40 |      |      |      |      |      |      |     |     |     |     |     |     |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 549.fotonik3d_r | 40 |      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 554.roms_r | 40 |      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |

### Hardware

- **CPU Name:** Intel Xeon Gold 5215L
- **Max MHz.:** 3400
- **Nominal:** 2500
- **Enabled:** 20 cores, 2 chips, 2 threads/core
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 13.75 MB I+D on chip per chip
- **Other:** None
- **Memory:** 192 GB (12 x 16 GB 2Rx8 PC4-2933Y-R, running at 2666)
- **Storage:** 1 x 960 GB SATA SSD
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 12 SP4 (x86_64)
- **Compiler:** C/C++: Version 19.0.0.117 of Intel C/C++
- **Compiler for Linux:** None
- **Parallel:** No
- **File System:** btrfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** Not Applicable
- **Other:** None
## SPEC CPU2017 Floating Point Rate Result

**Lenovo Global Technology**  
ThinkSystem SR590  
(2.50 GHz, Intel Xeon Gold 5215L)

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>40</td>
<td>1158</td>
<td>346</td>
<td>1156</td>
<td>347</td>
<td>1158</td>
<td>346</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>40</td>
<td>559</td>
<td>90.6</td>
<td>557</td>
<td>90.9</td>
<td>557</td>
<td>90.9</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>40</td>
<td>433</td>
<td>87.8</td>
<td>431</td>
<td>88.2</td>
<td>429</td>
<td>88.6</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>40</td>
<td>1432</td>
<td>73.1</td>
<td>1431</td>
<td>73.1</td>
<td>1431</td>
<td>73.1</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>40</td>
<td>715</td>
<td>131</td>
<td>717</td>
<td>130</td>
<td>715</td>
<td>131</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>40</td>
<td>488</td>
<td>86.3</td>
<td>489</td>
<td>86.3</td>
<td>489</td>
<td>86.1</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>40</td>
<td>632</td>
<td>142</td>
<td>629</td>
<td>142</td>
<td>634</td>
<td>141</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>40</td>
<td>533</td>
<td>114</td>
<td>535</td>
<td>114</td>
<td>536</td>
<td>114</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>40</td>
<td>545</td>
<td>128</td>
<td>544</td>
<td>129</td>
<td>544</td>
<td>129</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>40</td>
<td>380</td>
<td>262</td>
<td>380</td>
<td>262</td>
<td>380</td>
<td>262</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>40</td>
<td>347</td>
<td>194</td>
<td>346</td>
<td>195</td>
<td>346</td>
<td>195</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>40</td>
<td>1311</td>
<td>119</td>
<td>1307</td>
<td>119</td>
<td>1311</td>
<td>119</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>40</td>
<td>958</td>
<td>66.3</td>
<td>962</td>
<td>66.1</td>
<td>965</td>
<td>65.9</td>
</tr>
</tbody>
</table>

**SPECrate2017_fp_base** = 125  
**SPECrate2017_fp_peak** = Not Run

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

**Stack size set to unlimited using "ulimit -s unlimited"**

### General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19/lib/intel64"
```

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu <etc>
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Choose Operating Mode set to Custom Mode
MONITOR/MWAIT set to Enable
Stale AtoS set to Enable
LLC dead line alloc set to Disable
Sysinfo program /home/cpu2017-1.0.5-ic19/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-2uov Wed Apr 24 19:59:59 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5215L CPU @ 2.50GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 10
  siblings : 20
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
Architecture:       x86_64
CPU op-mode(s):     32-bit, 64-bit
Byte Order:         Little Endian
CPU(s):             40
On-line CPU(s) list: 0-39
Thread(s) per core: 2
Core(s) per socket: 10
Socket(s):          2
NUMA node(s):       2
Lenovo Global Technology
ThinkSystem SR590
(2.50 GHz, Intel Xeon Gold 5215L)

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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: Apr-2019
Tested by: Lenovo Global Technology
Hardware Availability: Apr-2019
Software Availability: Dec-2018

Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5215L CPU @ 2.50GHz
Stepping: 6
CPU MHz: 2500.000
CPU max MHz: 3400.0000
CPU min MHz: 1000.0000
BogoMIPS: 5000.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 14080K
NUMA node0 CPU(s): 0-9,20-29
NUMA node1 CPU(s): 10-19,30-39
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single ssbd mba ibrs ibpb tpr_shadow vnmi flexpriority ept vpid
fsbegsbase tsc_adjust bmi1 hle avx2 smep bmi2 erdsvs invpcid rtm cqm mpx rdt_a avx512f
avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local
dtherm ida arat pln pts pku ospke avx512_vnni flush_l1d arch_capabilities

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 20 21 22 23 24 25 26 27 28 29
node 0 size: 96356 MB
node 0 free: 89363 MB
node 1 cpus: 10 11 12 13 14 15 16 17 18 19 30 31 32 33 34 35 36 37 38 39
node 1 size: 96712 MB
node 1 free: 91577 MB
node distances:
node 0 1
 0: 10 21
 1: 21 10

From /proc/meminfo
MemTotal: 197702268 kB

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR590
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SPEC CPU2017 Floating Point Rate Result

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**Test Sponsor:** Lenovo Global Technology  
**Test Date:** Apr-2019  
**Tested by:** Lenovo Global Technology  
**Hardware Availability:** Apr-2019

**Software Availability:** Dec-2018

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**Platform Notes (Continued)**

HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
   SUSE Linux Enterprise Server 12 (x86_64)
   VERSION = 12
   PATCHLEVEL = 4
   # This file is deprecated and will be removed in a future service pack or release.
   # Please check /etc/os-release for details about this release.

os-release:
   NAME="SLES"
   VERSION="12-SP4"
   VERSION_ID="12.4"
   PRETTY_NAME="SUSE Linux Enterprise Server 12 SP4"
   ID="sles"
   ANSI_COLOR="0;32"
   CPE_NAME="cpe:/o:suse:sles:12:sp4"

uname -a:
   x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_PW

run-level 3 Apr 24 11:12

SPEC is set to: /home/cpu2017-1.0.5-ic19
   Filesystem   Type   Size  Used Avail Use% Mounted on
   /dev/sdb3    btrfs  740G  41G  699G   6% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMI BIOS" standard.
   BIOS Lenovo -[TEE135L-2.10]- 01/10/2019
   Memory:
      4x NO DIMM NO DIMM
      12x SK Hynix HMA82GR7CJR8N-WM 16 GB 2 rank 2933, configured at 2666

(End of data from sysinfo program)
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Compiler Version Notes

==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================

==============================================================================
CXXC 508.namd_r(base) 510.parest_r(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================

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CC  511.povray_r(base) 526.blender_r(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.0.117Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================

==============================================================================
FC  507.cactuBSSN_r(base)
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.0.117 Build 20180804
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==============================================================================
FC  503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
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Compiler Version Notes (Continued)

==============================================================================
CC  521.wrf_r(base) 527.cam4_r(base)
==============================================================================

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

503.bwaves_r -DSPEC_LP64
507.cactuBSSN_r -DSPEC_LP64
508.namd_r -DSPEC_LP64
510.parest_r -DSPEC_LP64
511.povray_r -DSPEC_LP64
519.lbm_r -DSPEC_LP64
521.wrf_r -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r -DSPEC_LP64 -DSPEC_LINUX -funsIGNED-char

(Continued on next page)
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Base Portability Flags (Continued)

527.cam4_r: -DSPEC_LP64 - DSPEC_CASE_FLAG
538.imagemick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -auto
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -auto
-nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -auto
-nostandard-realloc-lhs -align array32byte

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-A.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-A.xml
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<thead>
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| CPU2017 License: 9017                  | Test Date: Apr-2019 |
| Test Sponsor: Lenovo Global Technology | Hardware Availability: Apr-2019 |
| Tested by: Lenovo Global Technology   | Software Availability: Dec-2018 |

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-04-24 07:59:58-0400.