## SPEC® CPU2017 Floating Point Rate Result

### Hewlett Packard Enterprise

(2.50 GHz, Intel Xeon Gold 5215M)

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>80</td>
<td>249</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 5215M
- **Max MHz.:** 3400
- **Nominal:** 2500
- **Enabled:** 40 cores, 4 chips, 2 threads/core
- **Orderable:** 1, 2, 4 chip(s)
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 13.75 MB I+D on chip per chip
- **Other:** None
- **Memory:** 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R)
- **Storage:** 1 x 400 GB SAS SSD, RAID 0
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 15 (x86_64)
- **Kernel:** 4.12.14-23-default
- **Compiler:** C/C++: Version 19.0.2.187 of Intel C/C++
- **Compiler Build:** 20190117 for Linux;
  Fortran: Version 19.0.2.187 of Intel Fortran
- **Compiler Build:** 20190117 for Linux
- **Parallel:** No
- **Firmware:** HPE BIOS Version U34 02/02/2019 released Apr-2019
- **File System:** btrfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** Not Applicable
- **Other:** None
Hewlett Packard Enterprise  
ProLiant DL560 Gen10  
(2.50 GHz, Intel Xeon Gold 5215M)  

SPECrate2017_fp_base = 249  
SPECrate2017_fp_peak = Not Run

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>80</td>
<td>1172</td>
<td></td>
<td>685</td>
<td></td>
<td>1174</td>
<td>683</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>80</td>
<td>541</td>
<td>187</td>
<td>456</td>
<td>167</td>
<td>458</td>
<td>166</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>80</td>
<td>455</td>
<td>167</td>
<td>1544</td>
<td>136</td>
<td>1539</td>
<td>136</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>80</td>
<td>1534</td>
<td>136</td>
<td>723</td>
<td>258</td>
<td>720</td>
<td>259</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>80</td>
<td>720</td>
<td>259</td>
<td>509</td>
<td>166</td>
<td>510</td>
<td>165</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>80</td>
<td>510</td>
<td>165</td>
<td>509</td>
<td>166</td>
<td>510</td>
<td>165</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>80</td>
<td>647</td>
<td>277</td>
<td>643</td>
<td>279</td>
<td>648</td>
<td>277</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>80</td>
<td>482</td>
<td>253</td>
<td>482</td>
<td>253</td>
<td>483</td>
<td>252</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>80</td>
<td>562</td>
<td>249</td>
<td>564</td>
<td>248</td>
<td>562</td>
<td>249</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>80</td>
<td>375</td>
<td>531</td>
<td>376</td>
<td>529</td>
<td>374</td>
<td>532</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>80</td>
<td>350</td>
<td>384</td>
<td>345</td>
<td>390</td>
<td>346</td>
<td>389</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>80</td>
<td>1266</td>
<td>246</td>
<td>1266</td>
<td>246</td>
<td>1262</td>
<td>247</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>80</td>
<td>1008</td>
<td>126</td>
<td>1009</td>
<td>126</td>
<td>1004</td>
<td>127</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3 > /proc/sys/vm/drop_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

General Notes

Environment variables set by runcpu before the start of the run:  
LD_LIBRARY_PATH = "/home/cpu2017_u2/lib/ia32:/home/cpu2017_u2/lib/intel64"  

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)

(Continued on next page)
Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL560 Gen10
(2.50 GHz, Intel Xeon Gold 5215M)

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>249</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_peak</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

**General Notes (Continued)**

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

**Platform Notes**

BIOS Configuration:
- Thermal Configuration set to Maximum Cooling
- Memory Patrol Scrubbing set to Disabled
- LLC Prefetch set to Enabled
- LLC Dead Line Allocation set to Disabled
- Enhanced Processor Performance set to Enabled
- Workload Profile set to General Throughput Compute
- Workload Profile set to Custom
- Energy/Performance Bias set to Balanced Performance
- Advanced Memory Protection set to Advanced ECC

Sysinfo program /home/cpu2017_u2/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-x8dm Fri Apr 12 19:53:21 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
```
model name : Intel(R) Xeon(R) Gold 5215M CPU @ 2.50GHz
 4 "physical id"s (chips)
 80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
physical 2: cores 0 1 2 3 4 8 9 10 11 12
physical 3: cores 0 1 2 3 4 8 9 10 11 12
```

From lscpu:
```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 80
On-line CPU(s) list: 0-79
Thread(s) per core: 2
```

(Continued on next page)
Hewlett Packard Enterprise
ProLiant DL560 Gen10
(2.50 GHz, Intel Xeon Gold 5215M)

SPECrate2017_fp_base = 249
SPECrate2017_fp_peak = Not Run

CPU2017 License: 3
Test Sponsor: HPE
Test Date: Apr-2019
Tested by: HPE
Hardware Availability: Apr-2019
Software Availability: Feb-2019

Platform Notes (Continued)

Core(s) per socket: 10
Socket(s): 4
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5215M CPU @ 2.50GHz
Stepping: 6
CPU MHz: 2500.000
BogoMIPS: 5000.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 14080K
NUMA node0 CPU(s): 0-9,40-49
NUMA node1 CPU(s): 10-19,50-59
NUMA node2 CPU(s): 20-29,60-69
NUMA node3 CPU(s): 30-39,70-79
Flags: fpu vme de pse mso pmr mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abmm 3nowprefetch cpuid_fault epb cat_13 cdp_l3 invpcid_single intel.ppinn mba tpr_shadow vmmi flexpriority ept vpid fsgsbase tsc_adjust bni hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local ibpb ibrs stibp dtherm ida arat pln pts pkup ospe axx512_vnni arch_capabilities ssbd

/proc/cpuinfo cache data
  cache size : 14080 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 40 41 42 43 44 45 46 47 48 49
  node 0 size: 386653 MB
  node 0 free: 386283 MB
  node 1 cpus: 10 11 12 13 14 15 16 17 18 19 50 51 52 53 54 55 56 57 58 59
  node 1 size: 387067 MB
  node 1 free: 386821 MB
  node 2 cpus: 20 21 22 23 24 25 26 27 28 29 60 61 62 63 64 65 66 67 68 69
  node 2 size: 387067 MB
  node 2 free: 386652 MB
  node 3 cpus: 30 31 32 33 34 35 36 37 38 39 70 71 72 73 74 75 76 77 78 79

(Continued on next page)
Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL560 Gen10
(2.50 GHz, Intel Xeon Gold 5215M)

SPECrate2017_fp_base = 249
SPECrate2017_fp_peak = Not Run

Platform Notes (Continued)

node 3 size: 387037 MB
node 3 free: 386815 MB
node distances:
  node 0 1 2 3
  0:  10 21 21 21
  1:  21 10 21 21
  2:  21 21 10 21
  3:  21 21 21 10

From /proc/meminfo
MemTotal:       1584975108 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
  USE Linux Enterprise Server 15

From /etc/*release* /etc/*version*
os-release:
  NAME="SLES"
  VERSION="15"
  VERSION_ID="15"
  PRETTY_NAME="SUSE Linux Enterprise Server 15"
  ID="sles"
  ID_LIKE="suse"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
  Linux linux-x8dm 4.12.14-23-default #1 SMP Tue May 29 21:04:44 UTC 2018 (cd0437b)
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 Apr 12 19:52

SPEC is set to: /home/cpu2017_u2

Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda2      btrfs  371G  141G  230G  39%  /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow

(Continued on next page)
**SPEC CPU2017 Floating Point Rate Result**

Hewlett Packard Enterprise  
(Test Sponsor: HPE)  
ProLiant DL560 Gen10  
(2.50 GHz, Intel Xeon Gold 5215M)  

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>3</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>HPE</td>
</tr>
<tr>
<td>Tested by:</td>
<td>HPE</td>
</tr>
</tbody>
</table>

SPECrater2017_fp_base = 249  
SPECrater2017_fp_peak = Not Run

**Platform Notes (Continued)**

Hardware to be accurately determined”, but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  

BIOS HPE U34 02/02/2019  
Memory:  
5x HPE 840758-091 32 GB 2 rank 2666  
43x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2666  

(End of data from sysinfo program)

**Compiler Version Notes**

==============================================================================  
CC  519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)  
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.2.187 Build 20190117  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
==============================================================================  
CXXC 508.namd_r(base) 510.parest_r(base)  
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.2.187 Build 20190117  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
==============================================================================  
CC  511.povray_r(base) 526.blender_r(base)  
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.2.187 Build 20190117  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.2.187 Build 20190117  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
==============================================================================  
FC  507.cactuBSSN_r(base)  
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.2.187 Build 20190117  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.2.187 Build 20190117  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
**SPEC CPU2017 Floating Point Rate Result**

**Hewlett Packard Enterprise**  
(Procortor: HPE)  
ProLiant DL560 Gen10  
(2.50 GHz, Intel Xeon Gold 5215M)

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base =</th>
<th>249</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_peak =</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Apr-2019  
**Hardware Availability:** Apr-2019  
**Software Availability:** Feb-2019

**Compiler Version Notes (Continued)**

Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel (R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

---

FC 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)

---

Intel (R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

---

CC 521.wrf_r(base) 527.cam4_r(base)

---

Intel (R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

---

**Base Compiler Invocation**

C benchmarks:  
icc -m64 -std=c11

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:  
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:  
icpc -m64 icc -m64 -std=c11 ifort -m64
Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL560 Gen10
(2.50 GHz, Intel Xeon Gold 5215M)

SPECrate2017_fp_base = 249
SPECrate2017_fp_peak = Not Run

Test Date: Apr-2019
Hardware Availability: Apr-2019
Software Availability: Feb-2019

Base Portability Flags
503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags
C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte
SPEC CPU2017 Floating Point Rate Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL560 Gen10
(2.50 GHz, Intel Xeon Gold 5215M)

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_base</td>
<td>249</td>
</tr>
<tr>
<td>SPECrate2017_fp_peak</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Hewlett Packard Enterprise
(2.50 GHz, Intel Xeon Gold 5215M)

Test Date: Apr-2019
Hardware Availability: Apr-2019
Software Availability: Feb-2019

The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2019-04-03.xml
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revA.xml

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-04-12 10:23:20-0400.