## Lenovo Global Technology

### ThinkSystem ST550
(3.80 GHz, Intel Xeon Platinum 8256)

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>16</td>
<td>49.8</td>
<td>Not Run</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>16</td>
<td>49.8</td>
<td>0</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>16</td>
<td>45.9</td>
<td>0</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>16</td>
<td>59.4</td>
<td>0</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>16</td>
<td>72.3</td>
<td>0</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>16</td>
<td>45.0</td>
<td>0</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>16</td>
<td>88.0</td>
<td>0</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>16</td>
<td>67.9</td>
<td>0</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>16</td>
<td>76.5</td>
<td>0</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>16</td>
<td>83.4</td>
<td>0</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>16</td>
<td>49.8</td>
<td>0</td>
</tr>
<tr>
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<td>16</td>
<td>83.4</td>
<td>0</td>
</tr>
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</table>

### Hardware
- **CPU Name:** Intel Xeon Platinum 8256
- **Max MHz.:** 3900
- **Nominal:** 3800
- **Enabled:** 8 cores, 2 chips, 2 threads/core
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 16.5 MB I+D on chip per chip
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)
- **Storage:** 1 x 480 GB SATA SSD
- **Other:** None

### Software
- **OS:** SUSE Linux Enterprise Server 15 (x86_64)
- **Kernel:** 4.12.14-25.13-default
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++
- **Compiler Build:** 20181018 for Linux;
  Fortran: Version 19.0.1.144 of Intel Fortran
- **Compiler Build:** 20181018 for Linux
- **Parallel:** No
- **Firmware:** Lenovo BIOS Version O0E135M 2.10 released Jan-2019
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** Not Applicable
- **Other:** None
Lenovo Global Technology

ThinkSystem ST550
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SPECrate2017_fp_base = 77.4
SPECrate2017_fp_peak = Not Run

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>16</td>
<td>566</td>
<td>284</td>
<td>561</td>
<td>286</td>
<td>565</td>
<td>284</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>16</td>
<td>407</td>
<td>49.8</td>
<td>407</td>
<td>49.8</td>
<td>407</td>
<td>49.7</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>16</td>
<td>329</td>
<td>46.2</td>
<td>331</td>
<td>45.9</td>
<td>332</td>
<td>45.8</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>16</td>
<td>704</td>
<td>59.5</td>
<td>704</td>
<td>59.4</td>
<td>705</td>
<td>59.4</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>16</td>
<td>517</td>
<td>72.3</td>
<td>517</td>
<td>72.2</td>
<td>515</td>
<td>72.5</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>16</td>
<td>375</td>
<td>45.0</td>
<td>374</td>
<td>45.1</td>
<td>375</td>
<td>45.0</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>16</td>
<td>407</td>
<td>88.1</td>
<td>407</td>
<td>88.0</td>
<td>411</td>
<td>87.2</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>16</td>
<td>359</td>
<td>67.9</td>
<td>358</td>
<td>68.0</td>
<td>359</td>
<td>67.9</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>16</td>
<td>366</td>
<td>76.5</td>
<td>365</td>
<td>76.6</td>
<td>371</td>
<td>75.5</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>16</td>
<td>264</td>
<td>150</td>
<td>264</td>
<td>151</td>
<td>264</td>
<td>151</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>16</td>
<td>261</td>
<td>103</td>
<td>262</td>
<td>103</td>
<td>262</td>
<td>103</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>16</td>
<td>766</td>
<td>81.4</td>
<td>761</td>
<td>82.0</td>
<td>769</td>
<td>81.1</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>16</td>
<td>509</td>
<td>49.9</td>
<td>510</td>
<td>49.8</td>
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</tr>
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SPECrate2017_fp_base = 77.4
SPECrate2017_fp_peak = Not Run

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0u1/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numacl i.e.:
  numacl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Lenovo Global Technology
ThinkSystem ST550
(3.80 GHz, Intel Xeon Platinum 8256)

SPECrater2017_fp_base = 77.4
SPECrater2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

General Notes (Continued)
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Choose Operating Mode set to Custom Mode
LLC dead line alloc set to Disable
SNC set to Enable
Sysinfo program /home/cpu2017-1.0.5-ic19.0u1/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-pork Wed Apr 17 02:54:19 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8256 CPU @ 3.80GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 4
  siblings : 8
  physical 0: cores 1 5 9 13
  physical 1: cores 1 2 5 13

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 16
On-line CPU(s) list: 0-15
Thread(s) per core: 2
Core(s) per socket: 4
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel

(Continued on next page)
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SPECrated2017_fp_base = 77.4
SPECrated2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Apr-2019
Hardware Availability: Apr-2019
Software Availability: Nov-2018

Platform Notes (Continued)

CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Platinum 8256 CPU @ 3.80GHz
Stepping: 6
CPU MHz: 3800.000
CPU max MHz: 3900.0000
CPU min MHz: 1200.0000
BogoMIPS: 7600.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 16896K
NUMA node0 CPU(s): 0,2,8,10
NUMA node1 CPU(s): 1,3,9,11
NUMA node2 CPU(s): 4,5,12,13
NUMA node3 CPU(s): 6,7,14,15
Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdelgb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmpref pi pclmulqdq dtes64 ds_cpl vmx smx est tm2 sse3 sdbg fma cx16 xtpr pdcm pcid dca se4_l se4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512v1 xsaveopt xsave xsavec xsaveopt xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local dtherm ida arat pln pts pku ospke avx512_vnni flush_lld arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 2 8 10
node 0 size: 96340 MB
node 0 free: 96069 MB
node 1 cpus: 1 3 9 11
node 1 size: 96756 MB
node 1 free: 93531 MB
node 2 cpus: 4 5 12 13
node 2 size: 96756 MB
node 2 free: 96471 MB
node 3 cpus: 6 7 14 15
node 3 size: 96754 MB
node 3 free: 96553 MB

(Continued on next page)
Platform Notes (Continued)

node distances:
node  0  1  2  3
0:  10  11  21  21
1:  11  10  21  21
2:  21  21  10  11
3:  21  21  11  10

From /proc/meminfo
MemTotal:       395886724 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release*/etc/*version*
  os-release:
    NAME="SLES"
    VERSION="15"
    VERSION_ID="15"
    PRETTY_NAME="SUSE Linux Enterprise Server 15"
    ID="sles"
    ID_LIKE="suse"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
    x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 Apr 17 02:52

SPEC is set to: /home/cpu2017-1.0.5-ic19.0u1
  Filesystem Type Size Used Avail Use% Mounted on
  /dev/sdc3     xfs   445G  43G  403G  10% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS Lenovo -[00E135M-2.10]- 01/16/2019
  Memory:
    12x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

(Continued on next page)
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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology
Test Date: Apr-2019
Hardware Availability: Apr-2019
Software Availability: Nov-2018

Platform Notes (Continued)

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CXXC 508.namd_r(base) 510.parest_r(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CC  511.povray_r(base) 526.blender_r(base)
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Compiler Version Notes (Continued)

FC  503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
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Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
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Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
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Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64

(Continued on next page)
Lenovo Global Technology
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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Base Portability Flags (Continued)

508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
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</tr>
</tbody>
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<th>9017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Lenovo Global Technology</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Lenovo Global Technology</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Apr-2019</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Apr-2019</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Nov-2018</td>
</tr>
</tbody>
</table>

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-A.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-A.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.