Lenovo Global Technology
ThinkSystem SR650
(3.80 GHz, Intel Xeon Platinum 8256)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>77.5</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_peak</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

Test Date: Apr-2019
Hardware Availability: Apr-2019
Software Availability: Nov-2018

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base (77.5)</th>
</tr>
</thead>
</table>

### Hardware

- **CPU Name:** Intel Xeon Platinum 8256
- **Max MHz.:** 3900
- **Nominal:** 3800
- **Enabled:** 8 cores, 2 chips, 2 threads/core
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 16.5 MB I+D on chip per chip
- **Memory:** 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R)
- **Storage:** 1 x 800 GB SATA SSD
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux Server release 7.6 (Maipo)
- **Kernel:** 3.10.0-957.el7.x86_64
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++ Compiler Build 20181018 for Linux;
  Fortran: Version 19.0.1.144 of Intel Fortran Compiler Build 20181018 for Linux
- **Parallel:** No
- **Firmware:** Lenovo BIOS Version IVE135R 2.10 released Feb-2019
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** Not Applicable
- **Other:** None
SPECFP2017 Floating Point Rate Result

Lenovo Global Technology
ThinkSystem SR650
(3.80 GHz, Intel Xeon Platinum 8256)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

SPECRate2017_fp_base = 77.5
SPECRate2017_fp_peak = Not Run

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>16</td>
<td>573</td>
<td>280</td>
<td>573</td>
<td>280</td>
<td>574</td>
<td>280</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>16</td>
<td>414</td>
<td>48.9</td>
<td>413</td>
<td>49.0</td>
<td>414</td>
<td>49.0</td>
</tr>
<tr>
<td>508.namd</td>
<td>16</td>
<td>333</td>
<td>45.6</td>
<td>333</td>
<td>45.6</td>
<td>332</td>
<td>45.7</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>16</td>
<td>707</td>
<td>59.2</td>
<td>709</td>
<td>59.0</td>
<td>709</td>
<td>59.0</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>16</td>
<td>517</td>
<td>72.3</td>
<td>516</td>
<td>72.5</td>
<td>516</td>
<td>72.5</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>16</td>
<td>370</td>
<td>45.6</td>
<td>370</td>
<td>45.6</td>
<td>369</td>
<td>45.7</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>16</td>
<td>409</td>
<td>87.7</td>
<td>408</td>
<td>87.6</td>
<td>408</td>
<td>87.9</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>16</td>
<td>359</td>
<td>67.8</td>
<td>360</td>
<td>67.7</td>
<td>361</td>
<td>67.6</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>16</td>
<td>359</td>
<td>77.9</td>
<td>364</td>
<td>77.0</td>
<td>359</td>
<td>78.0</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>16</td>
<td>264</td>
<td>151</td>
<td>264</td>
<td>150</td>
<td>262</td>
<td>152</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>16</td>
<td>264</td>
<td>102</td>
<td>260</td>
<td>103</td>
<td>262</td>
<td>103</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>16</td>
<td>751</td>
<td>83.1</td>
<td>749</td>
<td>83.2</td>
<td>755</td>
<td>82.6</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>16</td>
<td>506</td>
<td>50.3</td>
<td>509</td>
<td>50.0</td>
<td>509</td>
<td>49.9</td>
</tr>
</tbody>
</table>

SPECRate2017_fp_base = 77.5
SPECRate2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0u1/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650
(3.80 GHz, Intel Xeon Platinum 8256)

SPECrate2017_fp_base = 77.5
SPECrate2017_fp_peak = Not Run

CPU2017 License: 9017
Test Date: Apr-2019
Test Sponsor: Lenovo Global Technology
Hardware Availability: Apr-2019
Tested by: Lenovo Global Technology
Software Availability: Nov-2018

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Choose Operating Mode set to Custom Mode
C-states set to Legacy
SNC set to Enable
DCU Streamer Prefetcher set to Disable
Trusted Execution Technology set to Enable
Stale AtoS set to Enable
LLC dead line alloc set to Disable
Patrol Scrub set to Disable
Sysinfo program /home/cpu2017-1.0.5-ic19.0u1/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985ea45859ea9
running on localhost.localdomain Fri Apr 19 18:05:40 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8256 CPU @ 3.80GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 8
  physical 0: cores 1 5 9 13
  physical 1: cores 1 2 5 13

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 16
On-line CPU(s) list: 0-15

(Continued on next page)
**Platform Notes (Continued)**

- **Thread(s) per core:** 2
- **Core(s) per socket:** 4
- **Socket(s):** 2
- **NUMA node(s):** 4
- **Vendor ID:** GenuineIntel
- **CPU family:** 6
- **Model:** 85
- **Model name:** Intel(R) Xeon(R) Platinum 8256 CPU @ 3.80GHz
- **Stepping:** 6
- **CPU MHz:** 3800.000
- **BogoMIPS:** 7600.00
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 1024K
- **L3 cache:** 16896K
- **NUMA node0 CPU(s):** 0, 2, 8, 10
- **NUMA node1 CPU(s):** 1, 3, 9, 11
- **NUMA node2 CPU(s):** 4, 5, 12, 13
- **NUMA node3 CPU(s):** 6, 7, 14, 15
- **Flags:** fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpl mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ebpx cat_l3 cdp_13 intei_pt ssbd mba ibrs ibpb stibp ibrs_enhanced trp_shadow vnni flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 ertz invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xsaveprec qmm_112 qcm_occup_llc qcm_mbb_total qcm_mbb_local dtherm ida arat pln pts pku ospke avx512_vnni spec_ctrl intel_stibp flush_l1d arch_capabilities

/proc/cpuinfo cache data

    cache size : 16896 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
Lenovo Global Technology
ThinkSystem SR650
(3.80 GHz, Intel Xeon Platinum 8256)

<table>
<thead>
<tr>
<th>CPU2017 License: 9017</th>
<th>Test Date: Apr-2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Lenovo Global Technology</td>
<td>Hardware Availability: Apr-2019</td>
</tr>
<tr>
<td>Tested by: Lenovo Global Technology</td>
<td>Software Availability: Nov-2018</td>
</tr>
</tbody>
</table>

**SPECrate2017_fp_base = 77.5**

**SPECrate2017_fp_peak = Not Run**

Platform Notes (Continued)

```
node 3 cpus: 6 7 14 15
node 3 size: 196608 MB
node 3 free: 192199 MB
node distances:
node   0   1   2   3
0: 10 11 21 21
1: 11 10 21 21
2: 21 21 10 11
3: 21 21 11 10
```

```
From /proc/meminfo
 MemTotal: 792179288 kB
 HugePages_Total: 0
 Hugepagesize: 2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
 NAME="Red Hat Enterprise Linux Server"
 VERSION="7.6 (Maipo)"
 ID="rhel"
 ID_LIKE="fedora"
 VARIANT="Server"
 VARIANT_ID="server"
 VERSION_ID="7.6"
 PRETTY_NAME="Red Hat Enterprise Linux Server 7.6 (Maipo)"
 redhat-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)
 system-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)
```

```
uname -a:
 Linux localhost.localdomain 3.10.0-957.el7.x86_64 #1 SMP Thu Oct 4 20:48:51 UTC 2018
 x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS
```

```
run-level 3 Apr 19 18:02
```

```
SPEC is set to: /home/cpu2017-1.0.5-ic19.0u1
 Filesystem Type Size Used Avail Use% Mounted on
 /dev/sdb2 xfs 689G 116G 573G 17% /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow (Continued on next page)
Lenovo Global Technology
ThinkSystem SR650
(3.80 GHz, Intel Xeon Platinum 8256)

SPECRate2017_fp_base = 77.5
SPECRate2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: Apr-2019
Tested by: Lenovo Global Technology
Hardware Availability: Apr-2019
Software Availability: Nov-2018

Platform Notes (Continued)

hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[IVE135R-2.10]- 02/27/2019
Memory:
24x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

=================================================================================
CC  519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
=================================================================================
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=================================================================================
CXXC 508.namd_r(base) 510.parest_r(base)
=================================================================================
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=================================================================================
CC  511.povray_r(base) 526.blender_r(base)
=================================================================================
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=================================================================================
FC  507.cactusBSSN_r(base)
=================================================================================
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650
(3.80 GHz, Intel Xeon Platinum 8256)

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>9017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor</td>
<td>Lenovo Global Technology</td>
</tr>
<tr>
<td>Tested by</td>
<td>Lenovo Global Technology</td>
</tr>
</tbody>
</table>

SPECrate2017_fp_base = 77.5
SPECrate2017_fp_peak = Not Run

Compiler Version Notes (Continued)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Compiler Version Notes (Continued)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
    icc -m64 -std=c11

C++ benchmarks:
    icpc -m64

Fortran benchmarks:
    ifort -m64

Benchmarks using both Fortran and C:
    ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
    icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
    icpc -m64 icc -m64 -std=c11 ifort -m64
Lenovo Global Technology
ThinkSystem SR650
(3.80 GHz, Intel Xeon Platinum 8256)

SPECrate2017_fp_base = 77.5
SPECrate2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: Apr-2019
Tested by: Lenovo Global Technology
Hardware Availability: Apr-2019
Software Availability: Nov-2018

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte
## Lenovo Global Technology

**ThinkSystem SR650**  
(3.80 GHz, Intel Xeon Platinum 8256)

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base =</th>
<th>77.5</th>
<th>SPECrate2017_fp_peak =</th>
<th>Not Run</th>
</tr>
</thead>
</table>

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>9017</th>
<th>Test Date:</th>
<th>Apr-2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Lenovo Global Technology</td>
<td>Hardware Availability:</td>
<td>Apr-2019</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Lenovo Global Technology</td>
<td>Software Availability:</td>
<td>Nov-2018</td>
</tr>
</tbody>
</table>

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


---

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-04-19 06:05:39-0400.  