Lenovo Global Technology
ThinkSystem SR530
(2.20 GHz, Intel Xeon Silver 4210)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Hardware
CPU Name: Intel Xeon Silver 4210
Max MHz.: 3200
Nominal: 2200
Enabled: 20 cores, 2 chips
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 13.75 MB I+D on chip per chip
Other: None
Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2933Y-R, running at 2400)
Storage: 1 x 960 GB SATA SSD
Other: None

Software
OS: SUSE Linux Enterprise Server 12 SP4 (x86_64)
Kernel 4.12.14-94.41-default
Compiler: C/C++: Version 19.0.1.144 of Intel C/C++
Compiler Build 20181018 for Linux;
Fortran: Version 19.0.1.144 of Intel Fortran
Compiler Build 20181018 for Linux
Parallel: Yes
Firmware: Lenovo BIOS Version TEE135L 2.10 released Jan-2019
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: None

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>89.9</td>
<td>Not Run</td>
</tr>
<tr>
<td>20</td>
<td>69.6</td>
<td>107</td>
</tr>
<tr>
<td>20</td>
<td>74.3</td>
<td></td>
</tr>
<tr>
<td>20</td>
<td>46.3</td>
<td></td>
</tr>
<tr>
<td>20</td>
<td>54.8</td>
<td></td>
</tr>
<tr>
<td>20</td>
<td>58.7</td>
<td></td>
</tr>
<tr>
<td>20</td>
<td>107</td>
<td></td>
</tr>
<tr>
<td>20</td>
<td>65.4</td>
<td></td>
</tr>
<tr>
<td>20</td>
<td>73.0</td>
<td></td>
</tr>
</tbody>
</table>
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds Base</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds Peak</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>20</td>
<td>168</td>
<td>352</td>
<td>168</td>
<td>352</td>
<td>167</td>
<td>353</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>20</td>
<td>185</td>
<td>90.0</td>
<td>187</td>
<td>89.3</td>
<td>185</td>
<td>89.9</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>20</td>
<td>75.3</td>
<td>69.6</td>
<td>75.2</td>
<td>69.6</td>
<td>75.4</td>
<td>69.5</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>20</td>
<td>76</td>
<td>75.1</td>
<td>178</td>
<td>74.3</td>
<td>178</td>
<td>74.2</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>20</td>
<td>192</td>
<td>46.3</td>
<td>191</td>
<td>46.3</td>
<td>192</td>
<td>46.3</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>20</td>
<td>217</td>
<td>54.8</td>
<td>215</td>
<td>55.2</td>
<td>218</td>
<td>54.5</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>20</td>
<td>245</td>
<td>59.0</td>
<td>246</td>
<td>58.6</td>
<td>246</td>
<td>58.7</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>20</td>
<td>164</td>
<td>107</td>
<td>163</td>
<td>107</td>
<td>163</td>
<td>107</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>20</td>
<td>139</td>
<td>65.4</td>
<td>139</td>
<td>65.7</td>
<td>140</td>
<td>65.3</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>20</td>
<td>215</td>
<td>73.2</td>
<td>216</td>
<td>73.0</td>
<td>216</td>
<td>73.0</td>
</tr>
</tbody>
</table>

SPECspeed2017_fp_base = 81.2  
SPECspeed2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0u1/lib/intel64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.
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**SPECspeed2017_fp_base** = 81.2
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**Platform Notes**

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Choose Operating Mode set to Custom Mode
MONITOR/MWAIT set to Enable
Hyper-Threading set to Disable

Sysinfo program /home/cpu2017-1.0.5-ic19.0u1/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-zitb Wed Apr 24 18:09:33 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4210 CPU @ 2.20GHz
  2 "physical id"s (chips)
  20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 10
  siblings : 10
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 20
On-line CPU(s) list: 0-19
Thread(s) per core: 1
Core(s) per socket: 10
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4210 CPU @ 2.20GHz
Stepping: 6
CPU MHz: 2200.000
CPU max MHz: 3200.0000
CPU min MHz: 1000.0000
BogoMIPS: 4400.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K

(Continued on next page)
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### Platform Notes (Continued)

- **L3 cache:** 14080K
- **NUMA node0 CPU(s):** 0-9
- **NUMA node1 CPU(s):** 10-19

**Flags:**
- fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
- pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
- lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
- aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
- xtr.Preference pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
- xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
- ipv4 Visibility invpcid_single ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid
- fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 ibrms invpcid rtm cgq mpx rdt_a avx512f
- avx512dq rseed adx clflushopt clwb intel_pt avx512cd avx512bw avx512vl
- xsaveopt xsave xstore xsetbv xsavec xsaveopt xsaveopt xgetbv1 xsavec cgq_llc cgq_occupy_llc
- cgq_mbm_total cgq_mbm_local dtherm ida arat pin pts pku ospke avx512_vnni flush_l1d arch_capabilities

From /proc/cpuinfo cache data
- cache size: 14080 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
- available: 2 nodes (0-1)
- node 0 cpus: 0 1 2 3 4 5 6 7 8 9
- node 0 size: 96319 MB
- node 0 free: 95723 MB
- node 1 cpus: 10 11 12 13 14 15 16 17 18 19
- node 1 size: 96742 MB
- node 1 free: 96420 MB
- node distances:
  - node 0: 10 21
  - node 1: 21 10

From /proc/meminfo
- MemTotal: 197696016 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
- SuSE-release:
  - SUSE Linux Enterprise Server 12 (x86_64)
  - VERSION = 12
  - PATCHLEVEL = 4
  - # This file is deprecated and will be removed in a future service pack or release.
  - # Please check /etc/os-release for details about this release.
- os-release:
  - NAME="SLES"
  - VERSION="12-SP4"
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Platform Notes (Continued)

VERSION_ID="12.4"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP4"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp4"

uname -a:
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 Apr 24 18:08

SPEC is set to: /home/cpu2017-1.0.5-ic19.0u1

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[TEE135L-2.10]- 01/10/2019
Memory:
12x SK Hynix HMA82GR7CJR8N-WM 16 GB 2 rank 2933, configured at 2400

Compiler Version Notes

==============================================================================
CC  619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

FC  607.cactuBSSN_s(base)

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Compiler Version Notes (Continued)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64
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| Test Date:       | Apr-2019 |
| Hardware Availability: | Apr-2019 |
| Software Availability: | Dec-2018 |

### Base Portability Flags

- 603.bwaves_s: -DSPEC_LP64  
- 607.cactuBSSN_s: -DSPEC_LP64  
- 619.lbm_s: -DSPEC_LP64  
- 621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian  
- 627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG  
- 628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl  
- 638.imagick_s: -DSPEC_LP64  
- 644.nab_s: -DSPEC_LP64  
- 649.fotonik3d_s: -DSPEC_LP64  
- 654.roms_s: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp

**Fortran benchmarks:**

-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp  
-nostandard-realloc-lhs

**Benchmarks using both Fortran and C:**

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP  
-nostandard-realloc-lhs

**Benchmarks using Fortran, C, and C++:**

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP  
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

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</tr>
</tbody>
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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-04-24 06:09:32-0400.