**Lenovo Global Technology**

**ThinkSystem SR570**
(1.90 GHz, Intel Xeon Bronze 3204)

### SPECspeed2017_fp_base = 43.9

### SPECspeed2017_fp_peak = Not Run

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed2017_fp_base</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>48.4</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>48.4</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>35.1</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>42.7</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>20.0</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>33.9</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>25.9</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>49.0</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>44.8</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>42.0</td>
</tr>
</tbody>
</table>

---

**Hardware**

- **CPU Name:** Intel Xeon Bronze 3204
- **Max MHz.:** 1900
- **Nominal:** 1900
- **Enabled:** 12 cores, 2 chips
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 8.25 MB I+D on chip per chip
- **Memory:** 192 GB (12 x 16 GB 2Rx8 PC4-2933Y-R, running at 2133)
- **Storage:** 1 x 960 GB SATA SSD
- **Other:** None

**Software**

- **OS:** SUSE Linux Enterprise Server 12 SP4 (x86_64)
- **Kernel:** 4.12.14-94.41-default
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++ Compiler Build 20181018 for Linux;
  Fortran: Version 19.0.1.144 of Intel Fortran Compiler Build 20181018 for Linux
- **Parallel:** Yes
- **Firmware:** Lenovo BIOS Version TEE135L 2.10 released Jan-2019
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** Not Applicable
- **Other:** None
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SPEC Speed2017_fp_base = 43.9
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>12</td>
<td>260</td>
<td>227</td>
<td>260</td>
<td>227</td>
<td>260</td>
<td>227</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>12</td>
<td>343</td>
<td>48.5</td>
<td>345</td>
<td>48.4</td>
<td>345</td>
<td>48.4</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>12</td>
<td>149</td>
<td>35.2</td>
<td>149</td>
<td>35.1</td>
<td>149</td>
<td>35.1</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>12</td>
<td>310</td>
<td>42.7</td>
<td>309</td>
<td>42.9</td>
<td>310</td>
<td>42.7</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>12</td>
<td>444</td>
<td>20.0</td>
<td>444</td>
<td>20.0</td>
<td>444</td>
<td>20.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>12</td>
<td>350</td>
<td>33.9</td>
<td>351</td>
<td>33.8</td>
<td>350</td>
<td>34.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>12</td>
<td>556</td>
<td>26.0</td>
<td>557</td>
<td>25.9</td>
<td>557</td>
<td>25.9</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>644.nab_s</td>
<td>12</td>
<td>356</td>
<td>49.1</td>
<td>356</td>
<td>49.0</td>
<td>357</td>
<td>49.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>12</td>
<td>205</td>
<td>44.5</td>
<td>203</td>
<td>44.8</td>
<td>203</td>
<td>45.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>654.roms_s</td>
<td>12</td>
<td>375</td>
<td>42.0</td>
<td>376</td>
<td>41.9</td>
<td>375</td>
<td>42.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity= fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0u1/lib/intel64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
FileSystem page cache synced and cleared with:
    sync; echo 3> /proc/sys/vm/drop_caches
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.
**Platform Notes**

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Choose Operating Mode set to Custom Mode
CPU P-state Control set to Automatic
MONITOR/MWAIT set to Enable
Adjacent Cache Prefetch set to Disable
Sysinfo program /home/cpu2017-1.0.5-ic19.0u1/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-et90 Tue Apr 23 09:29:20 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Bronze 3204 CPU @ 1.90GHz
  2  "physical id"s (chips)
 12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 6
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 12
On-line CPU(s) list: 0-11
Thread(s) per core: 1
Core(s) per socket: 6
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Bronze 3204 CPU @ 1.90GHz
Stepping: 6
CPU MHz: 1900.000
BogoMIPS: 3800.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 8448K
```

(Continued on next page)
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SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

SPEC speed2017_fp_base = 43.9
SPEC speed2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

NUMA node0 CPU(s): 0-5
NUMA node1 CPU(s): 6-11
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cdp_13
invpcid_single ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid
fsgsbased tsc_adjust bmi1 hle avx2 smep bmi2 ets invpcid rtm cqm mxp rdt_a avx512f
avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local
dtherm arat pln pts hwp_epp pku ospke avx512_vnni flush_l1d arch_capabilities

/platform_notes

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5
node 0 size: 96034 MB
node 0 free: 95482 MB
node 1 cpus: 6 7 8 9 10 11
node 1 size: 96743 MB
node 1 free: 96413 MB
node distances:
node 0 1
 0: 10 21
 1: 21 10

From /proc/meminfo
MemTotal: 197404716 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 4
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP4"
VERSION_ID="12.4"

(Continued on next page)
**Platform Notes (Continued)**

PRETTY_NAME="SUSE Linux Enterprise Server 12 SP4"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp4"

uname -a:
    x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 Apr 23 09:22

SPEC is set to: /home/cpu2017-1.0.5-ic19.0u1
    Filesystem     Type  Size  Used Avail Use% Mounted on
    /dev/sda3      xfs   892G   35G  857G   4% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[TEE135L-2.10]- 01/10/2019
Memory:
    4x NO DIMM NO DIMM
    12x SK Hynix HMA82GR7CJR8N-WM 16 GB 2 rank 2933, configured at 2133

(End of data from sysinfo program)

**Compiler Version Notes**

==============================================================================
CC  619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
    Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================

FC  607.cactusBSSN_s(base)
(Continued on next page)
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SPECspeed2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: Apr-2019
Tested by: Lenovo Global Technology
Hardware Availability: Apr-2019
Software Availability: Dec-2018

Compiler Version Notes (Continued)

Intel(R) C++ Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Intel(R) C Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Intel(R) Fortran Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64
# Lenovo Global Technology

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</tr>
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<td>Tested by</td>
<td>Lenovo Global Technology</td>
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</tbody>
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<thead>
<tr>
<th>Test Date</th>
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</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability</td>
<td>Apr-2019</td>
</tr>
<tr>
<td>Software Availability</td>
<td>Dec-2018</td>
</tr>
</tbody>
</table>

## Base Portability Flags

- 603.bwaves_s: -DSPEC_LP64
- 607.cactuBSSN_s: -DSPEC_LP64
- 619.libm_s: -DSPEC_LP64
- 621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- assume byterecl
- 638.imagick_s: -DSPEC_LP64
- 644.nab_s: -DSPEC_LP64
- 649.fotonik3d_s: -DSPEC_LP64
- 654.roms_s: -DSPEC_LP64

## Base Optimization Flags

### C benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP

### Fortran benchmarks:

-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp  
-nostandard-realloc-lhs

### Benchmarks using both Fortran and C:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP  
-nostandard-realloc-lhs

### Benchmarks using Fortran, C, and C++:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP  
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

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**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Apr-2019  
**Hardware Availability:** Apr-2019  
**Software Availability:** Dec-2018

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Tested with SPEC CPU2017 v1.0.5 on 2019-04-22 21:29:18-0400.  