# SPEC® CPU2017 Integer Rate Result

**Lenovo Global Technology**

ThinkSystem SR570  
(2.20 GHz, Intel Xeon Silver 4209T)

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**SPECrater2017_int_base = 82.9**

**SPECrater2017_int_peak = Not Run**

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### Hardware

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### Software

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<td>OS:</td>
<td>SUSE Linux Enterprise Server 12 SP4 (x86_64)</td>
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<td>Kernel:</td>
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### CPU2017 License

Lenovo Global Technology

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**CPU2017 License:** 9017

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**Test Date:** Apr-2019

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**Test Sponsor:** Lenovo Global Technology

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**Hardware Availability:** Apr-2019

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**Tested by:** Lenovo Global Technology

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**Software Availability:** Dec-2018

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**Hardware Name:** Intel Xeon Silver 4209T

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**Max MHz.:** 3200

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**Nominal:** 2200

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**Enabled:** 16 cores, 2 chips, 2 threads/core

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**Orderable:** 1,2 chips

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**Cache L1:** 32 KB I + 32 KB D on chip per core

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**L2:** 1 MB I+D on chip per core

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**L3:** 11 MB I+D on chip per chip

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**Other:** None

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**Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R, running at 2400)

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**Storage:** 1 x 960 GB SATA SSD

---

**Other:** None
Lenovo Global Technology
ThinkSystem SR570
(2.20 GHz, Intel Xeon Silver 4209T)

SPECrates

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<td>631</td>
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</table>

Results Table

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0u1/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Files system page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

(Continued on next page)
General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Choose Operating Mode set to Custom Mode
MONITOR/MWAIT set to Enable
Sysinfo program /home/cpu2017-1.0.5-ic19.0u1/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-dl3d Thu Apr 18 18:17:33 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Silver 4209T CPU @ 2.20GHz
   2 "physical id"s (chips)
   32 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
   siblings : 16
   physical 0: cores 0 1 2 3 4 5 6 7
   physical 1: cores 0 1 2 3 4 5 6 7
```

From lscpu:

```
Architecture:           x86_64
CPU op-mode(s):         32-bit, 64-bit
Byte Order:             Little Endian
CPU(s):                 32
On-line CPU(s) list:    0-31
Thread(s) per core:     2
Core(s) per socket:     8
Socket(s):              2
NUMA node(s):           2
Vendor ID:              GenuineIntel
CPU family:             6
Model:                  85
Model name:             Intel(R) Xeon(R) Silver 4209T CPU @ 2.20GHz
Stepping:               6
```
Lenovo Global Technology
ThinkSystem SR570
(2.20 GHz, Intel Xeon Silver 4209T)
**SPEC CPU2017 Integer Rate Result**

**Lenovo Global Technology**

ThinkSystem SR570  
(2.20 GHz, Intel Xeon Silver 4209T)

CPU2017 License: 9017  
Test Sponsor: Lenovo Global Technology  
Tested by: Lenovo Global Technology

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
<th>82.9</th>
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<tbody>
<tr>
<td>SPECrate2017_int_peak</td>
<td>Not Run</td>
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</tbody>
</table>

**Platform Notes (Continued)**

SUSE Linux Enterprise Server 12 (x86_64)  
VERSION = 12  
PATCHLEVEL = 4  
# This file is deprecated and will be removed in a future service pack or release.  
# Please check /etc/os-release for details about this release.

```
os-release:
  NAME  = "SLES"
  VERSION  = "12-SP4"
  VERSION_ID  = "12.4"
  PRETTY_NAME  = "SUSE Linux Enterprise Server 12 SP4"
  ID  = "sles"
  ANSI_COLOR  = "0;32"
  CPE_NAME  = "cpe:/o:suse:sles:12:sp4"
```

```
uname -a:
  x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

- CVE-2017-5754 (Meltdown): Not affected
- CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
- CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

```
run-level 3 Apr 18 18:15
SPEC is set to: /home/cpu2017-1.0.5-ic19.0u1
  Filesystem  Type  Size  Used Avail Use% Mounted on
  /dev/sda3  xfs  892G  31G  861G   4% /
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS Lenovo -[TEE135L-2.10]− 01/10/2019
Memory:
  4x NO DIMM NO DIMM
  12x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933, configured at 2400
```

(End of data from sysinfo program)

**Compiler Version Notes**

```
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
```

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR570
(2.20 GHz, Intel Xeon Silver 4209T)

SPECrate2017_int_base = 82.9
SPECrate2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Apr-2019
Hardware Availability: Apr-2019
Software Availability: Dec-2018

Compiler Version Notes (Continued)

557.xz_r(base)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
541.leela_r(base)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

FC 548.exchange2_r(base)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR570
(2.20 GHz, Intel Xeon Silver 4209T)

SPECrate2017_int_base = 82.9
SPECrate2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Apr-2019
Hardware Availability: Apr-2019
Software Availability: Dec-2018

Base Portability Flags (Continued)
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags
C benchmarks:
- Wl, -z, multidefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4
- L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
- lqkmalloc

C++ benchmarks:
- Wl, -z, multidefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4
- L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
- lqkmalloc

Fortran benchmarks:
- Wl, -z, multidefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
- L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
- lqkmalloc

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-A.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-A.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-04-18 06:17:32-0400.