### SPEC® CPU2017 Integer Speed Result

**Huawei**

**Huawei 2288H V5 (Intel Xeon Gold 6130)**

<table>
<thead>
<tr>
<th>Specspeed2017_int_base</th>
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<tbody>
<tr>
<td>Specspeed2017_int_peak</td>
<td>9.91</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei  
**Test Date:** Apr-2019  
**Hardware Availability:** Jul-2017  
**Software Availability:** Dec-2018

#### Hardware

- **CPU Name:** Intel Xeon Gold 6130  
- **Max MHz.:** 3700  
- **Nominal:** 2100  
- **Enabled:** 32 cores, 2 chips  
- **Orderable:** 1,2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 22 MB I+D on chip per chip  
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)  
- **Storage:** 1 x 900 GB SATA SSD  
- **Other:** None

#### Software

- **OS:** SUSE Linux Enterprise Server 12 SP4 (x86_64)  
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++ Compiler Build 20181018 for Linux; Fortran: Version 19.0.1.144 of Intel Fortran Compiler Build 20181018 for Linux  
- **Parallel:** Yes  
- **Firmware:** Version 6.36 Released Feb-2019  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator V5.0.1

#### Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench</td>
<td>32</td>
<td>6.47</td>
<td>9.89</td>
</tr>
<tr>
<td>gcc</td>
<td>32</td>
<td>7.64</td>
<td>10.2</td>
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<tr>
<td>mcf</td>
<td>32</td>
<td>7.58</td>
<td>12.6</td>
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<tr>
<td>omnetpp</td>
<td>32</td>
<td>7.59</td>
<td>12.7</td>
</tr>
<tr>
<td>xalancbmk</td>
<td>32</td>
<td>11.9</td>
<td>12.0</td>
</tr>
<tr>
<td>x264</td>
<td>32</td>
<td>13.9</td>
<td>13.9</td>
</tr>
<tr>
<td>deepsjeng</td>
<td>32</td>
<td>5.32</td>
<td>6.47</td>
</tr>
<tr>
<td>leela</td>
<td>32</td>
<td>4.54</td>
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<tr>
<td>exchange2</td>
<td>32</td>
<td>13.3</td>
<td>13.3</td>
</tr>
<tr>
<td>xz</td>
<td>32</td>
<td>22.4</td>
<td>22.5</td>
</tr>
</tbody>
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### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>32</td>
<td>274</td>
<td>6.48</td>
<td>274</td>
<td>6.47</td>
<td>274</td>
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<tr>
<td>602.gcc_s</td>
<td>32</td>
<td>402</td>
<td>9.91</td>
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<td>9.88</td>
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<td>605.mcf_s</td>
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<tr>
<td>620.omnetpp_s</td>
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<td>631.deepsjeng_s</td>
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<td>269</td>
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<tr>
<td>641.leela_s</td>
<td>32</td>
<td>376</td>
<td>4.54</td>
<td>376</td>
<td>4.54</td>
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<tr>
<td>648.exchange2_s</td>
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<td>221</td>
<td>13.3</td>
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<td>13.3</td>
<td>221</td>
<td>13.3</td>
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<td>657.xz_s</td>
<td>32</td>
<td>277</td>
<td>22.4</td>
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**SPECspeed2017_int_base = 9.68**  
**SPECspeed2017_int_peak = 9.91**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:

- KMP_AFFINITY = "granularity=fine,compact,1,0"
- OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
 sync; echo 3 > /proc/sys/vm/drop_caches
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

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CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Apr-2019
Tested by: Huawei
Software Availability: Dec-2018

Platform Notes

BIOS configuration:
- Power Efficiency Mode Set to Custom
- Hyper-Threading Set to Disable
- XPT Prefetch Set to Enabled

Sysinfo program /spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on linux-tr2u Tue Dec 25 17:57:21 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

- model name: Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
- 2 "physical id"s (chips)
- 32 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores: 16
  siblings: 16
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:

- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 32
- On-line CPU(s) list: 0-31
- Thread(s) per core: 1
- Core(s) per socket: 16
- Socket(s): 2
- NUMA node(s): 2
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
- Stepping: 4
- CPU MHz: 2100.000
- CPU max MHz: 3700.0000
- CPU min MHz: 1000.0000
- BogoMIPS: 4200.00
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 1024K
- L3 cache: 22528K

(Continued on next page)
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---

**Platform Notes (Continued)**

NUMA node0 CPU(s): 0-15
NUMA node1 CPU(s): 16-31

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmperf pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
rdrcr lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cdp_13 invpcid_single pti
ssbd mba ibrs ibpb tpr_shadow vmbni flexpriority ept vpid fsgsbase tsc_adjust
bm1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx
smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsaves cqm_llc
cqm_occup_llc cqm_mbb_total cqm_mbb_local dtherm ida arat pln pts pkur ospke flush_l1d

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a
physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
node 0 size: 191904 MB
node 0 free: 191241 MB
node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
node 1 size: 193278 MB
node 1 free: 190443 MB
node distances:
node 0 1
0: 10 21
1: 21 10

From /proc/meminfo

MemTotal: 394427212 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 4

# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"
VERSION="12-SP4"
VERSION_ID="12.4"

(Continued on next page)
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## Platform Notes (Continued)

```
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP4"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp4"
```

```
uname -a:
        x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Dec 24 17:54
SPEC is set to: /spec2017
```

```
Filesystem  Type  Size  Used Avail Use% Mounted on
/dev/sda3  xfs  882G  101G  782G  12% /
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS INSYDE Corp. 6.36 02/15/2019
Memory:
    24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666
```

(End of data from sysinfo program)

## Compiler Version Notes

```
==============================================================================
CC  600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base, peak) 657.xz_s(base)
==============================================================================
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
```

```
CC  600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 657.xz_s(peak)
==============================================================================
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
```

```
CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base,
(Continued on next page)```
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**Compiler Version Notes (Continued)**

```plaintext
header)

Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

---

**Base Compiler Invocation**

C benchmarks:
```bash
icc -m64 -std=c11
```

C++ benchmarks:
```bash
icpc -m64
```

Fortran benchmarks:
```bash
ifort -m64
```

**Base Portability Flags**

```plaintext
-DSPEC_LP64 -DSPEC_LINUX
```

(Continued on next page)
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Base Portability Flags (Continued)

641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-DCORE-AVX512
-ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-DCORE-AVX512
-ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-xCORE-AVX512
-ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-nostandard-realloc-lhs

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Peak Portability Flags

Same as Base Portability Flags
Peek Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -gopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -gopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -gopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

623.xalanbmks: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

631.deepsjeng_s: Same as 623.xalanbmks

641.leela_s: Same as 623.xalanbmks

Fortran benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4

(Continued on next page)
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Peak Optimization Flags (Continued)

Fortran benchmarks (continued):
- nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-12-25 17:57:21-0500.
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