## SPEC® CPU2017 Floating Point Rate Result

### Dell Inc.

PowerEdge C6420 (Intel Xeon Silver 4216, 2.10GHz)

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>64</td>
<td>170</td>
<td>174</td>
</tr>
</tbody>
</table>

### Copyright 2017-2019 Standard Performance Evaluation Corporation

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### Hardware

- **CPU Name:** Intel Xeon Silver 4216
- **Max MHz.:** 3200
- **Nominal:** 2100
- **Enabled:** 32 cores, 2 chips, 2 threads/core
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 22 MB I+D on chip per chip
- **Other:** None
- **Memory:** 384 GB (12 x 32 GB 2Rx8 PC4-2933Y-R, running at 2400)
- **Storage:** 1 x 480 GB SATA SSD
- **Other:** None

---

### Software

- **OS:** Ubuntu 18.04.2 LTS
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++ Compiler Build 20181018 for Linux; Fortran: Version 19.0.1.144 of Intel Fortran Compiler Build 20181018 for Linux
- **Parallel:** No
- **Firmware:** Version 2.1.6 released Mar-2019
- **File System:** ext4
- **System State:** Run level 5 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** None
SPEC CPU2017 Floating Point Rate Result

Dell Inc.

PowerEdge C6420 (Intel Xeon Silver 4216, 2.10GHz)

Results Table

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<thead>
<tr>
<th>Benchmark</th>
<th>Base Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base Seconds</th>
<th>Ratio</th>
<th>Peak Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>64</td>
<td>1507</td>
<td>426</td>
<td>1509</td>
<td>425</td>
<td>1505</td>
<td>426</td>
<td>1508</td>
<td>426</td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>64</td>
<td>566</td>
<td>143</td>
<td>568</td>
<td>143</td>
<td>569</td>
<td>142</td>
<td>568</td>
<td>143</td>
<td></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>64</td>
<td>487</td>
<td>125</td>
<td>489</td>
<td>124</td>
<td>485</td>
<td>125</td>
<td>483</td>
<td>126</td>
<td></td>
</tr>
<tr>
<td>510.parest_r</td>
<td>64</td>
<td>1686</td>
<td>99.3</td>
<td>1682</td>
<td>99.5</td>
<td>1685</td>
<td>99.3</td>
<td>1690</td>
<td>99.0</td>
<td></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>64</td>
<td>767</td>
<td>195</td>
<td>770</td>
<td>194</td>
<td>767</td>
<td>195</td>
<td>644</td>
<td>231</td>
<td></td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>64</td>
<td>695</td>
<td>97.1</td>
<td>696</td>
<td>97.0</td>
<td>696</td>
<td>96.9</td>
<td>668</td>
<td>101</td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>64</td>
<td>781</td>
<td>183</td>
<td>772</td>
<td>186</td>
<td>771</td>
<td>186</td>
<td>751</td>
<td>191</td>
<td></td>
</tr>
<tr>
<td>526.blender_r</td>
<td>64</td>
<td>543</td>
<td>179</td>
<td>544</td>
<td>179</td>
<td>544</td>
<td>179</td>
<td>544</td>
<td>179</td>
<td></td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>64</td>
<td>608</td>
<td>184</td>
<td>608</td>
<td>184</td>
<td>603</td>
<td>186</td>
<td>644</td>
<td>196</td>
<td></td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>64</td>
<td>411</td>
<td>387</td>
<td>416</td>
<td>383</td>
<td>412</td>
<td>386</td>
<td>415</td>
<td>383</td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>64</td>
<td>385</td>
<td>280</td>
<td>386</td>
<td>279</td>
<td>385</td>
<td>280</td>
<td>387</td>
<td>278</td>
<td></td>
</tr>
<tr>
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<td>64</td>
<td>1838</td>
<td>136</td>
<td>1844</td>
<td>135</td>
<td>1847</td>
<td>135</td>
<td>1846</td>
<td>135</td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>64</td>
<td>1286</td>
<td>79.1</td>
<td>1291</td>
<td>78.8</td>
<td>1290</td>
<td>78.8</td>
<td>1257</td>
<td>80.9</td>
<td></td>
</tr>
</tbody>
</table>

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run: LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation

(Continued on next page)
General Notes (Continued)

Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Platform Notes

BIOS settings:
ADDDC setting disabled
Sub NUMA Cluster enabled
Virtualization Technology disabled
DCU Streamer Prefetcher enabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Logical Processor enabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on intel-sut Tue Mar 12 06:00:36 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Silver 4216 CPU @ 2.10GHz
  2 "physical id"s (chips)
  64 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following
corrections from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 16
  siblings : 32
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian
  CPU(s): 64

(Continued on next page)
Dell Inc. PowerEdge C6420 (Intel Xeon Silver 4216, 2.10GHz)

SPECrater2017_fp_base = 170
SPECrater2017_fp_peak = 174

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Mar-2019
Hardware Availability: Apr-2019
Software Availability: Feb-2019

Platform Notes (Continued)

On-line CPU(s) list: 0-63
Thread(s) per core: 2
Core(s) per socket: 16
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4216 CPU @ 2.10GHz
Stepping: 6
CPU MHz: 2465.887
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 22528K
NUMA node0 CPU(s): 0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60
NUMA node1 CPU(s): 1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61
NUMA node2 CPU(s): 2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62
NUMA node3 CPU(s): 3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,63
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
tab pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsdp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref perf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrnr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand
lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single ssbd mbm ibrs
ibpb stibp ibrs_enhanced tpr_shadow vnum flexpriority ept vpid fsgsbase tsc_adjust
bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed advx
smash clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1
xsavec qcm_llc qcm_occup_ccq qcm_mbb_total qcm_mbb_local dtherm ida arat pln pts pku
ospk avx512_vnni flush_lld arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60
node 0 size: 95167 MB
node 0 free: 94706 MB
node 1 cpus: 1 5 9 13 17 21 25 29 33 37 41 45 49 53 57 61
node 1 size: 96744 MB
node 1 free: 96287 MB
node 2 cpus: 2 6 10 14 18 22 26 30 34 38 42 46 50 54 58 62
node 2 size: 96743 MB

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

Dell Inc.
PowerEdge C6420 (Intel Xeon Silver 4216, 2.10GHz)

SPECrate2017_fp_base = 170
SPECrate2017_fp_peak = 174

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Mar-2019
Hardware Availability: Apr-2019
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Platform Notes (Continued)

node 2 free: 96042 MB
node 3 cpus: 3 7 11 15 19 23 27 31 35 39 43 47 51 55 59 63
node 3 size: 96763 MB
node 3 free: 96327 MB
node distances:
node 0 1 2 3
0:  10  21  11  21
1:  21  10  21  11
2:  11  21  10  21
3:  21  11  21  10

From /proc/meminfo
MemTotal:       394690740 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
Ubuntu 18.04.2 LTS

From /etc/*release* /etc/*version*
debian_version: buster/sid
os-release:
   NAME="Ubuntu"
   VERSION="18.04.2 LTS (Bionic Beaver)"
   ID=ubuntu
   ID_LIKE=debian
   PRETTY_NAME="Ubuntu 18.04.2 LTS"
   VERSION_ID="18.04"
   HOME_URL="https://www.ubuntu.com/
   SUPPORT_URL="https://help.ubuntu.com/

uname -a:
Linux intel-sut 4.15.0-45-generic #48-Ubuntu SMP Tue Jan 29 16:28:13 UTC 2019 x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB

run-level 5 Mar 11 20:36

SPEC is set to: /home/cpu2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 ext4 439G 19G 398G 5% /

(Continued on next page)
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<td>174</td>
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</tr>
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**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Test Date:** Mar-2019  
**Tested by:** Dell Inc.  
**Hardware Availability:** Apr-2019  
**Software Availability:** Feb-2019

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**Platform Notes (Continued)**

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

- BIOS Dell Inc. 2.1.6 03/04/2019
- Memory:
  - 11x 00AD00B300AD HMA84GR7CR4N-WM 32 GB 2 rank 2933, configured at 2400
  - 1x 00AD063200AD HMA84GR7CR4N-WM 32 GB 2 rank 2933, configured at 2400
  - 4x Not Specified

(End of data from sysinfo program)

---

**Compiler Version Notes**

```
CC  519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base, peak)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

CC  519.lbm_r(peak)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

CXXC 508.namd_r(base) 510.parest_r(base, peak)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

CXXC 508.namd_r(peak)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
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```

(Continued on next page)
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CPU2017 License: 55
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Test Date: Mar-2019
Hardware Availability: Apr-2019
Software Availability: Feb-2019

Compiler Version Notes (Continued)

==============================================================================
CC  511.povray_r(base) 526.blender_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
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Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
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Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
FC  507.cactuBSSN_r(base, peak)
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Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
FC  503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

FC  554.roms_r(peak)
------------------------------------------------------------------------------

(Continued on next page)
Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64
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Test Date: Mar-2019
Tested by: Dell Inc.
Hardware Availability: Apr-2019
Software Availability: Feb-2019

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte
Dell Inc.

PowerEdge C6420 (Intel Xeon Silver 4216, 2.10GHz)

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**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
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**Test Date:** Mar-2019  
**Hardware Availability:** Apr-2019  
**Software Availability:** Feb-2019

---

**Peak Compiler Invocation**

C benchmarks:
```bash
icc -m64 -std=c11
```

C++ benchmarks:
```bash
icpc -m64
```

Fortran benchmarks:
```bash
ifort -m64
```

Benchmarks using both Fortran and C:
```bash
ifort -m64 icc -m64 -std=c11
```

Benchmarks using both C and C++:
```bash
icpc -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:
```bash
icpc -m64 icc -m64 -std=c11 ifort -m64
```

---

**Peak Portability Flags**

Same as Base Portability Flags

---

**Peak Optimization Flags**

C benchmarks:
```bash
519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
```
```bash
538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
```
```bash
544.nab_r: Same as 538.imagick_r
```

C++ benchmarks:
```bash
508.namd_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
```

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(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

Dell Inc.
PowerEdge C6420 (Intel Xeon Silver 4216, 2.10GHz)

SPECrate2017_fp_peak = 174
SPECrate2017_fp_base = 170

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Mar-2019
Hardware Availability: Apr-2019
Software Availability: Feb-2019

Peak Optimization Flags (Continued)

510.parest_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

Fortran benchmarks:

503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -auto
-nostandard-realloc-lhs -align array32byte

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:

-prof-gen(pass l) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:

511.povray_r: -prof-gen(pass l) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

526.blender_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
**SPEC CPU2017 Floating Point Rate Result**

**Dell Inc.**

PowerEdge C6420 (Intel Xeon Silver 4216, 2.10GHz)

<table>
<thead>
<tr>
<th>SPECrate2017_fp_peak</th>
<th>174</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_base</td>
<td>170</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55

**Test Sponsor:** Dell Inc.

**Tested by:** Dell Inc.

**Test Date:** Mar-2019

**Hardware Availability:** Apr-2019

**Software Availability:** Feb-2019

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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