SPEC® CPU2017 Integer Speed Result

Dell Inc.
PowerEdge C6420 (Intel Xeon Silver 4216, 2.10GHz)

SPECspeed2017_int_base = 8.17
SPECspeed2017_int_peak = 8.35

Dell Inc.
PowerEdge C6420 (Intel Xeon Silver 4216, 2.10GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Dell Inc.
PowerEdge C6420 (Intel Xeon Silver 4216, 2.10GHz)

Test Date: Mar-2019
Hardware Availability: Apr-2019
Software Availability: Feb-2019

Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench</td>
<td>64</td>
<td>5.47</td>
<td>8.35</td>
</tr>
<tr>
<td>gcc</td>
<td>64</td>
<td>7.99</td>
<td></td>
</tr>
<tr>
<td>mcf</td>
<td>64</td>
<td>8.25</td>
<td></td>
</tr>
<tr>
<td>omnetpp</td>
<td>64</td>
<td>6.55</td>
<td></td>
</tr>
<tr>
<td>xalancbmk</td>
<td>64</td>
<td>10.2</td>
<td></td>
</tr>
<tr>
<td>x264</td>
<td>64</td>
<td>10.2</td>
<td></td>
</tr>
<tr>
<td>deepsjeng</td>
<td>64</td>
<td>4.59</td>
<td></td>
</tr>
<tr>
<td>leela</td>
<td>64</td>
<td>4.60</td>
<td></td>
</tr>
<tr>
<td>exchange2</td>
<td>64</td>
<td>11.4</td>
<td></td>
</tr>
<tr>
<td>xz</td>
<td>64</td>
<td>11.5</td>
<td></td>
</tr>
</tbody>
</table>

Hardware

CPU Name: Intel Xeon Silver 4216
Max MHz.: 3200
Nominal: 2100
Enabled: 32 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 22 MB I+D on chip per chip
Other: None
Memory: 384 GB (12 x 32 GB 2Rx8 PC4-2933Y-R, running at 2400)
Storage: 1 x 480 GB SATA SSD
Other: None

Software

OS: Ubuntu 18.04.2 LTS
Compiler: C/C++: Version 19.0.1.144 of Intel C/C++ Compiler Build 20181018 for Linux;
Fortran: Version 19.0.1.144 of Intel Fortran Compiler Build 20181018 for Linux
Parallel: Yes
Firmware: Version 2.1.6 released Mar-2019
File System: ext4
System State: Run level 5 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
# Dell Inc.

## PowerEdge C6420 (Intel Xeon Silver 4216, 2.10GHz)

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>64</td>
<td>327</td>
<td>5.43</td>
<td>322</td>
<td>5.52</td>
<td>324</td>
<td>5.47</td>
<td>64</td>
<td>276</td>
<td>6.43</td>
<td>273</td>
<td>6.51</td>
<td>275</td>
<td>6.46</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>64</td>
<td>502</td>
<td>7.94</td>
<td>498</td>
<td>7.99</td>
<td>495</td>
<td>8.04</td>
<td>64</td>
<td>475</td>
<td>8.39</td>
<td>489</td>
<td>8.15</td>
<td>483</td>
<td>8.25</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>64</td>
<td>462</td>
<td>10.2</td>
<td>454</td>
<td>10.4</td>
<td>461</td>
<td>10.2</td>
<td>64</td>
<td>461</td>
<td>10.2</td>
<td>465</td>
<td>10.2</td>
<td>464</td>
<td>10.2</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>64</td>
<td>250</td>
<td>6.52</td>
<td>248</td>
<td>6.59</td>
<td>249</td>
<td>6.55</td>
<td>64</td>
<td>244</td>
<td>6.68</td>
<td>245</td>
<td>6.66</td>
<td>247</td>
<td>6.60</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>64</td>
<td>137</td>
<td>10.3</td>
<td>136</td>
<td>10.4</td>
<td>136</td>
<td>10.4</td>
<td>64</td>
<td>137</td>
<td>10.4</td>
<td>136</td>
<td>10.4</td>
<td>136</td>
<td>10.4</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>64</td>
<td>154</td>
<td>11.4</td>
<td>156</td>
<td>11.3</td>
<td>155</td>
<td>11.4</td>
<td>64</td>
<td>155</td>
<td>11.4</td>
<td>155</td>
<td>11.4</td>
<td>155</td>
<td>11.3</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>64</td>
<td>313</td>
<td>4.59</td>
<td>313</td>
<td>4.58</td>
<td>312</td>
<td>4.60</td>
<td>64</td>
<td>312</td>
<td>4.59</td>
<td>312</td>
<td>4.60</td>
<td>312</td>
<td>4.60</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>64</td>
<td>441</td>
<td>3.87</td>
<td>437</td>
<td>3.91</td>
<td>435</td>
<td>3.92</td>
<td>64</td>
<td>436</td>
<td>3.91</td>
<td>443</td>
<td>3.85</td>
<td>436</td>
<td>3.91</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>64</td>
<td>257</td>
<td>11.4</td>
<td>255</td>
<td>11.5</td>
<td>255</td>
<td>11.5</td>
<td>64</td>
<td>255</td>
<td>11.5</td>
<td>255</td>
<td>11.5</td>
<td>255</td>
<td>11.6</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>64</td>
<td>332</td>
<td>18.6</td>
<td>336</td>
<td>18.4</td>
<td>337</td>
<td>18.4</td>
<td>64</td>
<td>334</td>
<td>18.5</td>
<td>336</td>
<td>18.4</td>
<td>330</td>
<td>18.7</td>
</tr>
</tbody>
</table>

**SPECspeed2017_int_base** = **8.17**  
**SPECspeed2017_int_peak** = **8.35**

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**General Notes**

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
```

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:
```
sync; echo 3 > /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:
```
umactl --interleave=all runcpu <etc>
```

(Continued on next page)
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PowerEdge C6420 (Intel Xeon Silver 4216, 2.10GHz)  

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<tr>
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</thead>
</table>

**CPU2017 License:** 55  
**Test Date:** Mar-2019  
**Hardware Availability:** Apr-2019  
**Software Availability:** Feb-2019

**General Notes (Continued)**

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  

**Platform Notes**

BIOS settings:  
ADDDC setting disabled  
Sub NUMA Cluster enabled  
Virtualization Technology disabled  
DCU Streamer Prefetcher enabled  
System Profile set to Custom  
CPU Performance set to Maximum Performance  
C States set to Autonomous  
C1E disabled  
Uncore Frequency set to Dynamic  
Energy Efficiency Policy set to Performance  
Memory Patrol Scrub disabled  
Logical Processor enabled  
CPU Interconnect Bus Link Power Management disabled  
PCI ASPM L1 Link Power Management disabled  
Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9  
runtime on intel-sut Wed Mar 13 16:17:59 2019

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Silver 4216 CPU @ 2.10GHz  
2 "physical id"s (chips)  
64 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 16  
siblings : 32  
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15  
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:  
Architecture: x86_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 64  
On-line CPU(s) list: 0-63

(Continued on next page)
### Dell Inc.

#### PowerEdge C6420 (Intel Xeon Silver 4216, 2.10GHz)

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

<table>
<thead>
<tr>
<th>Processor</th>
<th>SPECspeed2017_int_peak</th>
<th>SPECspeed2017_int_base</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel Xeon Silver 4216</td>
<td>8.35</td>
<td>8.17</td>
</tr>
</tbody>
</table>

**Test Date:** Mar-2019  
**Hardware Availability:** Apr-2019  
**Software Availability:** Feb-2019

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**Platform Notes (Continued)**

- **Thread(s) per core:** 2  
- **Core(s) per socket:** 16  
- **Socket(s):** 2  
- **NUMA node(s):** 2  
- **Vendor ID:** GenuineIntel  
- **CPU family:** 6  
- **Model:** 85  
- **Model name:** Intel(R) Xeon(R) Silver 4216 CPU @ 2.10GHz  
- **Stepping:** 6  
- **CPU MHz:** 2339.437  
- **BogoMIPS:** 4200.00  
- **Virtualization:** VT-x  
- **L1d cache:** 32K  
- **L1i cache:** 32K  
- **L2 cache:** 1024K  
- **L3 cache:** 22528K  
- **NUMA node0 CPU(s):**  
  - 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58  
  - 60,62  
- **NUMA node1 CPU(s):**  
  - 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59  
  - 61,63  
- **Flags:**  
  - fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov  
  - pat pse36 clflush dts acpica pse sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp  
  - lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid  
  - aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16  
  - xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand  
  - lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cdqc _l3 invpcid_single ssbd mba ibrs  
  - ibpb stibp ibrs_enhanced tpr_shadow vnni flexpriority ept vpid fsgsbase tsc_adjust  
  - bmi1 hle avx2 smep bmi2  
  - ems invpcid rtm cmq mp xdt_a avx512f avx512dq rdseed adx  
  - smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsave xgetbv1  
  - xsave cqm_llc cqm_occup_1lc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts pku  
  - ospke avx512_vnni flush_l1d arch_capabilities  

/proc/cpuinfo cache data  
- cache size : 22528 KB  

From numactl --hardware  
- WARNING: a numactl 'node' might or might not correspond to a physical chip.  
- available: 2 nodes (0-1)  
  - node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46 48 50  
  - 52 54 56 58 60 62  
  - node 0 size: 191932 MB  
  - node 0 free: 191382 MB  
  - node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47 49 51  
  - 53 55 57 59 61 63  
  - node 1 size: 193507 MB  

(Continued on next page)
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Platform Notes (Continued)

node 1 free: 193133 MB
node distances:
node 0 1
0: 10 21
1: 21 10

From /proc/meminfo
MemTotal: 394691084 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Ubuntu 18.04.2 LTS

From /etc/*release*/etc/*version*
debian_version: buster/sid
os-release:
   NAME="Ubuntu"
   VERSION="18.04.2 LTS (Bionic Beaver)"
   ID=ubuntu
   ID_LIKE=debian
   PRETTY_NAME="Ubuntu 18.04.2 LTS"
   VERSION_ID="18.04"
   HOME_URL="https://www.ubuntu.com/"
   SUPPORT_URL="https://help.ubuntu.com/"

uname -a:
Linux intel-sut 4.15.0-45-generic #48-Ubuntu SMP Tue Jan 29 16:28:13 UTC 2019 x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB

run-level 5 Mar 13 16:16

SPEC is set to: /home/cpu2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 ext4 439G 19G 398G 5% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMI BIOS" standard.
BIOS Dell Inc. 2.1.6 03/04/2019

(Continued on next page)
Dell Inc.

PowerEdge C6420 (Intel Xeon Silver 4216, 2.10GHz)

SPECs p e e d 2 0 1 7 _i n t _p e a k = 8.35

SPECs p e e d 2 0 1 7 _i n t _b a s e = 8.17

Copyright 2017-2019 Standard Performance Evaluation Corporation

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**Platform Notes (Continued)**

Memory:
- 11x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933, configured at 2400
- 1x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933, configured at 2400
- 4x Not Specified Not Specified

(End of data from sysinfo program)

---

**Compiler Version Notes**

```
CC  600.perlbench_s(base)  602.gcc_s(base)  605.mcf_s(base)  625.x264_s(base, peak)  657.xz_s(base)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
CC  600.perlbench_s(peak)  602.gcc_s(peak)  605.mcf_s(peak)  657.xz_s(peak)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
CXXC  620.omnetpp_s(base)  623.xalancbmk_s(base, peak)  631.deepsjeng_s(base, peak)  641.leela_s(base, peak)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
CXXC  620.omnetpp_s(peak)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
FC  648.exchange2_s(base, peak)
```

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Dell Inc.
PowerEdge C6420 (Intel Xeon Silver 4216, 2.10GHz)

SPECspeed2017_int_base = 8.17
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Compiler Version Notes (Continued)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -gopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64 -lqkmalloc

(Continued on next page)
Dell Inc. PowerEdge C6420 (Intel Xeon Silver 4216, 2.10GHz)

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Base Optimization Flags (Continued)

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -fopenmp -DSPEC_OpenMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

(Continued on next page)
## Peak Optimization Flags (Continued)

625.x264_s: -W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -openmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: -W1,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -openmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

620.omnetpp_s: -W1,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

623.xalancbmk_s: -W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

631.deepsjeng_s: Same as 623.xalancbmk_s

641.leela_s: Same as 623.xalancbmk_s

Fortran benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:

<table>
<thead>
<tr>
<th>SPEC CPU2017 Integer Speed Result</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dell Inc.</td>
</tr>
<tr>
<td>PowerEdge C6420 (Intel Xeon Silver 4216, 2.10GHz)</td>
</tr>
<tr>
<td>SPECspeed2017_int_peak = 8.35</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU2017 License: 55</th>
<th>Test Date: Mar-2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Dell Inc.</td>
<td>Hardware Availability: Apr-2019</td>
</tr>
<tr>
<td>Tested by: Dell Inc.</td>
<td>Software Availability: Feb-2019</td>
</tr>
</tbody>
</table>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-03-13 12:17:59-0400.  
Originally published on 2019-05-29.