Lenovo Global Technology
ThinkSystem SR650
(2.50 GHz, Intel Xeon Silver 4215)

SPECrater2017_fp_base = 107
SPECrater2017_fp_peak = Not Run

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<th>511.povray_r</th>
<th>519.lbm_r</th>
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<th>526.blender_r</th>
<th>527.cam4_r</th>
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<th>549.fotonik3d_r</th>
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**Hardware**

- **CPU Name:** Intel Xeon Silver 4215
- **Max MHz.:** 3500
- **Nominal:** 2500
- **Enabled:** 16 cores, 2 chips, 2 threads/core
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 11 MB I+D on chip per chip
- **Other:** None
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2933Y-R, running at 2400)
- **Storage:** 1 x 800 GB SATA SSD
- **Other:** None

**Software**

- **OS:** Red Hat Enterprise Linux Server release 7.6 (Maipo)
- **Kernel:** 3.10.0-957.el7.x86_64
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++ Compiler Build 20181018 for Linux; Fortran: Version 19.0.1.144 of Intel Fortran Compiler Build 20181018 for Linux
- **Parallel:** No
- **Firmware:** Lenovo BIOS Version IVE135R 2.10 released Feb-2019
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** Not Applicable
- **Other:** None
Lenovo Global Technology
ThinkSystem SR650
(2.50 GHz, Intel Xeon Silver 4215)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Results Table

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SPECrate2017_fp_base = 107
SPECrate2017_fp_peak = Not Run

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

General Notes
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0u1/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
  numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Lenovo Global Technology
ThinkSystem SR650 (2.50 GHz, Intel Xeon Silver 4215)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

SPECrater2017_fp_base = 107
SPECrater2017_fp_peak = Not Run

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Choose Operating Mode set to Custom Mode
C-states set to Legacy
DCU Streamer Prefetcher set to Disable
Trusted Execution Technology set to Enable
Stale Atos set to Enable
LLC dead line alloc set to Disable
Patrol Scrub set to Disable
Sysinfo program /home/cpu2017-1.0.5-ic19.0u1/bin/sysinfo
Rev: r5974 of 2018-05-19 9b3e8f2999c33d61f64985e45859ea9
running on localhost.localdomain Tue May 7 17:18:05 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4215 CPU @ 2.50GHz
   2 "physical id"s (chips)
   32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 2

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650
(2.50 GHz, Intel Xeon Silver 4215)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

SPECrate2017_fp_base = 107
SPECrate2017_fp_peak = Not Run

Test Date: May-2019
Hardware Availability: Apr-2019
Software Availability: Nov-2018

CPU2017 Floating Point Rate Result
Copyright 2017-2019 Standard Performance Evaluation Corporation

Platform Notes (Continued)

Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4215 CPU @ 2.50GHz
Stepping: 6
CPU MHz: 2500.000
BogoMIPS: 5000.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 11264K
NUMA node0 CPU(s): 0-7,16-23
NUMA node1 CPU(s): 8-15,24-31
Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good ntop tsc aperf perfmon pebf nmi pml pml4 dtes64 monitor ds cpl vm xse_pxsr smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand vpd enc tsc_pka vnmi flexpriority ept vpid fsgsbase ts馐调 ctid bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rt_4 avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsaves xgetbv1 cqm_l1c cqm_occu1_l1c cqm_mmb_total cqm_mmb_local dtm2 ida arat pln pts pku ospke avx512_vnni spec_ctrl intel_stibp flush_l1d arch_capabilities

From /proc/cpuinfo

cache size : 11264 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
node 0 size: 196281 MB
node 0 free: 191431 MB
node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
node 1 size: 196608 MB
node 1 free: 191960 MB
node distances:
node   0   1
0: 10 21
1: 21 10

From /proc/meminfo

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650
(2.50 GHz, Intel Xeon Silver 4215)

SPECrates2017_fp_base = 107
SPECrates2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Platform Notes (Continued)

MemTotal: 395880312 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.6 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VARIANT="Server"
VARIANT_ID="server"
VERSION_ID="7.6"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.6 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)

uname -a:
Linux localhost.localdomain 3.10.0-957.el7.x86_64 #1 SMP Thu Oct 4 20:48:51 UTC 2018
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS

run-level 3 May 7 17:13

SPEC is set to: /home/cpu2017-1.0.5-ic19.0u1
Filesystem Type Size Used Avail Use% Mounted on
/dev/sdb2 xfs 689G 18G 671G 3% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
BIOS Lenovo -[IVE135R-2.10] - 02/27/2019
Memory:
24x Samsung M393A2K43CB2-CVF 16 GB 2 rank 2933, configured at 2400

(End of data from sysinfo program)
Lenovo Global Technology

ThinkSystem SR650
(2.50 GHz, Intel Xeon Silver 4215)

SPECratre2017_fp_base = 107
SPECratre2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: May-2019
Tested by: Lenovo Global Technology
Hardware Availability: Apr-2019
Software Availability: Nov-2018

Compiler Version Notes

==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------

CXXC 508.namd_r(base) 510.parest_r(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------

CC  511.povray_r(base) 526.blender_r(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------

FC  507.cactuBSSN_r(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------

FC  503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650
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**Compiler Version Notes (Continued)**

==============================================================================
| CC 521.wrf_r(base) 527.cam4_r(base) |
==============================================================================

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

**Base Compiler Invocation**

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

**Base Portability Flags**

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsiged-char

(Continued on next page)
Lenovo Global Technology  \[ \text{SPECrate2017\_fp\_base} = 107 \]

ThinkSystem SR650  \[ \text{SPECrate2017\_fp\_peak} = \text{Not Run} \]

(2.50 GHz, Intel Xeon Silver 4215)

CPU2017 License: 9017  
Test Sponsor: Lenovo Global Technology  
Test Date: May-2019  
Tested by: Lenovo Global Technology  
Hardware Availability: Apr-2019  
Software Availability: Nov-2018

**Base Portability Flags (Continued)**

- 527.cam4_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG
- 538.imagick_r: -DSPEC\_LP64
- 544.nab_r: -DSPEC\_LP64
- 549.fotonik3d_r: -DSPEC\_LP64
- 554.roms_r: -DSPEC\_LP64

**Base Optimization Flags**

**C benchmarks:**
- -xCORE-AVX2 -ipo -03 -no-prec-div -qopt-prefetch -ffinite-math-only  
  -qopt-mem-layout-trans=4

**C++ benchmarks:**
- -xCORE-AVX2 -ipo -03 -no-prec-div -qopt-prefetch -ffinite-math-only  
  -qopt-mem-layout-trans=4

**Fortran benchmarks:**
- -xCORE-AVX2 -ipo -03 -no-prec-div -qopt-prefetch -ffinite-math-only  
  -qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs  
  -align array32byte

**Benchmarks using both Fortran and C:**
- -xCORE-AVX2 -ipo -03 -no-prec-div -qopt-prefetch -ffinite-math-only  
  -qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs  
  -align array32byte

**Benchmarks using both C and C++:**
- -xCORE-AVX2 -ipo -03 -no-prec-div -qopt-prefetch -ffinite-math-only  
  -qopt-mem-layout-trans=4

**Benchmarks using Fortran, C, and C++:**
- -xCORE-AVX2 -ipo -03 -no-prec-div -qopt-prefetch -ffinite-math-only  
  -qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs  
  -align array32byte

The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:

## Lenovo Global Technology

**ThinkSystem SR650**  
(2.50 GHz, Intel Xeon Silver 4215)

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**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  

**Test Date:** May-2019  
**Hardware Availability:** Apr-2019  
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