**SPEC® CPU2017 Integer Rate Result**

**Dell Inc.**

PowerEdge T640 (Intel Xeon Gold 5217, 3.00GHz)

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
<th>SPECrate2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>105</td>
<td>109</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55

**Test Sponsor:** Dell Inc.

**Tested by:** Dell Inc.

**Test Date:** May-2019

**Hardware Availability:** Apr-2019

**Software Availability:** Feb-2019

### Copied

<table>
<thead>
<tr>
<th>Benchmark</th>
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<th>SPECrate2017_int_base</th>
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<tbody>
<tr>
<td>perlbench_r</td>
<td>32</td>
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<td>93.0</td>
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<tr>
<td>gcc_r</td>
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<td>mcf_r</td>
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<td>142</td>
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<td>omnetpp_r</td>
<td>32</td>
<td>64.9</td>
<td>142</td>
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<tr>
<td>xalancbmk_r</td>
<td>32</td>
<td>121</td>
<td>129</td>
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<tr>
<td>x264_r</td>
<td>32</td>
<td>212</td>
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<tr>
<td>deepsjeng_r</td>
<td>32</td>
<td>90.5</td>
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<tr>
<td>leela_r</td>
<td>32</td>
<td>85.3</td>
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<tr>
<td>exchange2_r</td>
<td>32</td>
<td>194</td>
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<tr>
<td>xz_r</td>
<td>32</td>
<td>67.9</td>
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### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon Gold 5217</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz.:</td>
<td>3700</td>
</tr>
<tr>
<td>Nominal:</td>
<td>3000</td>
</tr>
<tr>
<td>Enabled:</td>
<td>16 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1,2 chips</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>1 MB I+D on chip per core</td>
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<tr>
<td>L3:</td>
<td>11 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Memory:</td>
<td>384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R, running at 2666)</td>
</tr>
<tr>
<td>Storage:</td>
<td>1 x 960 GB SATA SSD</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
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</table>

### Software

<table>
<thead>
<tr>
<th>OS:</th>
<th>Ubuntu 18.04.2 LTS</th>
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</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 19.0.1.144 of Intel C/C++</td>
</tr>
<tr>
<td>Compiler Build:</td>
<td>20181018 for Linux;</td>
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<tr>
<td>Fortran:</td>
<td>Version 19.0.1.144 of Intel Fortran</td>
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<td>Compiler Build:</td>
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<td>Parallel:</td>
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<td>Firmware:</td>
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<tr>
<td>File System:</td>
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<td>System State:</td>
<td>Run level 5 (multi-user)</td>
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<tr>
<td>Base Pointers:</td>
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<tr>
<td>Peak Pointers:</td>
<td>32/64-bit</td>
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<td>Other:</td>
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Results Table

<table>
<thead>
<tr>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Seconds</th>
<th>Ratio</th>
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<tr>
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<td>82.7</td>
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<td>557</td>
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<td>32</td>
<td>509</td>
<td>67.9</td>
<td>509</td>
<td>67.8</td>
</tr>
</tbody>
</table>

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesysten page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:

(Continued on next page)
**General Notes (Continued)**

```
numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
```

**Platform Notes**

BIOS settings:
- ADDDC setting disabled
- Sub NUMA Cluster enabled
- Virtualization Technology disabled
- DCU Streamer Prefetcher disabled
- System Profile set to Custom
- CPU Performance set to Maximum Performance
- C States set to Autonomous
- C1E disabled
- Uncore Frequency set to Dynamic
- Energy Efficiency Policy set to Performance
- Memory Patrol Scrub disabled
- Logical Processor enabled
- CPU Interconnect Bus Link Power Management disabled
- PCI ASPM L1 Link Power Management disabled
- Sysinfo program /home/cpu2017/bin/sysinfo
- Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
- running on intel-sut Fri May 17 19:09:40 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name: Intel(R) Xeon(R) Gold 5217 CPU @ 3.00GHz
- 2 "physical id"s (chips)
- 32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
- cpu cores: 8
- siblings: 16
- physical 0: cores 0 1 2 3 4 5 6 7
- physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 32

(Continued on next page)
SPEC CPU2017 Integer Rate Result

Dell Inc.

PowerEdge T640 (Intel Xeon Gold 5217, 3.00GHz)

SPECrate2017_int_peak = 109
SPECrate2017_int_base = 105

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: May-2019
Hardware Availability: Apr-2019
Software Availability: Feb-2019

Platform Notes (Continued)

On-line CPU(s) list: 0-31
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5217 CPU @ 3.00GHz
Stepping: 6
CPU MHz: 3426.922
BogoMIPS: 6000.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 11264K
NUMA node0 CPU(s): 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30
NUMA node1 CPU(s): 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aernfperf pni pclmulqdq dtc64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand
lahf_lm abm 3dnowprefetch cpuid_fault epb cat13 cdp13 invpcid_single intel_pinn
ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid
fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 ertm invpcid rtm cqm mpx rt_e avx512f
avx512dq rdseed adx smap clflushopt clwb intel_ptu avx512cd avx512bw avx512vl
xsaveopt xsaves xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local
dtherm ida arat pln pts pku ospke avx512_vnni flush_l1d arch_capabilities

/proc/cpuinfo cache data
  cache size : 11264 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30
  node 0 size: 192859 MB
  node 0 free: 192398 MB
  node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31
  node 1 size: 193511 MB
  node 1 free: 193147 MB
  node distances:
    node 0 1
    0: 10 21
    1: 21 10
SPEC CPU2017 Integer Rate Result

Dell Inc.

PowerEdge T640 (Intel Xeon Gold 5217, 3.00GHz)

SPECrate2017_int_base = 105
SPECrate2017_int_peak = 109

CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: May-2019
Hardware Availability: Apr-2019
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Software Availability: Feb-2019

Platform Notes (Continued)

From /proc/meminfo
MemTotal: 395643836 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Ubuntu 18.04.2 LTS

From /etc/*release* /etc/*version*
debian_version: buster/sid
os-release:
NAME=Ubuntu
VERSION="18.04.2 LTS (Bionic Beaver)"
ID=ubuntu
ID_LIKE=debian
PRETTY_NAME="Ubuntu 18.04.2 LTS"
VERSION_ID="18.04"
HOME_URL="https://www.ubuntu.com/
SUPPORT_URL="https://help.ubuntu.com/

uname -a:
Linux intel-sut 4.15.0-45-generic #48-Ubuntu SMP Tue Jan 29 16:28:13 UTC 2019 x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB

run-level 5 May 17 18:55

SPEC is set to: /home/cpu2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 ext4 439G 19G 398G 5% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 2.2.6 04/16/2019
Memory:
12x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933, configured at 2666
12x Not Specified Not Specified

(End of data from sysinfo program)
## Compiler Version Notes

**CC  502.gcc_r(peak)**

Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

**CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak)**

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

**CC  500.perlbench_r(peak)**

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

**CXXC  523.xalancbmk_r(peak)**

Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

**CXXC  520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)**

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

**FC  548.exchange2_r(base, peak)**

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018

(Continued on next page)
**SPEC CPU2017 Integer Rate Result**

Dell Inc.

PowerEdge T640 (Intel Xeon Gold 5217, 3.00GHz)

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CPU2017 License: 55
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Test Date: May-2019
Hardware Availability: Apr-2019
Software Availability: Feb-2019

**Compiler Version Notes (Continued)**

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

---

**Base Compiler Invocation**

C benchmarks:

```
icc -m64 -std=c11
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

**Base Portability Flags**

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:

```
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div 
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc
```

C++ benchmarks:

```
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div 
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc
```

(Continued on next page)
### SPEC CPU2017 Integer Rate Result

**Dell Inc.**

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**CPU2017 License:** 55

**Test Sponsor:** Dell Inc.

**Test Date:** May-2019

**Hardware Availability:** Apr-2019

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**Software Availability:** Feb-2019

---

### Base Optimization Flags (Continued)

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_andLibraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

---

### Peak Compiler Invocation

**C benchmarks (except as noted below):**

```sh
icc -m64 -std=c11
```


**C++ benchmarks (except as noted below):**

```sh
icpc -m64
```


Fortran benchmarks:

```sh
ifort -m64
```

---

### Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64

502.gcc_r: -D_FILE_OFFSET_BITS=64

505.mcf_r: -DSPEC_LP64

520.omnetpp_r: -DSPEC_LP64

523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX

525.x264_r: -DSPEC_LP64

531.deepsjeng_r: -DSPEC_LP64

541.leela_r: -DSPEC_LP64

548.exchange2_r: -DSPEC_LP64

557.xz_r: -DSPEC_LP64

---

### Peak Optimization Flags

**C benchmarks:**

```sh
500.perlbench_r: (-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
```

(Continued on next page)
Dell Inc. PowerEdge T640 (Intel Xeon Gold 5217, 3.00GHz)

| SPECrate2017_int_base | 105 |
| SPECrate2017_int_peak | 109 |

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.
Test Date: May-2019
Hardware Availability: Apr-2019
Software Availability: Feb-2019

Peak Optimization Flags (Continued)

500.perlbench_r (continued):
- fno-strict-overflow
- L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
- lqkmalloc

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
- xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
- L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=4
- L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
- lqkmalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=4
- fno-alias
- L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
- lqkmalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=4
- L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
- lqkmalloc

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
- xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
- L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:

- Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
- L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
- lqkmalloc

The flags files that were used to format this result can be browsed at
**SPEC CPU2017 Integer Rate Result**

**Dell Inc.**

PowerEdge T640 (Intel Xeon Gold 5217, 3.00GHz)  

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**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  

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You can also download the XML flags sources by saving the following links:

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