## SPEC® CPU2017 Integer Rate Result

### Dell Inc.

**PowerEdge FC640 (Intel Xeon Gold 6238T, 1.90GHz)**

- **CPU2017 License:** 55
- **Test Sponsor:** Dell Inc.
- **Tested by:** Dell Inc.
- **Test Date:** Mar-2019
- **Hardware Availability:** Apr-2019
- **Software Availability:** Feb-2019

### Hardware

- **CPU Name:** Intel Xeon Gold 6238T
- **Max MHz.:** 3700
- **Nominal:** 1900
- **Enabled:** 44 cores, 2 chips, 2 threads/core
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 30.25 MB I+D on chip per chip
- **Other:** None
- **Memory:** 384 GB (12 x 32 GB 2Rx8 PC4-2933Y-R)
- **Storage:** 1 x 480 GB SATA SSD
- **Other:** None

### Software

- **OS:** Ubuntu 18.04.2 LTS
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++ Compiler Build 20181018 for Linux;
  Fortran: Version 19.0.1.144 of Intel Fortran Compiler Build 20181018 for Linux
- **Parallel:** No
- **Firmware:** Version 2.2.2 released Mar-2019
- **File System:** ext4
- **System State:** Run level 5 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 32/64-bit
- **Other:** jemalloc memory allocator V5.0.1

### SPECrate2017_int_base = 217

<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS: Ubuntu 18.04.2 LTS</td>
<td>CPU Name: Intel Xeon Gold 6238T</td>
</tr>
<tr>
<td>Compiler: C/C++: Version 19.0.1.144 of Intel C/C++ Compiler Build 20181018 for Linux; Fortran: Version 19.0.1.144 of Intel Fortran Compiler Build 20181018 for Linux</td>
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<tr>
<td>Base Pointers: 64-bit</td>
<td>L2: 1 MB I+D on chip per core</td>
</tr>
<tr>
<td>Peak Pointers: 32/64-bit</td>
<td>L3: 30.25 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other: jemalloc memory allocator V5.0.1</td>
<td>Other: None</td>
</tr>
</tbody>
</table>

- **SPECrate2017_int_peak = 225**

### SPECrate2017_int_base (217)

| Copies | 0 | 20.0 | 40.0 | 60.0 | 80.0 | 100 | 120 | 140 | 160 | 180 | 200 | 220 | 240 | 260 | 280 | 300 | 320 | 340 | 360 | 380 | 400 | 420 | 440 | 460 |
|---------|---|---------|---------|---------|---------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|
| 500.perlbench_r | 88 | 88 | 168 | 193 | 178 | 205 | 281 | 280 |
| 502.gcc_r | 88 | 88 | 145 | 145 |
| 505.mcf_r | 88 | 88 | 88 | 240 | 253 | 433 | 452 |
| 520.omnetpp_r | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 |
| 523.xalancbmk_r | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 |
| 525.x264_r | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 |
| 531.deepsjeng_r | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 |
| 541.leela_r | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 |
| 548.exchange2_r | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 |
| 557.xz_r | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 | 88 |

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**SPECrate2017_int_peak (225)**
**SPEC CPU2017 Integer Rate Result**

Dell Inc.

PowerEdge FC640 (Intel Xeon Gold 6238T, 1.90GHz)

<table>
<thead>
<tr>
<th>Benchmark</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>727</td>
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<td>281</td>
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<td>280</td>
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<tr>
<td>520.omnetpp_r</td>
<td>88</td>
<td>795</td>
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<td>796</td>
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<td>795</td>
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<td>561</td>
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<td>558</td>
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<td>561</td>
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<td>572</td>
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<td>541.leela_r</td>
<td>88</td>
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<td>825</td>
<td>177</td>
<td>826</td>
<td>176</td>
<td>88</td>
<td>849</td>
<td>172</td>
<td>831</td>
<td>175</td>
<td>825</td>
<td>177</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>88</td>
<td>593</td>
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<td>557.xz_r</td>
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<td>146</td>
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<td>146</td>
<td>651</td>
<td>146</td>
</tr>
</tbody>
</table>

**SPECrate2017_int_base =** 217

**SPECrate2017_int_peak =** 225

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**General Notes**

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
```

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3 > /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

(Continued on next page)
General Notes (Continued)

numactl --interleave=all runcpu <etc>
njemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS settings:
ADDDC setting disabled
Sub NUMA Cluster enabled
Virtualization Technology disabled
DCU Streamer Prefetcher enabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Logical Processor enabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on intel-sut Fri Apr 5 22:09:24 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

<table>
<thead>
<tr>
<th>model name:</th>
<th>Intel(R) Xeon(R) Gold 6238T CPU @ 1.90GHz</th>
</tr>
</thead>
<tbody>
<tr>
<td>2</td>
<td>&quot;physical id&quot;s (chips)</td>
</tr>
<tr>
<td>88</td>
<td>&quot;processors&quot;</td>
</tr>
<tr>
<td>cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)</td>
<td></td>
</tr>
<tr>
<td>cpu cores :</td>
<td>22</td>
</tr>
<tr>
<td>siblings   :</td>
<td>44</td>
</tr>
<tr>
<td>physical 0:</td>
<td>cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28</td>
</tr>
<tr>
<td>physical 1:</td>
<td>cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28</td>
</tr>
</tbody>
</table>

From lscpu:

<table>
<thead>
<tr>
<th>Architecture:</th>
<th>x86_64</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU op-mode(s):</td>
<td>32-bit, 64-bit</td>
</tr>
<tr>
<td>Byte Order:</td>
<td>Little Endian</td>
</tr>
<tr>
<td>CPU(s):</td>
<td>88</td>
</tr>
</tbody>
</table>
SPEC CPU2017 Integer Rate Result

Dell Inc. PowerEdge FC640 (Intel Xeon Gold 6238T, 1.90GHz) SPECrate2017_int_base = 217
SPECrate2017_int_peak = 225

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Mar-2019
Hardware Availability: Apr-2019
Software Availability: Feb-2019

Platform Notes (Continued)

On-line CPU(s) list: 0-87
Thread(s) per core: 2
Core(s) per socket: 22
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6238T CPU @ 1.90GHz
Stepping: 6
CPU MHz: 3025.471
BogoMIPS: 3800.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 30976K
NUMA node0 CPU(s):
0, 2, 4, 6, 8, 10, 12, 14, 16, 18, 20, 22, 24, 26, 28, 30, 32, 34, 36, 38, 40, 42, 44, 46, 48, 50, 52, 54, 56, 58
, 60, 62, 64, 66, 68, 70, 72, 74, 76, 78, 80, 82, 84, 86
NUMA node1 CPU(s):
1, 3, 5, 7, 9, 11, 13, 15, 17, 19, 21, 23, 25, 27, 29, 31, 33, 35, 37, 39, 41, 43, 45, 47, 49, 51, 53, 55, 57, 59
, 61, 63, 65, 67, 69, 71, 73, 75, 77, 79, 81, 83, 85, 87
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl cmp_legacy sse cmpxchg
shrinkage arith tm mtrr pge mca cmov pat pse cx8 pmx pclmulqdq
movbe setfcbit tsc_adjust fpcmt pe vme pse36 dts mce cx16
pat mtrr pge mca cmov pat pse cx8 mp xcase mmx cmov cx8
 cmov pmx pclmulqdq movbe setfcbit
setfcbit tsc_adjust fpcmt pe vme pse36 dts mce cx16
pat mtrr pge mca cmov pat pse cx8 mp xcase mmx cmov cx8
 cmov pmx pclmulqdq movbe setfcbit
setfcbit tsc_adjust fpcmt pe vme pse36 dts mce cx16

/proc/cpuinfo cache data
cache size : 30976 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46 48 50
52 54 56 58 60 62 64 66 68 70 72 74 76 78 80 82 84 86
node 0 size: 191928 MB
node 0 free: 191374 MB
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47 49 51
53 55 57 59 61 63 65 67 69 71 73 75 77 79 81 83 85 87

(Continued on next page)
Dell Inc. PowerEdge FC640 (Intel Xeon Gold 6238T, 1.90GHz)  Dell Inc.

SPECrate2017_int_base = 217
SPECrate2017_int_peak = 225

Platform Notes (Continued)

node 1 size: 193505 MB
node 1 free: 192992 MB
node distances:
  node 0 1
  0: 10 21
  1: 21 10

From /proc/meminfo
  MemTotal: 394684252 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Ubuntu 18.04.2 LTS

From /etc/*release* /etc/*version*
  debian_version: buster/sid
  os-release:
    NAME="Ubuntu"
    VERSION="18.04.2 LTS (Bionic Beaver)"
    ID=ubuntu
    ID_LIKE=debian
    PRETTY_NAME="Ubuntu 18.04.2 LTS"
    VERSION_ID="18.04"
    HOME_URL="https://www.ubuntu.com/"
    SUPPORT_URL="https://help.ubuntu.com/"

uname -a:
  Linux intel-sut 4.15.0-45-generic #48-Ubuntu SMP Tue Jan 29 16:28:13 UTC 2019 x86_64
  x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
  CVE-2017-5754 (Meltdown): Not affected
  CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
  CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB

run-level 5 Apr 5 22:08

SPEC is set to: /home/cpu2017
  Filesystem Type Size Used Avail Use% Mounted on
  /dev/sda2  ext4 439G 19G 398G 5% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

(Continued on next page)
Dell Inc.
PowerEdge FC640 (Intel Xeon Gold 6238T, 1.90GHz)

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**CPU2017 License:** 55
**Test Date:** Mar-2019
**Test Sponsor:** Dell Inc.
**Hardware Availability:** Apr-2019
**Tested by:** Dell Inc.
**Software Availability:** Feb-2019

**Platform Notes (Continued)**

BIOS Dell Inc. 2.2.2 03/05/2019
Memory:
3x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
9x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
4x Not Specified Not Specified

(End of data from sysinfo program)

### Compiler Version Notes

<table>
<thead>
<tr>
<th>CC</th>
<th>502.gcc_r (peak)</th>
<th>Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.1.144 Build 20181018 Copyright (C) 1985-2018 Intel Corporation. All rights reserved.</th>
</tr>
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<td>CC</td>
<td>500.perlbench_r (base) 502.gcc_r (base) 505.mcf_r (base, peak) 525.x264_r (base, peak) 557.xz_r (base, peak)</td>
<td>Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018 Copyright (C) 1985-2018 Intel Corporation. All rights reserved.</td>
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<tr>
<td>CC</td>
<td>500.perlbench_r (peak)</td>
<td>Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018 Copyright (C) 1985-2018 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>CXXC</td>
<td>523.xalancbmk_r (peak)</td>
<td>Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.1.144 Build 20181018 Copyright (C) 1985-2018 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>CXXC</td>
<td>520.omnetpp_r (base, peak) 523.xalancbmk_r (base) 531.deepsjeng_r (base, peak) 541.leela_r (base, peak)</td>
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SPECrater2017_int_base = 217
SPECrater2017_int_peak = 225

Compiler Version Notes (Continued)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
FC 548.exchange2_r(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalanchmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
SPEC CPU2017 Integer Rate Result
Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.
PowerEdge FC640 (Intel Xeon Gold 6238T, 1.90GHz)

SPECrates2017_int_base = 217
SPECrates2017_int_peak = 225

CPU2017 License: 55
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Hardware Availability: Apr-2019
Software Availability: Feb-2019

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64 -std=c11


C++ benchmarks (except as noted below):
icpc -m64

523.xalancbmk_r:icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64

(Continued on next page)
Dell Inc.
PowerEdge FC640 (Intel Xeon Gold 6238T, 1.90GHz)

SPECrate2017_int_base = 217
SPECrate2017_int_peak = 225

CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: Mar-2019
Hardware Availability: Apr-2019
Tested by: Dell Inc.
Software Availability: Feb-2019

Peak Portability Flags (Continued)

531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass l) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass l) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

(Continued on next page)
Dell Inc. PowerEdge FC640 (Intel Xeon Gold 6238T, 1.90GHz) SPECrate2017_int_base = 217
SPECrate2017_int_peak = 225

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Peak Optimization Flags (Continued)

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-04-05 18:09:24-0400.
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