## SPEC® CPU2017 Integer Speed Result

### Dell Inc.

**PowerEdge M640 (Intel Xeon Gold 6248, 2.50GHz)**

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>55</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Mar-2019</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Apr-2019</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Feb-2019</td>
</tr>
</tbody>
</table>

### SPECspeed2017_int_base = 9.84

### SPECspeed2017_int_peak = 10.0

### Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
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</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>80</td>
<td>7.77</td>
<td>9.46</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>80</td>
<td></td>
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<tr>
<td>605.mcf_s</td>
<td>80</td>
<td>9.49</td>
<td>11.8</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>80</td>
<td>8.10</td>
<td>11.8</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>80</td>
<td>12.5</td>
<td>12.5</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>80</td>
<td></td>
<td></td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>80</td>
<td>5.45</td>
<td>14.2</td>
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<tr>
<td>641.leela_s</td>
<td>80</td>
<td>4.76</td>
<td></td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>80</td>
<td>14.1</td>
<td></td>
</tr>
<tr>
<td>657.xz_s</td>
<td>80</td>
<td></td>
<td>21.7</td>
</tr>
</tbody>
</table>

---

### Software

- **OS:** Ubuntu 18.04.2 LTS
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++
- **Compiler Build:** 20181018 for Linux;
- **Fortran:** Version 19.0.1.144 of Intel Fortran
- **Compiler Build:** 20181018 for Linux
- **Parallel:** Yes
- **Firmware:** Version 2.2.2 released Mar-2019
- **File System:** ext4
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1

### Hardware

- **CPU Name:** Intel Xeon Gold 6248
- **Max MHz.:** 3900
- **Nominal:** 2500
- **Enabled:** 40 cores, 2 chips, 2 threads/core
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 27.5 MB I+D on chip per chip
- **Other:** None
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)
- **Storage:** 1 x 480 GB SATA SSD
- **Other:** None
Dell Inc.  
PowerEdge M640 (Intel Xeon Gold 6248, 2.50GHz)

**SPEC speed2017_int_base** = 9.84  
**SPEC speed2017_int_peak** = 10.0

**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
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<th>Ratio</th>
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<th>Ratio</th>
</tr>
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<tbody>
<tr>
<td>600.perlbench_s</td>
<td>80</td>
<td>268</td>
<td>6.63</td>
<td>272</td>
<td>6.52</td>
<td>272</td>
<td>6.53</td>
<td>80</td>
<td>229</td>
<td>7.73</td>
<td>228</td>
<td>7.80</td>
<td>229</td>
<td>7.77</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>80</td>
<td>390</td>
<td>12.1</td>
<td>392</td>
<td>12.0</td>
<td>394</td>
<td>12.0</td>
<td>80</td>
<td>400</td>
<td>11.8</td>
<td>400</td>
<td>11.8</td>
<td>400</td>
<td>11.8</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>80</td>
<td>201</td>
<td>12.6</td>
<td>114</td>
<td>12.5</td>
<td>113</td>
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<td>80</td>
<td>113</td>
<td>12.5</td>
<td>114</td>
<td>12.5</td>
<td>113</td>
<td>12.5</td>
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<tr>
<td>641.leela_s</td>
<td>80</td>
<td>358</td>
<td>4.76</td>
<td>359</td>
<td>4.76</td>
<td>358</td>
<td>4.77</td>
<td>80</td>
<td>358</td>
<td>4.76</td>
<td>358</td>
<td>4.77</td>
<td>358</td>
<td>4.77</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>80</td>
<td>287</td>
<td>21.6</td>
<td>283</td>
<td>21.8</td>
<td>284</td>
<td>21.7</td>
<td>80</td>
<td>280</td>
<td>22.1</td>
<td>284</td>
<td>21.8</td>
<td>286</td>
<td>21.6</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**General Notes**

Environment variables set by runcpu before the start of the run:

- LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```bash
sync; echo 3>/proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu <etc>
```

(Continued on next page)
Dell Inc.
PowerEdge M640 (Intel Xeon Gold 6248, 2.50GHz)

SPECspeed2017_int_base = 9.84
SPECspeed2017_int_peak = 10.0

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

General Notes (Continued)
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS settings:
ADDDC setting disabled
Sub NUMA Cluster enabled
Virtualization Technology disabled
DCU Streamer Prefetcher enabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Logical Processor enabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on intel-sut Fri Apr 26 16:55:22 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6248 CPU @ 2.50GHz
  2 "physical id"s (chips)
  80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 20
siblings : 40
physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 80
On-line CPU(s) list: 0-79

(Continued on next page)
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Tested by: Dell Inc.

Test Date: Mar-2019
Hardware Availability: Apr-2019
Software Availability: Feb-2019

Platform Notes (Continued)

Thread(s) per core: 2
Core(s) per socket: 20
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6248 CPU @ 2.50GHz
Stepping: 6
CPU MHz: 2109.830
BogoMIPS: 5000.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 28160K
NUMA node0 CPU(s):
0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58
,60,62,64,66,68,70,72,74,76,78
NUMA node1 CPU(s):
1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59
,61,63,65,67,69,71,73,75,77,79
Flags:

fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acp1 mmx fxsr sse sse2 ss ht tm pbe syscall nx pdelpgb rdtsscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrp pdcm pcd dca sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand
lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single ssbd mba ibrs
ibpb stibp ibrs_enhanced tpr_shadow vהייתי flexpriority ept vpid fsgsbase tsc_adjust
bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx
smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaves cqm_llc cqm_occup_llc
cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts pku
ospke avx512_vnni flush_l1d arch_capabilities

From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
physical chip.

available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46 48 50
52 54 56 58 60 62 64 66 68 70 72 74 76 78
node 0 size: 191929 MB
node 0 free: 191374 MB
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47 49 51
53 55 57 59 61 63 65 67 69 71 73 75 77 79
node 1 size: 193506 MB

(Continued on next page)
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Platform Notes (Continued)

node 1 free: 192980 MB
node distances:
node 0 1
  0: 10 21
  1: 21 10

From /proc/meminfo
  MemTotal: 394685728 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Ubuntu 18.04.2 LTS

From /etc/*release* /etc/*version*
  debian_version: buster/sid
  os-release:
    NAME="Ubuntu"
    VERSION="18.04.2 LTS (Bionic Beaver)"
    ID=ubuntu
    ID_LIKE=debian
    PRETTY_NAME="Ubuntu 18.04.2 LTS"
    VERSION_ID="18.04"
    HOME_URL="https://www.ubuntu.com/"
    SUPPORT_URL="https://help.ubuntu.com/"

uname -a:
  Linux intel-sut 4.15.0-45-generic #48-Ubuntu SMP Tue Jan 29 16:28:13 UTC 2019 x86_64
  x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB

run-level 3 Apr 26 16:54

SPEC is set to: /home/cpu2017
  Filesystem  Type  Size  Used  Avail  Use% Mounted on
  /dev/sda2    ext4  439G  19G  398G   5%  /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS Dell Inc. 2.2.2 03/05/2019

(Continued on next page)
SPEC CPU2017 Integer Speed Result

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PowerEdge M640 (Intel Xeon Gold 6248, 2.50GHz)

SPECspeed2017_int_base = 9.84
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CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: Mar-2019
Tested by: Dell Inc.
Hardware Availability: Apr-2019
Software Availability: Feb-2019

Platform Notes (Continued)

Memory:
6x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
6x 00AD069D00AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
4x Not Specified Not Specified

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base, peak) 657.xz_s(base)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
CC  600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 657.xz_s(peak)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
CXXC 620.omnetpp_s(peak)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
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==============================================================================
FC  648.exchange2_s(base, peak)
(Continued on next page)
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**Test Sponsor:** Dell Inc.  
**Hardware Availability:** Apr-2019  
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**Tested by:** Dell Inc.  
**Software Availability:** Feb-2019

### Compiler Version Notes (Continued)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

---

### Base Compiler Invocation

**C benchmarks:**

```bash
icc -m64 -std=c11
```

**C++ benchmarks:**

```bash
icpc -m64
```

**Fortran benchmarks:**

```bash
ifort -m64
```

### Base Portability Flags

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>-DSPEC_LP64 -DSPEC_LINUX_X64</td>
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<td>657.xz_s</td>
<td>-DSPEC_LP64</td>
</tr>
</tbody>
</table>

### Base Optimization Flags

**C benchmarks:**

```bash
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

**C++ benchmarks:**

```bash
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64 -lqkmallocc
```
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Base Optimization Flags (Continued)

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

(Continued on next page)
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Peak Optimization Flags (Continued)

625.x264_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -oopenmp -DSPEC_OPENMP  
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2  
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3  
-no-prec-div -DSPEC_SUPPRESS_OPENMP -oopenmp  
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4  
-DSPEC_SUPPRESS_OPENMP  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64  
-lqkmalloc

623.xalancbmk_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64  
-lqkmalloc

631.deepsjeng_s: Same as 623.xalancbmk_s

641.leela_s: Same as 623.xalancbmk_s

Fortran benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
### SPEC CPU2017 Integer Speed Result

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<td>9.84</td>
<td>10.0</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**Test Date:** Mar-2019  
**Hardware Availability:** Apr-2019  
**Software Availability:** Feb-2019

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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