Lenovo Global Technology
ThinkSystem SR630
(2.50 GHz, Intel Xeon Gold 5215L)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology
Test Date: May-2019
Hardware Availability: Apr-2019
Software Availability: Oct-2018

SPECrate2017_fp_base = 128
SPECrate2017_fp_peak = Not Run

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>128</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

**Hardware**

CPU Name: Intel Xeon Gold 5215L
Max MHz.: 3400
Nominal: 2500
Enabled: 20 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 13.75 MB I+D on chip per chip
Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2933Y-R, running at 2666)
Storage: 1 x 800 GB SATA SSD
Other: None

**Software**

OS: Red Hat Enterprise Linux Server release 7.6 (Maipo)
Compiler: C/C++: Version 19.0.0.117 of Intel C/C++
Compiler for Linux;
Fortran: Version 19.0.0.117 of Intel Fortran
Compiler for Linux
Parallel: No
Firmware: Lenovo BIOS Version IVE135P 2.10 released Feb-2019
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: None
Lenovo Global Technology
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(2.50 GHz, Intel Xeon Gold 5215L)

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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>40</td>
<td>1116</td>
<td>360</td>
<td>1116</td>
<td>360</td>
<td>1115</td>
<td>360</td>
<td>1116</td>
<td>360</td>
<td>1116</td>
<td>360</td>
<td>1115</td>
<td>360</td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>40</td>
<td>553</td>
<td>91.6</td>
<td>554</td>
<td>91.5</td>
<td>552</td>
<td>91.7</td>
<td>553</td>
<td>91.5</td>
<td>554</td>
<td>91.6</td>
<td>552</td>
<td>91.6</td>
<td></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>40</td>
<td>429</td>
<td>88.5</td>
<td>429</td>
<td>88.5</td>
<td>430</td>
<td>88.3</td>
<td>429</td>
<td>88.5</td>
<td>429</td>
<td>88.5</td>
<td>430</td>
<td>88.3</td>
<td></td>
</tr>
<tr>
<td>510.parest_r</td>
<td>40</td>
<td>1415</td>
<td>73.9</td>
<td>1412</td>
<td>74.1</td>
<td>1416</td>
<td>73.9</td>
<td>1415</td>
<td>73.9</td>
<td>1412</td>
<td>74.1</td>
<td>1416</td>
<td>73.9</td>
<td></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>40</td>
<td>705</td>
<td>132</td>
<td>705</td>
<td>133</td>
<td>705</td>
<td>132</td>
<td>705</td>
<td>132</td>
<td>705</td>
<td>132</td>
<td>705</td>
<td>132</td>
<td></td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>40</td>
<td>464</td>
<td>90.8</td>
<td>464</td>
<td>90.9</td>
<td>464</td>
<td>90.8</td>
<td>464</td>
<td>90.8</td>
<td>464</td>
<td>90.8</td>
<td>464</td>
<td>90.8</td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>40</td>
<td>591</td>
<td>152</td>
<td>609</td>
<td>147</td>
<td>619</td>
<td>145</td>
<td>591</td>
<td>152</td>
<td>609</td>
<td>147</td>
<td>619</td>
<td>145</td>
<td></td>
</tr>
<tr>
<td>526.blender_r</td>
<td>40</td>
<td>528</td>
<td>115</td>
<td>527</td>
<td>116</td>
<td>528</td>
<td>115</td>
<td>528</td>
<td>115</td>
<td>527</td>
<td>116</td>
<td>528</td>
<td>115</td>
<td></td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>40</td>
<td>540</td>
<td>130</td>
<td>539</td>
<td>130</td>
<td>536</td>
<td>131</td>
<td>540</td>
<td>130</td>
<td>539</td>
<td>130</td>
<td>536</td>
<td>131</td>
<td></td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>40</td>
<td>383</td>
<td>260</td>
<td>379</td>
<td>263</td>
<td>384</td>
<td>259</td>
<td>383</td>
<td>260</td>
<td>379</td>
<td>263</td>
<td>384</td>
<td>259</td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>40</td>
<td>350</td>
<td>192</td>
<td>346</td>
<td>195</td>
<td>346</td>
<td>195</td>
<td>350</td>
<td>192</td>
<td>346</td>
<td>195</td>
<td>346</td>
<td>195</td>
<td></td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>40</td>
<td>1248</td>
<td>125</td>
<td>1250</td>
<td>125</td>
<td>1251</td>
<td>125</td>
<td>1248</td>
<td>125</td>
<td>1250</td>
<td>125</td>
<td>1251</td>
<td>125</td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>40</td>
<td>925</td>
<td>68.7</td>
<td>933</td>
<td>68.1</td>
<td>932</td>
<td>68.2</td>
<td>925</td>
<td>68.7</td>
<td>933</td>
<td>68.1</td>
<td>932</td>
<td>68.2</td>
<td></td>
</tr>
</tbody>
</table>

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
  numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Lenovo Global Technology
ThinkSystem SR630
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SPECratenet2017_fp_base = 128
SPECratenet2017_fp_peak = Not Run

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Choose Operating Mode set to Custom Mode
C-states set to Legacy
DCU Streamer Prefetcher set to Disable
Trusted Execution Technology set to Enable
Stale AtoS set to Enable
LLC dead line alloc set to Disable
Patrol Scrub set to Disable
Sysinfo program /home/cpu2017-1.0.5-ic19/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on localhost.localdomain Wed May 22 01:32:38 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 5215L CPU @ 2.50GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 10
  siblings : 20
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 40
On-line CPU(s) list: 0-39
Thread(s) per core: 2

(Continued on next page)
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CPU2017 License: 9017
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Platform Notes (Continued)

Core(s) per socket: 10
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5215L CPU @ 2.50GHz
Stepping: 6
CPU MHz: 2500.000
BogoMIPS: 5000.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 14080K
NUMA node0 CPU(s): 0-9,20-29
NUMA node1 CPU(s): 10-19,30-39
Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good ntop tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 inteleistop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 intel_pt ssbd mba ibrs ibpb stibp ibrs增强了tpr_shadow vmmi flexpriority ept vpid fsqsbasetcadjust bni hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_11c cqm_occup_llc cqm_mbb_total cqm_mbb_local dtherm ida arat pln pts pkup ospke avx512_vnni spec_ctrl intel_stibp flush_l1d arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

From /proc/cpuinfo cache data
cache size : 14080 KB

(Continued on next page)
## Lenovo Global Technology

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### SPEC CPU2017 Floating Point Rate Result

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<th>SPECrate2017_fp_base</th>
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</tr>
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</tbody>
</table>

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

### Platform Notes (Continued)

- **MemTotal:** 395879464 kB  
- **HugePages_Total:** 0  
- **Hugepagesize:** 2048 kB

From `/etc/*release* /etc/*version*`

- `os-release:`
  - `NAME="Red Hat Enterprise Linux Server"`
  - `VERSION="7.6 (Maipo)"`
  - `ID="rhel"`
  - `ID_LIKE="fedora"`
  - `VARIANT="Server"`
  - `VARIANT_ID="server"`
  - `VERSION_ID="7.6"`
  - `PRETTY_NAME="Red Hat Enterprise Linux Server 7.6 (Maipo)"`

- `redhat-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)`
- `system-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)`

```
uname -a:
    Linux localhost.localdomain 3.10.0-957.el7.x86_64 #1 SMP Thu Oct 4 20:48:51 UTC 2018
    x86_64 x86_64 x86_64 GNU/Linux
```

**Kernel self-reported vulnerability status:**

- **CVE-2017-5754 (Meltdown):** Not affected
- **CVE-2017-5753 (Spectre variant 1):** Mitigation: Load fences, __user pointer sanitization
- **CVE-2017-5715 (Spectre variant 2):** Mitigation: Enhanced IBRS

**run-level 3 May 21 17:41**

**SPEC is set to:** /home/cpu2017-1.0.5-ic19  
`Filesystem` `Type` `Size` `Used` `Avail` `Use%` `Mounted on`
 `dev/sdb2` `xfs` `689G` `38G` `652G` `6%` `/home`

Additional information from `dmidecode` follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

- **BIOS Lenovo -[IVE135P-2.10]-** 02/13/2019
- **Memory:**
  - 24x Samsung M393A2K43CB2-CVF 16 GB 2 rank 2933, configured at 2666

(End of data from `sysinfo` program)
Lenovo Global Technology
ThinkSystem SR630
(2.50 GHz, Intel Xeon Gold 5215L)

SPEC CPU2017 Floating Point Rate Result
Copyright 2017-2019 Standard Performance Evaluation Corporation

SPECRate2017_fp_base = 128
SPECRate2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology
Test Date: May-2019
Hardware Availability: Apr-2019
Software Availability: Oct-2018

Compiler Version Notes
==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base) □ 544.nab_r(base)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
CXXC 508.namd_r(base) □ 510.parest_r(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
CC  511.povray_r(base) □ 526.blender_r(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
FC  507.cactuBSSN_r(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------
FC  503.bwaves_r(base) □ 549.fotonik3d_r(base) □ 554.roms_r(base)
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
(Continued on next page)
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SPECrates

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<th>SPECrate2017_fp_base</th>
<th>128</th>
</tr>
</thead>
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<td>SPECrate2017_fp_peak</td>
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</tr>
</tbody>
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Compiler Version Notes (Continued)

==============================================================================
CC  521.wrf_r(base) 527.cam4_r(base)
==============================================================================
Intel (R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel (R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.1bm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char

(Continued on next page)
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**Base Portability Flags (Continued)**

527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:
-xCORE-AVX512 -ipo -03 -no-prec-div -qopt-prefetch
-
finite-math-only -qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX512 -ipo -03 -no-prec-div -qopt-prefetch
-
finite-math-only -qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX512 -ipo -03 -no-prec-div -qopt-prefetch
-
finite-math-only -qopt-mem-layout-trans=3 -auto
-
notstandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -03 -no-prec-div -qopt-prefetch
-
finite-math-only -qopt-mem-layout-trans=3 -auto
-
notstandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-xCORE-AVX512 -ipo -03 -no-prec-div -qopt-prefetch
-
finite-math-only -qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -03 -no-prec-div -qopt-prefetch
-
finite-math-only -qopt-mem-layout-trans=3 -auto
-
notstandard-realloc-lhs -align array32byte

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-A.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-A.xml
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</tr>
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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-05-21 13:32:38-0400.
Originally published on 2019-06-11.