**CPU2017 Floating Point Rate Result**

**Lenovo Global Technology**

ThinkSystem SN550  
(2.30 GHz, Intel Xeon Gold 5218N)  

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name: Intel Xeon Gold 5218N</td>
<td>OS: SUSE Linux Enterprise Server 15 (x86_64)</td>
</tr>
<tr>
<td>Max MHz.: 3700</td>
<td>Kernel 4.12.14-25.13-default</td>
</tr>
<tr>
<td>Nominal: 2300</td>
<td>Compiler: C/C++: Version 19.0.1.144 of Intel C/C++</td>
</tr>
<tr>
<td>Enabled: 32 cores, 2 chips, 2 threads/core</td>
<td>Compiler Build 20181018 for Linux;</td>
</tr>
<tr>
<td>Orderable: 1.2 chips</td>
<td>Fortran: Version 19.0.1.144 of Intel Fortran</td>
</tr>
<tr>
<td>Cache L1: 32 KB I + 32 KB D on chip per core</td>
<td>Compiler Build 20181018 for Linux</td>
</tr>
<tr>
<td>L2: 1 MB I+D on chip per core</td>
<td>Firmware: Lenovo BIOS Version IVE135M 2.10 released Jan-2019</td>
</tr>
<tr>
<td>L3: 22 MB I+D on chip per chip</td>
<td>File System: xfs</td>
</tr>
<tr>
<td>Other: None</td>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R)</td>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>Storage: 1 x 960 GB SATA SSD</td>
<td>Peak Pointers: Not Applicable</td>
</tr>
<tr>
<td>Other: None</td>
<td>Other: None</td>
</tr>
</tbody>
</table>

**Test Sponsor:** Lenovo Global Technology  

**Test Date:** May-2019  

**Hardware Availability:** Apr-2019  

**Tested by:** Lenovo Global Technology  

**Software Availability:** Nov-2018

---

**SPECrate2017_fp_base = 174**  
**SPECrate2017_fp_peak = Not Run**

---

<table>
<thead>
<tr>
<th>Copies</th>
<th>503.bwaves_r</th>
<th>507.cactuBSSN_r</th>
<th>508.namd_r</th>
<th>510.parest_r</th>
<th>511.povray_r</th>
<th>519.lbm_r</th>
<th>521.wrf_r</th>
<th>526.blender_r</th>
<th>527.cam4_r</th>
<th>538.imagick_r</th>
<th>544.nab_r</th>
<th>549.fotonik3d_r</th>
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**Lenovo Global Technology**

ThinkSystem SN550  
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</table>
## Lenovo Global Technology

**ThinkSystem SN550**  
(2.30 GHz, Intel Xeon Gold 5218N)

### CPU2017 License: 9017  
Test Sponsor: Lenovo Global Technology  
Tested by: Lenovo Global Technology

### Results Table

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<td>1214</td>
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</tr>
</tbody>
</table>

 SPECrate2017_fp_base = 174  
 SPECrate2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0ul/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.5  
Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```bash
sync; echo 3 > /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```bash
numactl --interleave=all runcpu <etc>
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Trusted Execution Technology set to Enable
SNC set to Enable
CPU Frequency Limits set to Restrict Maximum Frequency
Workload Configuration set to I/O Sensitive
Sysinfo program /home/cpu2017-1.0.5-ic19.0ul/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-30ed Thu May 16 23:57:56 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5218N CPU @ 2.30GHz
  2 "physical id"s (chips)
  64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Thread(s) per core: 2
Core(s) per socket: 16
Socket(s): 2
NUMA node(s): 4

(Continued on next page)
Lenovo Global Technology

ThinkSystem SN550
(2.30 GHz, Intel Xeon Gold 5218N)

SPECrate2017_fp_base = 174
SPECrate2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology
Test Date: May-2019
Hardware Availability: Apr-2019
Software Availability: Nov-2018

Platform Notes (Continued)

Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5218N CPU @ 2.30GHz
Stepping: 6
CPU MHz: 2300.000
CPU max MHz: 3900.0000
CPU min MHz: 1000.0000
BogoMIPS: 4600.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 22528K
NUMA node0 CPU(s): 0-3,8-11,32-35,40-43
NUMA node1 CPU(s): 4-7,12-15,36-39,44-47
NUMA node2 CPU(s): 16-19,24-27,51,56-59
NUMA node3 CPU(s): 20-23,28-31,60-63
Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperf perfkey pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erts invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsaves cqm_llc cqm_occu_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts pku ospke avx512_vnni flush_lld arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

Lenovo Global Technology
ThinkSystem SN550
(2.30 GHz, Intel Xeon Gold 5218N)

SPECrate2017_fp_base = 174
SPECrate2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

node 3 free: 193269 MB
node distances:
node  0  1  2  3
  0:  10  11  21  21
  1:  11  10  21  21
  2:  21  21  10  11
  3:  21  21  11  10

From /proc/meminfo
MemTotal:       792239292 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
os-release:
  NAME="SLES"
  VERSION="15"
  VERSION_ID="15"
  PRETTY_NAME="SUSE Linux Enterprise Server 15"
  ID="sles"
  ID_LIKE="suse"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 May 16 23:56

SPEC is set to: /home/cpu2017-1.0.5-ic19.0u1

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda3</td>
<td>xfs</td>
<td>893G</td>
<td>42G</td>
<td>852G</td>
<td>5%</td>
<td>/</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[IVE135M-2.10]- 01/16/2019

Memory:

(Continued on next page)
Lenovo Global Technology
ThinkSystem SN550
(2.30 GHz, Intel Xeon Gold 5218N)

SPECrate2017_fp_base = 174
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Test Date: May-2019
Tested by: Lenovo Global Technology
Hardware Availability: Apr-2019
Software Availability: Nov-2018

Platform Notes (Continued)

24x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
CXXC 508.namd_r(base) 510.parest_r(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
CC  511.povray_r(base) 526.blender_r(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
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FC  507.cactuBSSN_r(base)
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Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
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(Continued on next page)
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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
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Compiler Version Notes (Continued)

==============================================================================
FC  503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC  521.wrf_r(base) 527.cam4_r(base)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

503.bwaves_r: -DSPEC_LP64

(Continued on next page)
**SPEC CPU2017 Floating Point Rate Result**

**Lenovo Global Technology**
ThinkSystem SN550
(2.30 GHz, Intel Xeon Gold 5218N)

| SPECrate2017_fp_base = | 174 |
| SPECrate2017_fp_peak = | Not Run |

**CPU2017 License:** 9017  
**CPU2017 License:** Lenovo Global Technology  
**CPU2017 License:** Lenovo Global Technology

**Test Sponsor:** Lenovo Global Technology  
**Test Sponsor:** Lenovo Global Technology  
**Test Sponsor:** Lenovo Global Technology

| Test Date: | May-2019 |
| Hardware Availability: | Apr-2019 |
| Software Availability: | Nov-2018 |

### Base Portability Flags (Continued)

- 507.cactuBSSN_r: -DSPEC_LP64
- 508.namd_r: -DSPEC_LP64
- 510.parest_r: -DSPEC_LP64
- 511.povray_r: -DSPEC_LP64
- 519.lbm_r: -DSPEC_LP64
- 521.wrf_r: -DSPEC_LP64  -DSPEC_CASE_FLAG  -convert big_endian
- 526.blender_r: -DSPEC_LP64  -DSPEC_LINUX  -funsigned-char
- 527.cam4_r: -DSPEC_LP64  -DSPEC_CASE_FLAG
- 538.imagick_r: -DSPEC_LP64
- 544.nab_r: -DSPEC_LP64
- 549.fotonik3d_r: -DSPEC_LP64
- 554.roms_r: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**
-xCORE-AVX2  -ipo  -O3  -no-prec-div  -qopt-prefetch  -ffinite-math-only
-qopt-mem-layout-trans=4

**C++ benchmarks:**
-xCORE-AVX2  -ipo  -O3  -no-prec-div  -qopt-prefetch  -ffinite-math-only
-qopt-mem-layout-trans=4

**Fortran benchmarks:**
-xCORE-AVX2  -ipo  -O3  -no-prec-div  -qopt-prefetch  -ffinite-math-only
-qopt-mem-layout-trans=4  -auto  -nostandard-realloc-lhs
-align array32byte

**Benchmarks using both Fortran and C:**
-xCORE-AVX2  -ipo  -O3  -no-prec-div  -qopt-prefetch  -ffinite-math-only
-qopt-mem-layout-trans=4  -auto  -nostandard-realloc-lhs
-align array32byte

**Benchmarks using both C and C++:**
-xCORE-AVX2  -ipo  -O3  -no-prec-div  -qopt-prefetch  -ffinite-math-only
-qopt-mem-layout-trans=4

**Benchmarks using Fortran, C, and C++:**
-xCORE-AVX2  -ipo  -O3  -no-prec-div  -qopt-prefetch  -ffinite-math-only
-qopt-mem-layout-trans=4  -auto  -nostandard-realloc-lhs
-align array32byte
**Lenovo Global Technology**

**ThinkSystem SN550**  
(2.30 GHz, Intel Xeon Gold 5218N)

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**Software Availability:** Nov-2018

The flags files that were used to format this result can be browsed at:

http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-A.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-A.xml

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