Lenovo Global Technology
ThinkSystem SR650
(2.50 GHz, Intel Xeon Gold 5215M)

**SPECrate2017_int_base = 121**

**SPECrate2017_int_peak = Not Run**

<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS: Red Hat Enterprise Linux Server release 7.6 (Maipo)</td>
<td>CPU Name: Intel Xeon Gold 5215M</td>
</tr>
<tr>
<td>Compiler: C++: Version 19.0.1.144 of Intel C++</td>
<td>Max MHz.: 3400</td>
</tr>
<tr>
<td>Fortran: Version 19.0.1.144 of Intel Fortran</td>
<td>Compiler Build 20181018 for Linux;</td>
</tr>
<tr>
<td></td>
<td>Compiler Build 20181018 for Linux</td>
</tr>
<tr>
<td>Parallel: No</td>
<td>Orderable: 1,2 chips</td>
</tr>
<tr>
<td>Firmware: Lenovo BIOS Version IVE135R 2.10 released Feb-2019</td>
<td>Cache L1: 32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>File System: xfs</td>
<td>L2: 1 MB I+D on chip per core</td>
</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
<td>L3: 13.75 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Base Pointers: 64-bit</td>
<td>Other: None</td>
</tr>
<tr>
<td>Peak Pointers: Not Applicable</td>
<td>Other: None</td>
</tr>
</tbody>
</table>

<table>
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</tr>
<tr>
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<td>Compiler: C++: Version 19.0.1.144 of Intel C++</td>
</tr>
<tr>
<td>Nominal: 2500</td>
<td>Fortran: Version 19.0.1.144 of Intel Fortran</td>
</tr>
<tr>
<td>Enabled: 20 cores, 2 chips, 2 threads/core</td>
<td>Compiler Build 20181018 for Linux;</td>
</tr>
<tr>
<td>Orderable: 1,2 chips</td>
<td>Compiler Build 20181018 for Linux</td>
</tr>
<tr>
<td>Cache L1: 32 KB I + 32 KB D on chip per core</td>
<td>Parallel: No</td>
</tr>
<tr>
<td>L2: 1 MB I+D on chip per core</td>
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</tr>
<tr>
<td>L3: 13.75 MB I+D on chip per chip</td>
<td>File System: xfs</td>
</tr>
<tr>
<td>Other: None</td>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R, running at 2666)</td>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>Storage: 1 x 800 GB SATA SSD</td>
<td>Peak Pointers: Not Applicable</td>
</tr>
<tr>
<td>Other: None</td>
<td>Other: None</td>
</tr>
</tbody>
</table>
Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>40</td>
<td>687</td>
<td>92.7</td>
<td>684</td>
<td>93.1</td>
<td>685</td>
<td>93.0</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>40</td>
<td>563</td>
<td>101</td>
<td>566</td>
<td>100</td>
<td>557</td>
<td>102</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>40</td>
<td>394</td>
<td>164</td>
<td>395</td>
<td>164</td>
<td>395</td>
<td>164</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>40</td>
<td>657</td>
<td>79.9</td>
<td>655</td>
<td>80.1</td>
<td>657</td>
<td>79.9</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>40</td>
<td>295</td>
<td>143</td>
<td>294</td>
<td>144</td>
<td>295</td>
<td>143</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>40</td>
<td>300</td>
<td>234</td>
<td>302</td>
<td>232</td>
<td>301</td>
<td>233</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>40</td>
<td>456</td>
<td>101</td>
<td>455</td>
<td>101</td>
<td>457</td>
<td>100</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>40</td>
<td>711</td>
<td>93.1</td>
<td>698</td>
<td>94.9</td>
<td>713</td>
<td>92.9</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>40</td>
<td>493</td>
<td>213</td>
<td>495</td>
<td>212</td>
<td>494</td>
<td>212</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>40</td>
<td>540</td>
<td>80.0</td>
<td>540</td>
<td>79.9</td>
<td>541</td>
<td>79.9</td>
</tr>
</tbody>
</table>

**SPECrate2017_int_base =** 121
**SPECrate2017_int_peak =** Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0u1/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Choose Operating Mode set to Custom Mode
C-states set to Legacy
Trusted Execution Technology set to Enable
Stale AtoS set to Enable
LLC dead line alloc set to Disable
Sysinfo program /home/cpu2017-1.0.5-ic19.0u1/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on localhost.localdomain Sun May 19 01:29:44 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 5215M CPU @ 2.50GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian
  CPU(s): 40
  On-line CPU(s) list: 0-39
  Thread(s) per core: 2
  Core(s) per socket: 10
  Socket(s): 2
  NUMA node(s): 2
  Vendor ID: GenuineIntel
  CPU family: 6

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650
(2.50 GHz, Intel Xeon Gold 5215M)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

SPECratre2017_int_base = 121
SPECratre2017_int_peak = Not Run

Platform Notes (Continued)

Model: 85
Model name: Intel(R) Xeon(R) Gold 5215M CPU @ 2.50GHz
Stepping: 6
CPU MHz: 2500.000
BogoMIPS: 5000.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 14080K
NUMA node0 CPU(s): 0-9,20-29
NUMA node1 CPU(s): 10-19,30-39
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rtldscp
lm constant_tsc arch_perfmon pebs bts rep_good nop1 xtopology nonstop_tsc
aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ebт cat_13 cdp_13 intel_pt ssbd mba
ibr sibp stibp ibrs_enhanced tpr_shadow vni flexpriority ept vpid fsgsbase
tsc_adjust bm1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq
rdsseed adx snap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1
cqm_11c cqm_occup_11c cqm_mbm_total cqm_mbm_local dtherm ida arat pln pku ospke
avx512_vnni spec_ctrl intel_stibp flush_l1d arch_capabilities

/proc/cpuinfo cache data
  cache size: 14080 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 20 21 22 23 24 25 26 27 28 29
  node 0 size: 392829 MB
  node 0 free: 383905 MB
  node 1 cpus: 10 11 12 13 14 15 16 17 18 19 30 31 32 33 34 35 36 37 38 39
  node 1 size: 393216 MB
  node 1 free: 384152 MB
  node distances:
    node 0 1
    0: 10 21
    1: 21 10

From /proc/meminfo
  MemTotal: 792179796 KB
  HugePages_Total: 0
  Hugepagesize: 2048 KB

From /etc/*release* /etc/*version*

(Continued on next page)
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SPECrate2017_int_base = 121
SPECrate2017_int_peak = Not Run

Test Date: May-2019
Hardware Availability: Apr-2019
Software Availability: Nov-2018

Platform Notes (Continued)

os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.6 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VARIANT="Server"
VARIANT_ID="server"
VERSION_ID="7.6"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.6 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)

uname -a:
Linux localhost.localdomain 3.10.0-957.el7.x86_64 #1 SMP Thu Oct 4 20:48:51 UTC 2018
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS

run-level 3 May 19 01:26

SPEC is set to: /home/cpu2017-1.0.5-ic19.0u1

Filesystem Type Size Used Avail Use% Mounted on
/dev/sdb2 xfs 689G 116G 573G 17% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[IVE135R-2.10]- 02/27/2019
Memory:
24x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933, configured at 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
557.xz_r(base)
==============================================================================

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,

(Continued on next page)
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Tested by: Lenovo Global Technology

Test Date: May-2019
Hardware Availability: Apr-2019
Software Availability: Nov-2018

Compiler Version Notes (Continued)

Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
541.leela_r(base)
------------------------------------------------------------------------------

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
FC 548.exchange2_r(base)
------------------------------------------------------------------------------

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64

(Continued on next page)
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ThinkSystem SR650  
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**SPECrate2017_int_base = 121**  
**SPECrate2017_int_peak = Not Run**

### Base Portability Flags (Continued)

557.xz_r: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`  
- `-gopt-mem-layout=trans=4`  
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64 -lqkmalloc`

**C++ benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`  
- `-gopt-mem-layout=trans=4`  
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64 -lqkmalloc`

**Fortran benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`  
- `-gopt-mem-layout=trans=4 -nostandard-realloc-lhs -align array32byte`  
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64 -lqkmalloc`

### The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:


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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-05-18 13:29:43-0400.  
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