# SPEC® CPU2017 Integer Rate Result

**Dell Inc.**

**PowerEdge MX740c (Intel Xeon Gold 6234, 3.30GHz)**

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate2017_int_base</th>
<th>SPECrate2017_int_peak</th>
</tr>
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<tbody>
<tr>
<td>32</td>
<td>128</td>
<td>132</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Mar-2019  
**Hardware Availability:** Apr-2019  
**Software Availability:** Mar-2019

## Hardware

- **CPU Name:** Intel Xeon Gold 6234  
- **Max MHz.:** 4000  
- **Nominal:** 3300  
- **Enabled:** 16 cores, 2 chips, 2 threads/core  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB L1+D on chip per core  
- **L3:** 24.75 MB L1+D on chip per chip  
- **Other:** None  
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)  
- **Storage:** 1 x 480 GB SATA SSD  
- **Other:** None

## Software

- **OS:** Ubuntu 18.04.2 LTS  
  - kernel 4.15.0-46-generic  
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++  
  - Compiler Build 20181018 for Linux;  
  - Fortran: Version 19.0.1.144 of Intel Fortran  
  - Compiler Build 20181018 for Linux  
- **Parallel:** No  
- **Firmware:** Version 2.2.9 released May-2019  
- **File System:** ext4  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other:** jemalloc memory allocator V5.0.1
## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>32</td>
<td>527</td>
<td>96.7</td>
<td>530</td>
<td>96.2</td>
<td>528</td>
<td>96.5</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>32</td>
<td>419</td>
<td>108</td>
<td>422</td>
<td>107</td>
<td>420</td>
<td>108</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>32</td>
<td>297</td>
<td>174</td>
<td>297</td>
<td>174</td>
<td>297</td>
<td>174</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>32</td>
<td>531</td>
<td>79.1</td>
<td>530</td>
<td>79.2</td>
<td>530</td>
<td>79.2</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>32</td>
<td>211</td>
<td>160</td>
<td>208</td>
<td>163</td>
<td>207</td>
<td>163</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>32</td>
<td>216</td>
<td>259</td>
<td>214</td>
<td>262</td>
<td>214</td>
<td>261</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>32</td>
<td>350</td>
<td>105</td>
<td>350</td>
<td>105</td>
<td>350</td>
<td>105</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>32</td>
<td>547</td>
<td>96.9</td>
<td>539</td>
<td>98.2</td>
<td>530</td>
<td>100</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>32</td>
<td>373</td>
<td>225</td>
<td>372</td>
<td>225</td>
<td>374</td>
<td>224</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>32</td>
<td>427</td>
<td>81.0</td>
<td>428</td>
<td>80.8</td>
<td>426</td>
<td>81.1</td>
</tr>
</tbody>
</table>

**SPECrate2017_int_base = 128**  
**SPECrate2017_int_peak = 132**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
```

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
```

(Continued on next page)
Dell Inc.
PowerEdge MX740c (Intel Xeon Gold 6234, 3.30GHz)

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**General Notes (Continued)**

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

**Platform Notes**

BIOS settings:
ADDDC setting disabled
Sub NUMA Cluster enabled
Virtualization Technology disabled
DCU Streamer Prefetcher enabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Logical Processor enabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on intel-sut Tue May 21 18:39:33 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6234 CPU @ 3.30GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 2 3 9 11 24 25 27
physical 1: cores 2 4 9 11 17 25 26 27

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
**SPEC CPU2017 Integer Rate Result**

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**Platform Notes (Continued)**

Thread(s) per core: 2  
Core(s) per socket: 8  
Socket(s): 2  
NUMA node(s): 4  
Vendor ID: GenuineIntel  
CPU family: 6  
Model: 85  
Model name: Intel(R) Xeon(R) Gold 6234 CPU @ 3.30GHz  
Stepping: 7  
CPU MHz: 3976.377  
BogoMIPS: 6600.00  
Virtualization: VT-x  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 1024K  
L3 cache: 25344K  
NUMA node0 CPU(s): 0,8,12,14,16,24,28,30  
NUMA node1 CPU(s): 1,5,9,13,17,21,25,29  
NUMA node2 CPU(s): 2,4,6,10,18,20,22,26  
NUMA node3 CPU(s): 3,7,11,15,19,23,27,31  
Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fsxr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_ppin ssbd mba ibrs ibpb ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erm sinvpcid rtm cqm mxp rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local dtherm ida arat ping pt ospe avx512_vnni flush_l1d arch_capabilities

From numactl --hardware  
WARNING: a numactl 'node' might or might not correspond to a physical chip.

`/proc/cpuinfo` cache data  
cache size : 25344 KB

(Continued on next page)
### Platform Notes (Continued)

- node 3 cpus: 3 7 11 15 19 23 27 31
- node 3 size: 96744 MB
- node 3 free: 96387 MB
- node distances:
  - node 0 1 2 3
  - 0: 10 21 11 21
  - 1: 21 10 21 11
  - 2: 11 21 10 21
  - 3: 21 11 21 10

From `/proc/meminfo`
- MemTotal: 394676836 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

```
/usr/bin/lsb_release -d
Ubuntu 18.04.2 LTS
```

From `/etc/*release* /etc/*version*`
- debian_version: buster/sid
- os-release:
  - NAME="Ubuntu"
  - VERSION="18.04.2 LTS (Bionic Beaver)"
  - ID=ubuntu
  - ID_LIKE=debian
  - PRETTY_NAME="Ubuntu 18.04.2 LTS"
  - VERSION_ID="18.04"
  - HOME_URL="https://www.ubuntu.com/"
  - SUPPORT_URL="https://help.ubuntu.com/"

```
uname -a:
Linux intel-sut 4.15.0-46-generic #49~Ubuntu SMP Wed Feb 6 09:33:07 UTC 2019 x86_64
x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

- CVE-2017-5754 (Meltdown): Not affected
- CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
- CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB

run-level 3 21 17:43

SPEC is set to: /home/cpu2017

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda2</td>
<td>ext4</td>
<td>439G</td>
<td>22G</td>
<td>396G</td>
<td>6%</td>
<td>/</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret

(Continued on next page)
**Platform Notes (Continued)**

this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 2.2.9 05/08/2019

Memory:

11x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
1x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
12x Not Specified Not Specified

(End of data from sysinfo program)

---

**Compiler Version Notes**

```
CC  502.gcc_r(peak)

Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
525.x264_r(base, peak) 557.xz_r(base, peak)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
CC  500.perlbench_r(peak)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
CXXC 523.xalancbmk_r(peak)

Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

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**Compiler Version Notes (Continued)**

```plaintext
CXXC 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

FC 548.exchange2_r(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

**Base Compiler Invocation**

- **C benchmarks:**
  ```bash
  icc -m64 -std=c11
  ```

- **C++ benchmarks:**
  ```bash
  icpc -m64
  ```

- **Fortran benchmarks:**
  ```bash
  ifort -m64
  ```

**Base Portability Flags**

```bash
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```
Dell Inc.

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**SPECrate2017_int_base = 128**

**SPECrate2017_int_peak = 132**

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## Base Optimization Flags

**C benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=4`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64 -lqkmalloc`

**C++ benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=4`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64 -lqkmalloc`

**Fortran benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64 -lqkmalloc`

## Peak Compiler Invocation

**C benchmarks (except as noted below):**
- `icc -m64 -std=c11`

- `502.gcc_r icc -m32 -std=c11 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/ia32_lin`

**C++ benchmarks (except as noted below):**
- `icpc -m64`

- `523.xalancbmk_r icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/ia32_lin`

**Fortran benchmarks:**
- `ifort -m64`

## Peak Portability Flags

- `500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64`
- `502.gcc_r: -D_FILE_OFFSET_BITS=64`
- `505.mcf_r: -DSPEC_LP64`
- `520.omnetpp_r: -DSPEC_LP64`
- `523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX`
- `525.x264_r: -DSPEC_LP64`

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#### Peak Portability Flags (Continued)

531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64  
548.exchange2_r: -DSPEC_LP64  
557.xz_r: -DSPEC_LP64

#### Peak Optimization Flags

**C benchmarks:**

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4  
-fno-strict-overflow  
-L/usr/local/IntelCompiler19/compilers_and_libaries_2019.1.144/linux/compiler/lib/intel64  
-lqkmalloc

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4  
-L/usr/local/je5.0.1-32/lib  
-ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64  
-lqkmalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -fno-alias  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64  
-lqkmalloc

557.xz_r: Same as 505.mcf_r

**C++ benchmarks:**

520.omnetpp_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64  
-lqkmalloc

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4  
-L/usr/local/je5.0.1-32/lib  
-ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

(Continued on next page)
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Peak Optimization Flags (Continued)

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

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