### CPU2017 Integer Speed Result

**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)  
Synergy 660 Gen10  
(2.70 GHz, Intel Xeon Gold 6226)

**SPECspeak2017_int_base** = 9.27  
**SPECspeak2017_int_peak** = Not Run

<table>
<thead>
<tr>
<th>Test Date:</th>
<th>May-2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Apr-2019</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Feb-2019</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Test Sponsor:</th>
<th>HPE</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tested by:</td>
<td>HPE</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>3</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware:</td>
<td>Intel Xeon Gold 6226</td>
</tr>
<tr>
<td>Max MHz.:</td>
<td>3700</td>
</tr>
<tr>
<td>Nominal:</td>
<td>2700</td>
</tr>
<tr>
<td>Enabled:</td>
<td>48 cores, 4 chips</td>
</tr>
<tr>
<td>Orderable:</td>
<td>2, 4 chip(s)</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Cache L2:</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>Cache L3:</td>
<td>19.25 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R)</td>
</tr>
<tr>
<td>Storage:</td>
<td>1 x 480 GB SATA SSD, RAID 0</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>OS:</td>
<td>SUSE Linux Enterprise Server 15 (x86_64)</td>
</tr>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 19.0.2.187 of Intel C/C++ Compiler Build 20190117 for Linux; Fortran: Version 19.0.2.187 of Intel Fortran Compiler Build 20190117 for Linux</td>
</tr>
<tr>
<td>Parallel:</td>
<td>Yes</td>
</tr>
<tr>
<td>Firmware:</td>
<td>HPE BIOS Version I43 02/02/2019 released Apr-2019</td>
</tr>
<tr>
<td>File System:</td>
<td>btrfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>Not Applicable</td>
</tr>
<tr>
<td>Other:</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
</tbody>
</table>

#### Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>6.47</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>8.50</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>11.7</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>6.71</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>11.6</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>13.6</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>5.15</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>4.51</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>13.3</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>22.3</td>
</tr>
</tbody>
</table>

**SPECspeak2017_int_base** (9.27)
Hewlett Packard Enterprise
Synergy 660 Gen10
(2.70 GHz, Intel Xeon Gold 6226)

SPEC CPU2017 Integer Speed Result
Copyright 2017-2019 Standard Performance Evaluation Corporation

SPECspeed2017_int_base = 9.27
SPECspeed2017_int_peak = Not Run

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>48</td>
<td>275</td>
<td>6.45</td>
<td>274</td>
<td>6.47</td>
<td>273</td>
<td>6.49</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>48</td>
<td>480</td>
<td>8.29</td>
<td>469</td>
<td>8.50</td>
<td>461</td>
<td>8.63</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>48</td>
<td>401</td>
<td>11.8</td>
<td>404</td>
<td>11.7</td>
<td>405</td>
<td>11.7</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>48</td>
<td>244</td>
<td>6.68</td>
<td>243</td>
<td>6.71</td>
<td>243</td>
<td>6.71</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>48</td>
<td>122</td>
<td>11.6</td>
<td>122</td>
<td>11.6</td>
<td>121</td>
<td>11.7</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>48</td>
<td>130</td>
<td>13.6</td>
<td>130</td>
<td>13.6</td>
<td>130</td>
<td>13.6</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>48</td>
<td>278</td>
<td>5.15</td>
<td>278</td>
<td>5.16</td>
<td>278</td>
<td>5.15</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>48</td>
<td>378</td>
<td>4.51</td>
<td>378</td>
<td>4.51</td>
<td>378</td>
<td>4.51</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>48</td>
<td>221</td>
<td>13.3</td>
<td>221</td>
<td>13.3</td>
<td>220</td>
<td>13.3</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>48</td>
<td>278</td>
<td>22.3</td>
<td>278</td>
<td>22.2</td>
<td>278</td>
<td>22.3</td>
</tr>
</tbody>
</table>

SPECspeed2017_int_base = 9.27
SPECspeed2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
 sync; echo 3 > /proc/sys/vm/drop_caches

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017_u2/lib/ia32:/home/cpu2017_u2/lib/intel64:
/home/cpu2017_u2/je5.0.1-32:/home/cpu2017_u2/je5.0.1-64"
OMP_STACKSIZE = "192M"
Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
 sync; echo 3 > /proc/sys/vm/drop_caches
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

(Continued on next page)
SPEC CPU2017 Integer Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise
(Test Sponsor: HPE)
Synergy 660 Gen10
(2.70 GHz, Intel Xeon Gold 6226)

| SPECspeed2017_int_base = 9.27 |
| SPECspeed2017_int_peak = Not Run |

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: May-2019
Hardware Availability: Apr-2019
Software Availability: Feb-2019

General Notes (Continued)


Platform Notes

BIOS Configuration:
Hyper-Threading set to Disabled
Thermal Configuration set to Maximum Cooling
Memory Patrol Scrubbing set to Disabled
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
Enhanced Processor Performance set to Enabled
Workload Profile set to General Peak Frequency Compute
Minimum Processor Idle Power Core C-State set to C1E State
Energy/Performance Bias set to Balanced Power
Workload Profile set to Custom
Numa Group Size Optimization set to Flat
Sysinfo program /home/cpu2017_u2/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on sy660-gen10 Thu May 23 12:48:06 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6226 CPU @ 2.70GHz
4 "physical id"s (chips)
48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 12
siblings : 12
physical 0: cores 0 2 3 4 5 6 8 9 10 11 12 13
physical 1: cores 0 2 3 4 5 6 8 9 10 11 13 14
physical 2: cores 0 2 3 4 5 6 8 9 10 11 13 14
physical 3: cores 1 2 3 4 5 6 8 9 10 11 12 13

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 1
Core(s) per socket: 12
Socket(s): 4

(Continued on next page)
SPEC CPU2017 Integer Speed Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
Synergy 660 Gen10
(2.70 GHz, Intel Xeon Gold 6226)

SPECspeed2017_int_base = 9.27
SPECspeed2017_int_peak = Not Run

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6226 CPU @ 2.70GHz
Stepping: 7
CPU MHz: 2700.000
BogoMIPS: 5400.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 19712K
NUMA node0 CPU(s): 0-11
NUMA node1 CPU(s): 12-23
NUMA node2 CPU(s): 24-35
NUMA node3 CPU(s): 36-47

Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cdp_13 invpcid_single intel_puin mba tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 ets invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsave xsavec xsavecs xsaveq cxsave cxsaveopt stibp stibmw stifxrc stid tsc_adjust mcm avx512f16c avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsave xsavec xsavecs xsaveq cxsave cxsaveopt stibp stibmw stifxrc stid tsc_adjust mcm

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

Available: 4 nodes (0-3)
Node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11
Node 0 size: 193047 MB
Node 0 free: 192619 MB
Node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23
Node 1 size: 193504 MB
Node 1 free: 193335 MB
Node 2 cpus: 24 25 26 27 28 29 30 31 32 33 34 35
Node 2 size: 193533 MB
Node 2 free: 193379 MB
Node 3 cpus: 36 37 38 39 40 41 42 43 44 45 46 47
Node 3 size: 193324 MB
Node 3 free: 192985 MB

(Continued on next page)
SPEC CPU2017 Integer Speed Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
Synergy 660 Gen10
(2.70 GHz, Intel Xeon Gold 6226)

SPECspeed2017_int_base = 9.27
SPECspeed2017_int_peak = Not Run

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>Test Date</th>
<th>Hardware Availability</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>May-2019</td>
<td>Apr-2019</td>
</tr>
</tbody>
</table>

Test Sponsor: HPE  Hardware Availability: Apr-2019
Tested by: HPE     Software Availability: Feb-2019

Platform Notes (Continued)

node distances:
node  0  1  2  3
0: 10 21 21 21
1: 21 10 21 21
2: 21 21 10 21
3: 21 21 21 10

From /proc/meminfo
MemTotal: 791972360 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
    os-release:
      NAME="SLES"
      VERSION="15"
      VERSION_ID="15"
      PRETTY_NAME="SUSE Linux Enterprise Server 15"
      ID="sles"
      ID_LIKE="suse"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
    Linux sy660-gen10 4.12.14-23-default #1 SMP Tue May 29 21:04:44 UTC 2018 (cd0437b)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 May 23 12:47

SPEC is set to: /home/cpu2017_u2
    Filesystem  Type Size  Used Avail Use% Mounted on
    /dev/sdb2   btrfs 445G 114G 331G 26% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
    BIOS HPE I43 02/02/2019
    Memory:
      24x UNKNOWN NOT AVAILABLE

(Continued on next page)
SPEC CPU2017 Integer Speed Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
Synergy 660 Gen10
(2.70 GHz, Intel Xeon Gold 6226)

SPECspeed2017_int_base = 9.27
SPECspeed2017_int_peak = Not Run

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Platform Notes (Continued)

24x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2933
(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base)  
  657.xz_s(base)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
  Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)  
  641.leela_s(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
  Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
FC  648.exchange2_s(base)
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,  
  Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64
Hewlett Packard Enterprise
(Test Sponsor: HPE)
Synergy 660 Gen10
(2.70 GHz, Intel Xeon Gold 6226)

SPEC CPU2017 Integer Speed Result
Copyright 2017-2019 Standard Performance Evaluation Corporation

SPECspeed2017_int_base = 9.27
SPECspeed2017_int_peak = Not Run

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE
Test Date: May-2019
Hardware Availability: Apr-2019
Software Availability: Feb-2019

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/home/cpu2017_u2/je5.0.1-64/ -ljemalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/HPE-ic19.0u1-flags-linux64.html
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/HPE-ic19.0u1-flags-linux64.xml
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.xml

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-05-23 13:48:06-0400.