## SPEC® CPU2017 Floating Point Rate Result

**Hewlett Packard Enterprise**
(Test Sponsor: HPE)
ProLiant DL380 Gen10
(2.10 GHz, Intel Xeon Gold 6238)

<table>
<thead>
<tr>
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</tr>
</thead>
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<td>HPE</td>
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<td>Test Date:</td>
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<td>Apr-2019</td>
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</tbody>
</table>

### Copies

<table>
<thead>
<tr>
<th>Test</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>88</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>88</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>88</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>88</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>88</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>88</td>
</tr>
<tr>
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<td>88</td>
</tr>
<tr>
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<td>88</td>
</tr>
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<td>88</td>
</tr>
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<td>88</td>
</tr>
<tr>
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<td>88</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>88</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>88</td>
</tr>
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</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 6238
- **Max MHz.:** 3700
- **Nominal:** 2100
- **Enabled:** 44 cores, 2 chips, 2 threads/core
- **Orderable:** 1, 2 chip(s)
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 30.25 MB I+D on chip per chip
- **Other:** None
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2933Y-R)
- **Storage:** 1 x 960 GB SATA SSD, RAID 0
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 15 (x86_64)
- **Kernel:** 4.12.14-23-default
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++
  - Compiler Build 20181018 for Linux;
  - Fortran: Version 19.0.1.144 of Intel Fortran
  - Compiler Build 20181018 for Linux
- **Parallel:** No
- **Firmware:** HPE BIOS Version U30 04/18/2019 released Apr-2019
- **File System:** btrfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** Not Applicable
- **Other:** None
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Test Sponsor: HPE
Tested by: HPE

SPECrate2017_fp_base = 227
SPECrate2017_fp_peak = Not Run

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>88</td>
<td>1711</td>
<td>516</td>
<td>1715</td>
<td>514</td>
<td>1711</td>
<td>516</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>88</td>
<td>563</td>
<td>198</td>
<td>563</td>
<td>198</td>
<td>563</td>
<td>198</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>88</td>
<td>481</td>
<td>174</td>
<td>476</td>
<td>175</td>
<td>477</td>
<td>175</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>88</td>
<td>1893</td>
<td>122</td>
<td>1888</td>
<td>122</td>
<td>1894</td>
<td>122</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>88</td>
<td>754</td>
<td>273</td>
<td>759</td>
<td>271</td>
<td>757</td>
<td>271</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>88</td>
<td>748</td>
<td>124</td>
<td>748</td>
<td>124</td>
<td>750</td>
<td>124</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>88</td>
<td>878</td>
<td>225</td>
<td>884</td>
<td>223</td>
<td>888</td>
<td>222</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>88</td>
<td>508</td>
<td>264</td>
<td>509</td>
<td>264</td>
<td>510</td>
<td>263</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>88</td>
<td>574</td>
<td>268</td>
<td>578</td>
<td>266</td>
<td>572</td>
<td>269</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>88</td>
<td>390</td>
<td>562</td>
<td>391</td>
<td>560</td>
<td>390</td>
<td>561</td>
</tr>
<tr>
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<td>88</td>
<td>371</td>
<td>399</td>
<td>373</td>
<td>398</td>
<td>369</td>
<td>402</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>88</td>
<td>2035</td>
<td>168</td>
<td>2027</td>
<td>169</td>
<td>2028</td>
<td>169</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>88</td>
<td>1469</td>
<td>95.2</td>
<td>1472</td>
<td>95.0</td>
<td>1475</td>
<td>94.8</td>
</tr>
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
  numactl --interleave=all runcpu <etc>

General Notes

Environment variables set by runcpu before the start of the run:
  LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configuration:
- Thermal Configuration set to Maximum Cooling
- Memory Patrol Scrubbing set to Disabled
- LLC Prefetch set to Enabled
- LLC Dead Line Allocation set to Disabled
- Enhanced Processor Performance set to Enabled
- Workload Profile set to General Throughput Compute
- Workload Profile set to Custom
- Energy/Performance Bias set to Balanced Performance

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-ta8j Tue Jun 18 08:31:24 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6238 CPU @ 2.10GHz
  2 "physical id"s (chips)
  88 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 22
  siblings : 44
  physical 0: cores 0 1 2 3 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
  physical 1: cores 0 1 2 3 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28

From lscpu:

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 88
On-line CPU(s) list: 0-87
Thread(s) per core: 2
Core(s) per socket: 22
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel

(Continued on next page)
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**Platform Notes (Continued)**

- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Gold 6238 CPU @ 2.10GHz
- Stepping: 7
- CPU MHz: 2100.000
- BogoMIPS: 4200.00
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 1024K
- L3 cache: 30976K
- NUMA node0 CPU(s): 0-10,44-54
- NUMA node1 CPU(s): 11-21,55-65
- NUMA node2 CPU(s): 22-32,66-76
- NUMA node3 CPU(s): 33-43,77-87
- Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdse1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_pinn mba tpr_shadow vmmi flexpriority ept vpid fsgsbase tsc_adjust bni hle avx2 smep bmi2 erms invpcid rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsavesopt xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local ibpb ibrs stibp dtherm ida arat pln pts pku ospke avx512_vnni arch_capabilities ssbd

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
  available: 4 nodes (0-3)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 44 45 46 47 48 49 50 51 52 53 54
  node 0 size: 96248 MB
  node 0 free: 95720 MB
  node 1 cpus: 11 12 13 14 15 16 17 18 19 20 21 55 56 57 58 59 60 61 62 63 64 65
  node 1 size: 96763 MB
  node 1 free: 94560 MB
  node 2 cpus: 22 23 24 25 26 27 28 29 30 31 32 66 67 68 69 70 71 72 73 74 75 76
  node 2 size: 96763 MB
  node 2 free: 96544 MB
  node 3 cpus: 33 34 35 36 37 38 39 40 41 42 43 77 78 79 80 81 82 83 84 85 86 87
  node 3 size: 96565 MB
  node 3 free: 96360 MB
  node distances: node 0 1 2 3
```

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Platform Notes (Continued)

<table>
<thead>
<tr>
<th>0:</th>
<th>10</th>
<th>21</th>
<th>31</th>
<th>31</th>
</tr>
</thead>
<tbody>
<tr>
<td>1:</td>
<td>21</td>
<td>10</td>
<td>31</td>
<td>31</td>
</tr>
<tr>
<td>2:</td>
<td>31</td>
<td>31</td>
<td>10</td>
<td>21</td>
</tr>
<tr>
<td>3:</td>
<td>31</td>
<td>31</td>
<td>21</td>
<td>10</td>
</tr>
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</table>

From /proc/meminfo
MemTotal: 395613340 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 15

From /etc/*release* /etc/*version*
os-release:
NAME="SLES"
VERSION="15"
VERSION_ID="15"
PRETTY_NAME="SUSE Linux Enterprise Server 15"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
Linux linux-ta8j 4.12.14-23-default #1 SMP Tue May 29 21:04:44 UTC 2018 (cd0437b)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 Jun 18 08:29

SPEC is set to: /home/cpu2017
  Filesystem  Type  Size  Used  Avail Use% Mounted on
  /dev/sdb1   btrfs 895G  57G  837G  7%  /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U30 04/18/2019
Memory:

(Continued on next page)
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**SPECrate2017_fp_peak =** Not Run

**Platform Notes (Continued)**

24x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2933  
(End of data from sysinfo program)

**Compiler Version Notes**

```
CC  519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)  
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

CXXC 508.namd_r(base) 510.parest_r(base)  
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

CC  511.povray_r(base) 526.blender_r(base)  
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

FC  507.cactuBSSN_r(base)  
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Compiler Version Notes (Continued)

==============================================================================
FC  503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
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Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
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------------------------------------------------------------------------------

==============================================================================
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Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

503.bwaves_r: -DSPEC_LP64

(Continued on next page)
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Base Portability Flags (Continued)

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508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
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Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
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The flags files that were used to format this result can be browsed at:
- [http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.html](http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.html)

You can also download the XML flags sources by saving the following links:
- [http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.xml](http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-06-18 08:31:23-0400.