# SPEC CPU®2017 Floating Point Rate Result

## ASUSTeK Computer Inc.

**ASUS ESC8000 G4(Z11PG-D24) Server System (2.30 GHz, Intel Xeon Gold 5218)**

<table>
<thead>
<tr>
<th>Test Date:</th>
<th>Jun-2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Apr-2019</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>May-2019</td>
</tr>
</tbody>
</table>

### SPECrate®2017_fp_base = 189

### SPECrate®2017_fp_peak = 199

### CPU2017 License: 9016

### Test Sponsor: ASUSTeK Computer Inc.

### Tested by: ASUSTeK Computer Inc.

## Hardware

### CPU Name: Intel Xeon Gold 5218

- **Max MHz:** 3900
- **Nominal:** 2300
- **Enabled:** 32 cores, 2 chips, 2 threads/core
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **Cache L2:** 1 MB I+D on chip per core
- **Cache L3:** 22 MB I+D on chip per core
- **Other:** None
- **Memory:** 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R, running at 2666)
- **Storage:** 1 x 1 TB SATA SSD
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 15
- **Compiler:** C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux;
  Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux
- **Parallel:** No
- **Firmware:** Version 5102 released Feb-2019
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** None
- **Power Management:** --
**SPEC CPU®2017 Floating Point Rate Result**

ASUSTeK Computer Inc.
ASUS ESC8000 G4(Z11PG-D24) Server System (2.30 GHz, Intel Xeon Gold 5218)

**SPECrate®2017_fp_base** = 189
**SPECrate®2017_fp_peak** = 199

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>64</td>
<td>1294</td>
<td>496</td>
<td>1295</td>
<td>496</td>
<td>1294</td>
<td>496</td>
<td>32</td>
<td>641</td>
<td>500</td>
<td>641</td>
<td>501</td>
<td>641</td>
<td>501</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>64</td>
<td>497</td>
<td>163</td>
<td>497</td>
<td>163</td>
<td>497</td>
<td>163</td>
<td>64</td>
<td>497</td>
<td>163</td>
<td>498</td>
<td>163</td>
<td>497</td>
<td>163</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>64</td>
<td>451</td>
<td>135</td>
<td>451</td>
<td>135</td>
<td>450</td>
<td>135</td>
<td>64</td>
<td>447</td>
<td>136</td>
<td>446</td>
<td>136</td>
<td>450</td>
<td>135</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>64</td>
<td>1503</td>
<td>111</td>
<td>1505</td>
<td>111</td>
<td>1508</td>
<td>111</td>
<td>32</td>
<td>644</td>
<td>130</td>
<td>644</td>
<td>130</td>
<td>644</td>
<td>130</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>64</td>
<td>710</td>
<td>210</td>
<td>706</td>
<td>212</td>
<td>706</td>
<td>212</td>
<td>64</td>
<td>588</td>
<td>254</td>
<td>590</td>
<td>253</td>
<td>587</td>
<td>254</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>64</td>
<td>598</td>
<td>113</td>
<td>598</td>
<td>113</td>
<td>597</td>
<td>113</td>
<td>64</td>
<td>570</td>
<td>118</td>
<td>570</td>
<td>118</td>
<td>571</td>
<td>118</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>64</td>
<td>682</td>
<td>210</td>
<td>689</td>
<td>208</td>
<td>684</td>
<td>210</td>
<td>32</td>
<td>337</td>
<td>213</td>
<td>336</td>
<td>213</td>
<td>336</td>
<td>213</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>64</td>
<td>484</td>
<td>201</td>
<td>483</td>
<td>202</td>
<td>484</td>
<td>202</td>
<td>64</td>
<td>484</td>
<td>202</td>
<td>484</td>
<td>201</td>
<td>484</td>
<td>201</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>64</td>
<td>574</td>
<td>195</td>
<td>571</td>
<td>196</td>
<td>571</td>
<td>196</td>
<td>64</td>
<td>555</td>
<td>202</td>
<td>556</td>
<td>201</td>
<td>557</td>
<td>201</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>64</td>
<td>404</td>
<td>394</td>
<td>403</td>
<td>395</td>
<td>406</td>
<td>392</td>
<td>64</td>
<td>400</td>
<td>398</td>
<td>404</td>
<td>394</td>
<td>403</td>
<td>395</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>64</td>
<td>364</td>
<td>296</td>
<td>369</td>
<td>292</td>
<td>364</td>
<td>296</td>
<td>64</td>
<td>368</td>
<td>293</td>
<td>366</td>
<td>294</td>
<td>368</td>
<td>292</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>64</td>
<td>1568</td>
<td>159</td>
<td>1572</td>
<td>159</td>
<td>1566</td>
<td>159</td>
<td>64</td>
<td>1571</td>
<td>159</td>
<td>1568</td>
<td>159</td>
<td>1566</td>
<td>159</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>64</td>
<td>1118</td>
<td>90.9</td>
<td>1117</td>
<td>91.1</td>
<td>1114</td>
<td>91.3</td>
<td>32</td>
<td>453</td>
<td>112</td>
<td>450</td>
<td>113</td>
<td>458</td>
<td>111</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**General Notes**

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/spec2017_19u4/lib/intel64"
Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
umactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configuration:
VT-d = Disabled
Patrol Scrub = Disabled
ENERGY_PERF_BIAS_CFG mode = performance
SNC = Enabled
IMC interleaving = 1-way
Engine Boost = Level3(Max)
LLC dead line alloc = Disabled
SR-IOV Support = Disabled
CSM Support = Disabled
Sysinfo program /spec2017_19u4/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-gh78 Sat Jun 29 22:33:18 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5218 CPU @ 2.30GHz
2 "physical id"s (chips)
64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Thread(s) per core: 2
Core(s) per socket: 16
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel

(Continued on next page)
ASUSTeK Computer Inc.
ASUS ESC8000 G4(Z11PG-D24) Server System
(2.30 GHz, Intel Xeon Gold 5218)

CPU2017 License: 9016
Test Sponsor: ASUSTeK Computer Inc.
Tested by: ASUSTeK Computer Inc.

SPECrate®2017_fp_base = 189
SPECrate®2017_fp_peak = 199

Test Date: Jun-2019
Hardware Availability: Apr-2019
Software Availability: May-2019

Platform Notes (Continued)

CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5218 CPU @ 2.30GHz
Stepping: 6
CPU MHz: 2300.000
CPU max MHz: 3900.0000
CPU min MHz: 1000.0000
BogoMIPS: 4600.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 22528K
NUMA node0 CPU(s): 0-3,8-11,32-35,40-43
NUMA node1 CPU(s): 4-7,12-15,36-39,44-47
NUMA node2 CPU(s): 16-19,24-27,48-51,56-59
NUMA node3 CPU(s): 20-23,28-31,60-63
Flags: fpu vme de pse tsc msr pae mce cmov
pat pse36 clflush dts acpi mmx fxsr sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref tsc_known_freq pni pclmulqdq dtex64 monitor ds_cpl vmx smx est tm2 ssse3
sdbg fma cx16 xtrr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
tsc_deadline_timer aes xsavx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault
epb cat_l3 cdp_l3 invpcid_single mba tpr_shadow vmmi flexpriority ept vpid fsgsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq
rdsseadadx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec
xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local ibpb ibrs stibp
tdtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req pku ospke avx512_vnni
arch_capabilities ssbd

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
    available: 4 nodes (0-3)
    node 0 cpus: 0 1 2 3 8 9 10 11 32 33 34 35 40 41 42 43
    node 0 size: 192062 MB
    node 0 free: 191261 MB
    node 1 cpus: 4 5 6 7 12 13 14 15 36 37 38 39 44 45 46 47
    node 1 size: 193512 MB
    node 1 free: 192789 MB
    node 2 cpus: 16 17 18 19 24 25 26 27 48 49 50 51 56 57 58 59
    node 2 size: 193483 MB
    node 2 free: 192802 MB
    node 3 cpus: 20 21 22 23 28 29 30 31 52 53 54 55 60 61 62 63
    node 3 size: 193507 MB

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SPECRate®2017_fp_base = 189
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Platform Notes (Continued)

node 3 free: 192811 MB
node distances:
node 0 1 2 3
 0: 10 11 21 21
 1: 11 10 21 21
 2: 21 21 10 11
 3: 21 21 11 10

From /proc/meminfo
MemTotal: 791107372 kB
hugePages_Total: 0
hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="SLES"
VERSION="15"
VERSION_ID="15"
PRETTY_NAME="SUSE Linux Enterprise Server 15"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
Linux linux-gh78 4.12.14-23-default #1 SMP Tue May 29 21:04:44 UTC 2018 (cd0437b)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 Jun 28 14:06

SPEC is set to: /spec2017_19u4
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 929G 15G 914G 2% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
BIOS American Megatrends Inc. 5102 02/11/2019
Memory:
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Hardware Availability: Apr-2019
Software Availability: May-2019

Platform Notes (Continued)
24x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933, configured at 2666
(End of data from sysinfo program)

Compiler Version Notes

C

| 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak) |

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

C++

| 508.namd_r(base, peak) 510.parest_r(base, peak) |

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

C++, C

| 511.povray_r(base, peak) 526.blender_r(base, peak) |

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
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Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

C++, C, Fortran

| 507.cactuBSSN_r(base, peak) |

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
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Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
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Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
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Compiler Version Notes (Continued)

==============================================================================
Fortran                | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak)
                       | 554.roms_r(base, peak)
==============================================================================
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
==============================================================================
Fortran, C             | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
==============================================================================
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
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Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
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Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64
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Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.ibm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte
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Peak Compiler Invocation

C benchmarks:
    icc -m64 -std=c11
C++ benchmarks:
    icpc -m64
Fortran benchmarks:
    ifort -m64

Benchmarks using both Fortran and C:
    ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
    icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
    icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

544.nab_r: Same as 538.imagick_r

C++ benchmarks:

508.namd_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

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Copyright 2017-2020 Standard Performance Evaluation Corporation

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Tested by: ASUSTeK Computer Inc.

SPECrate®2017_fp_base = 189
Test Date: Jun-2019
Hardware Availability: Apr-2019

SPECrate®2017_fp_peak = 199
Software Availability: May-2019

Peak Optimization Flags (Continued)

510.parest_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

Fortran benchmarks:

503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -auto
-nostandard-realloc-lhs -align array32byte

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:

-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:

511.povray_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

526.blender_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
<table>
<thead>
<tr>
<th></th>
<th>SPECrate®2017_fp_base = 189</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>SPECrate®2017_fp_peak = 199</td>
</tr>
</tbody>
</table>

<p>| | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU2017 License:</td>
<td>9016</td>
</tr>
<tr>
<td>Test Sponsor:</td>
<td>ASUSTeK Computer Inc.</td>
</tr>
<tr>
<td>Tested by:</td>
<td>ASUSTeK Computer Inc.</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Jun-2019</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Apr-2019</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>May-2019</td>
</tr>
</tbody>
</table>

ASUSTeK Computer Inc.
ASUS ESC8000 G4(Z11PG-D24) Server System (2.30 GHz, Intel Xeon Gold 5218)

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