## SPEC® CPU2017 Integer Rate Result

**ASUSTeK Computer Inc.**

ASUS RS100-E10(P11C-M/4L) Server System (3.30 GHz, Intel Xeon E-2124)

### SPECrate2017_int_base = 27.3

### SPECrate2017_int_peak = 28.1

<table>
<thead>
<tr>
<th>Test Sponsor:</th>
<th>ASUSTeK Computer Inc.</th>
<th>Hardware Availability:</th>
<th>Jun-2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tested by:</td>
<td>ASUSTeK Computer Inc.</td>
<td>Software Availability:</td>
<td>May-2019</td>
</tr>
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</table>

<table>
<thead>
<tr>
<th>Software Availability:</th>
<th>Jun-2019</th>
<th>Software Availability:</th>
<th>May-2019</th>
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</thead>
</table>

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>9016</th>
<th>Test Date:</th>
<th>Jul-2019</th>
</tr>
</thead>
</table>

### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon E-2124</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz.:</td>
<td>4300</td>
</tr>
<tr>
<td>Nominal:</td>
<td>3300</td>
</tr>
<tr>
<td>Enabled:</td>
<td>4 cores, 1 chip</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1 chip</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>256 KB I+D on chip per core</td>
</tr>
<tr>
<td>L3:</td>
<td>8 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>64 GB (4 x 16 GB 2Rx8 PC4-2666V-E)</td>
</tr>
<tr>
<td>Storage:</td>
<td>1 x 500 GB SATA HDD, 7200RPM</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>OS:</th>
<th>SUSE Linux Enterprise Server 15</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 19.0.4.227 of Intel C/C++</td>
</tr>
<tr>
<td>Firmware:</td>
<td>Version 0703 released Jun-2019</td>
</tr>
<tr>
<td>File System:</td>
<td>xfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other:</td>
<td>jemalloc: jemalloc memory allocator library V5.0.1</td>
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<table>
<thead>
<tr>
<th>Compilation:</th>
<th>Intel C/C++ Compiler Build 20190416 for Linux; Intel Fortran Compiler Build 20190416 for Linux</th>
</tr>
</thead>
<tbody>
<tr>
<td>Parallel:</td>
<td>No</td>
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<tr>
<td>File System:</td>
<td>xfs</td>
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<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
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<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
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<td>Peak Pointers:</td>
<td>32/64-bit</td>
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<td>jemalloc: jemalloc memory allocator library V5.0.1</td>
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---

500.perlbench_r 4
502.gcc_r 4
505.mcf_r 4
520.omnetpp_r 4
523.xalancbmk_r 4
525.x264_r 4
531.deepsjeng_r 4
541.leela_r 4
548.exchange2_r 4
557.xz_r 4

SPECrate2017_int_base (27.3)  
SPECrate2017_int_peak (28.1)
## SPEC CPU2017 Integer Rate Result

**ASUSTeK Computer Inc.**  
**ASUS RS100-E10(P11C-M/4L) Server System**  
(3.30 GHz, Intel Xeon E-2124)  

**SPECrate2017_int_base = 27.3**  
**SPECrate2017_int_peak = 28.1**

---

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Ratio</th>
<th>Peak</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>247</td>
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<td>505.mcf_r</td>
<td>4</td>
<td>197</td>
<td>32.8</td>
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<tr>
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<tr>
<td>548.exchange2_r</td>
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<td></td>
<td></td>
</tr>
</tbody>
</table>

**SPECrate2017_int_base = 27.3**  
**SPECrate2017_int_peak = 28.1**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/spec2017_19u4/lib/intel64:/spec2017_19u4/lib/ia32:
/spec2017_19u4/je5.0.1-32"
```

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.5  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
```
sync; echo 3>/proc/sys/vm/drop_caches
```

jemalloc: configured and built at default for 32bit (i648) and 64bit (x86_64) targets;  
jemalloc: built with the RedHat Enterprise 7.4,  
and the system compiler gcc 4.8.5;  
jemalloc: sources available from jemalloc.net or  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)

(Continued on next page)
### General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

### Platform Notes

**BIOS Configuration:**
- VT-d = Disabled
- AES = Disabled
- Hardware Prefetcher = Disabled
- Adjacent Cache Line Prefetch = Disabled
- Race to Halt (RTH) = Disabled

**Sysinfo program /spec2017_19u4/bin/sysinfo**
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-ngvl Thu Jul 11 10:29:48 2019

**SUT (System Under Test) info as seen by some common utilities.**
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) E-2124 CPU @ 3.30GHz
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 4
  - siblings : 4
  - physical 0: cores 0 1 2 3

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 4
- On-line CPU(s) list: 0-3
- Thread(s) per core: 1
- Core(s) per socket: 4
- Socket(s): 1
- NUMA node(s): 1
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 158
- Model name: Intel(R) Xeon(R) E-2124 CPU @ 3.30GHz

(Continued on next page)
ASUSTeK Computer Inc.  
ASUS RS100-E10(P11C-M/4L) Server System  
(3.30 GHz, Intel Xeon E-2124)  

SPEC CPU2017 License: 9016  
Test Sponsor: ASUSTeK Computer Inc.  
Tested by: ASUSTeK Computer Inc.  

Test Date: Jul-2019  
Hardware Availability: Jun-2019  
Software Availability: May-2019

**SPECrate2017_int_base = 27.3**  
**SPECrate2017_int_peak = 28.1**

---

**Platform Notes (Continued)**

```
Stepping: 10  
CPU MHz: 3300.000  
CPU max MHz: 4300.0000  
CPU min MHz: 800.0000  
BogoMIPS: 6624.00  
Virtualization: VT-x  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 256K  
L3 cache: 8192K  
NUMA node0 CPU(s): 0-3  
Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb invpcid_single pti ssbd ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2  
```

```
From /proc/cpuinfo cache data  
cache size : 8192 KB
```

```
WARNING: a numactl 'node' might or might not correspond to a physical chip.  
available: 1 nodes (0)  
node 0 cpus: 0 1 2 3  
node 0 size: 64323 MB  
node 0 free: 63828 MB  
node distances:  
node 0 0: 10
```

```
From /proc/meminfo  
MemTotal: 65867300 kB  
HugePages_Total: 0  
Hugepagesize: 2048 kB
```

```
From /etc/*release* /etc/*version*  
os-release:  
NAME="SLES"  
VERSION="15"  
VERSION_ID="15"  
PRETTY_NAME="SUSE Linux Enterprise Server 15"  
ID="sles"
```

(Continued on next page)
ASUSTeK Computer Inc.  
ASUS RS100-E10(P11C-M/4L) Server System  
(3.30 GHz, Intel Xeon E-2124)

**SPEC CPU2017 Integer Rate Result**

*Copyright 2017-2019 Standard Performance Evaluation Corporation*

**ASUSTeK Computer Inc.**  
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---

**Platform Notes (Continued)**

```
ID_LIKE="suse"  
ANSI_COLOR="0;32"  
CPE_NAME="cpe:/o:suse:sles:15"
```

```
uname -a:  
  Linux linux-ngvl 4.12.14-150.17-default #1 SMP Thu May 2 15:15:46 UTC 2019 (bf13fb8)  
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

- CVE-2017-5754 (Meltdown): Mitigation: PTI
- CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
- CVE-2017-5715 (Spectre variant 2): Mitigation: Full generic retpoline, IBPB: conditional, IBRS_FW, STIBP: disabled, RSB filling

```
rn-level 3 Jul 11 10:28
```

SPEC is set to: /spec2017_19u4

```
Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda4      xfs   442G   23G  419G   6% /
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

- BIOS American Megatrends Inc. 0703 06/13/2019
- Memory:  
  4x Samsung M391A2K43BB1-CTD 16 GB 2 rank 2667, configured at 2666

(End of data from sysinfo program)

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**Compiler Version Notes**

```
CC   502.gcc_r(peak)□
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version  
19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
```
ASUSTeK Computer Inc.
ASUS RS100-E10(P11C-M/4L) Server System
(3.30 GHz, Intel Xeon E-2124)

SPECrates2017_int_base = 27.3
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CPU2017 License: 9016
Test Sponsor: ASUSTeK Computer Inc.
Tested by: ASUSTeK Computer Inc.

Compiler Version Notes (Continued)

Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

CC  500.perlbench_r(peak)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

CXXC 523.xalancbmk_r(peak)
Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version
19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

CXXC 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base,
peak) 541.leela_r(base, peak)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

FC 548.exchange2_r(base, peak)
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

(Continued on next page)
## Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort -m64

### Base Portability Flags

- 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
- 502.gcc_r: -DSPEC_LP64
- 505.mcf_r: -DSPEC_LP64
- 520.omnetpp_r: -DSPEC_LP64
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
- 525.x264_r: -DSPEC_LP64
- 531.deepsjeng_r: -DSPEC_LP64
- 541.leela_r: -DSPEC_LP64
- 548.exchange2_r: -DSPEC_LP64
- 557.xz_r: -DSPEC_LP64

### Base Optimization Flags

#### C benchmarks:
-W1, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

#### C++ benchmarks:
-W1, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

#### Fortran benchmarks:
-W1, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64 -std=c11

(Continued on next page)
ASUSTeK Computer Inc.
ASUS RS100-E10(P11C-M/4L) Server System
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CPU2017 License: 9016
Test Sponsor: ASUSTeK Computer Inc.
Tested by: ASUSTeK Computer Inc.

Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):
icpc -m64
523.xalancbmk_r: icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/ia32_lin
Fortran benchmarks:
ifort -m64

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc
502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc
505.mcf_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64

(Continued on next page)
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SPECrate2017_int_base = 27.3
SPECrate2017_int_peak = 28.1

Peak Optimization Flags (Continued)

505.mcf_r (continued):
-1lqkmalloc

525.x264_r -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-ipo-opt-mem-layout-trans=4 -fno-alias
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-1lqkmalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-ipo-opt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-1lqkmalloc

523.xalancbmk_r -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-ipo-xCORE-AVX2 -O3 -no-prec-div -ipo-opt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -1jemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-ipo-opt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-1lqkmalloc

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/ASUSTeKPlatform-Settings-p11-V2.0-revB.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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