**Lenovo Global Technology**

**ThinkSystem SR950**

(2.50 GHz, Intel Xeon Gold 5215L)

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
<th>235</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_int_peak</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Jul-2019  
**Hardware Availability:** Apr-2019  
**Software Availability:** Nov-2018

<table>
<thead>
<tr>
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</tr>
</thead>
<tbody>
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<td>80</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>80</td>
</tr>
<tr>
<td>505.mcf_r</td>
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</tr>
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<td>80</td>
</tr>
<tr>
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<td>80</td>
</tr>
<tr>
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<td>80</td>
</tr>
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</tr>
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<td>80</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>80</td>
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**Hardware**

- **CPU Name:** Intel Xeon Gold 5215L  
- **Max MHz.:** 3400  
- **Nominal:** 2500  
- **Enabled:** 40 cores, 4 chips, 2 threads/core  
- **Orderable:** 2,3,4 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **Cache L2:** 1 MB I+D on chip per core  
- **Cache L3:** 13.75 MB I+D on chip per chip  
- **Memory:** 768 GB (48 x 16 GB 2Rx8 PC4-2933Y-R, running at 2666)  
- **Storage:** 1 x 800 GB SATA SSD  
- **Other:** None

**Software**

- **OS:** SUSE Linux Enterprise Server 15 (x86_64)  
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++  
  - Compiler Build 20181018 for Linux;  
  - Fortran: Version 19.0.1.144 of Intel Fortran  
  - Compiler Build 20181018 for Linux  
- **Parallel:** No  
- **Firmware:** Lenovo BIOS Version PSE122N 1.50 released May-2019 tested as PSE121N 1.50 Apr-2019  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** Not Applicable  
- **Other:** None
Lenovo Global Technology
ThinkSystem SR950
(2.50 GHz, Intel Xeon Gold 5215L)

SPECrate2017_int_base = 235
SPECrate2017_int_peak = Not Run

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>80</td>
<td>697</td>
<td>183</td>
<td>704</td>
<td>181</td>
<td>699</td>
<td>182</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>80</td>
<td>664</td>
<td>171</td>
<td>645</td>
<td>176</td>
<td>643</td>
<td>176</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>80</td>
<td>416</td>
<td>311</td>
<td>416</td>
<td>311</td>
<td>416</td>
<td>310</td>
</tr>
<tr>
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<td>80</td>
<td>650</td>
<td>162</td>
<td>651</td>
<td>161</td>
<td>653</td>
<td>161</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>80</td>
<td>312</td>
<td>271</td>
<td>313</td>
<td>270</td>
<td>312</td>
<td>271</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>80</td>
<td>301</td>
<td>465</td>
<td>299</td>
<td>469</td>
<td>300</td>
<td>466</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>80</td>
<td>459</td>
<td>200</td>
<td>460</td>
<td>199</td>
<td>460</td>
<td>200</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>80</td>
<td>710</td>
<td>187</td>
<td>712</td>
<td>186</td>
<td>700</td>
<td>189</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>80</td>
<td>493</td>
<td>425</td>
<td>494</td>
<td>424</td>
<td>494</td>
<td>425</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>80</td>
<td>543</td>
<td>159</td>
<td>543</td>
<td>159</td>
<td>543</td>
<td>159</td>
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SPECrate2017_int_base = 235
SPECrate2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

General Notes
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0u1/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

(Continued on next page)
## Lenovo Global Technology

**ThinkSystem SR950**  
(2.50 GHz, Intel Xeon Gold 5215L)  

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| SPECrate2017_int_base = | 235 |  |
| SPECrate2017_int_peak = | Not Run |  |

### General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

### Platform Notes

**BIOS configuration:**
Choose Operating Mode set to Maximum Performance  
Choose Operating Mode set to Custom Mode  
DCU Streamer Prefetcher set to Disable  
MONITOR/MWAIT set to Enable  
Sysinfo program /home/cpu2017-1.0.5-ic19.0u1/bin/sysinfo  
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9  
running on linux-u1b8 Tue Jul 30 18:36:56 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

**From /proc/cpuinfo**
```
model name : Intel(R) Xeon(R) Gold 5215L CPU @ 2.50GHz
  4 "physical id"s (chips)
  80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 20
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12
  physical 2: cores 0 1 2 3 4 8 9 10 11 12
  physical 3: cores 0 1 2 3 4 8 9 10 11 12
```

**From lscpu:**
```
Architecture:       x86_64
CPU op-mode(s):    32-bit, 64-bit
Byte Order:        Little Endian
CPU(s):            80
On-line CPU(s) list: 0-79
Thread(s) per core: 2
Core(s) per socket: 10
Socket(s):         4
NUMA node(s):      4
Vendor ID:         GenuineIntel
CPU family:        6
```
SPEC CPU2017 Integer Rate Result

Lenovo Global Technology
ThinkSystem SR950
(2.50 GHz, Intel Xeon Gold 5215L)

SPECrate2017_int_base = 235
SPECrate2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Model: 85
Model name: Intel(R) Xeon(R) Gold 5215L CPU @ 2.50GHz
Stepping: 6
CPU MHz: 2500.000
CPU max MHz: 3400.0000
CPU min MHz: 1000.0000
BogoMIPS: 5000.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 14080K
NUMA node0 CPU(s): 0-9, 40-49
NUMA node1 CPU(s): 10-19, 50-59
NUMA node2 CPU(s): 20-29, 60-69
NUMA node3 CPU(s): 30-39, 70-79
Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single ssbd mba ibrs ibpb tpr_shadow vnumi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erts invpcid rtm cqm mpx rdtx a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsaves xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local dtherm ida arat pln pts pku ospke avx512_vnni flush_l1d arch_capabilities

/proc/cpuinfo cache data
cache size : 14080 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 40 41 42 43 44 45 46 47 48 49
node 0 size: 193118 MB
node 0 free: 189485 MB
node 1 cpus: 10 11 12 13 14 15 16 17 18 19 50 51 52 53 54 55 56 57 58 59
node 1 size: 193486 MB
node 1 free: 193195 MB
node 2 cpus: 20 21 22 23 24 25 26 27 28 29 60 61 62 63 64 65 66 67 68 69
node 2 size: 193515 MB
node 2 free: 193178 MB
node 3 cpus: 30 31 32 33 34 35 36 37 38 39 70 71 72 73 74 75 76 77 78 79
node 3 size: 193512 MB
node 3 free: 193212 MB
node distances:

(Continued on next page)
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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
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Software Availability: Nov-2018

Platform Notes (Continued)

node 0 1 2 3
  0: 10 31 21 21
  1: 31 10 21 21
  2: 21 21 10 31
  3: 21 21 31 10

From /proc/meminfo
MemTotal:       792200080 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
NAME="SLES"
VERSION="15"
VERSION_ID="15"
PRETTY_NAME="SUSE Linux Enterprise Server 15"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
Linux linux-u1b8 4.12.14-25.13-default #1 SMP Tue Aug 14 15:07:35 UTC 2018 (947aa51)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 Jul 30 18:36

SPEC is set to: /home/cpu2017-1.0.5-ic19.0u1
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2     xfs   744G  33G  711G   5% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[PSE121N-1.50]- 04/22/2019
Memory:
48x NO DIMM NO DIMM
48x Samsung M393A2K43CB2-CVF 16 GB 2 rank 2933, configured at 2666

(Continued on next page)
Lenovo Global Technology
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SPECrater2017_int_base = 235
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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jul-2019
Hardware Availability: Apr-2019
Software Availability: Nov-2018

Platform Notes (Continued)

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
557.xz_r(base)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
541.leela_r(base)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
FC 548.exchange2_r(base)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64
SPEC CPU2017 Integer Rate Result

Lenovo Global Technology
ThinkSystem SR950
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Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-1qkmalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-1qkmalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-1qkmalloc

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-D.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-D.xml
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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-07-30 06:36:55-0400.
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