Lenovo Global Technology
ThinkSystem SR850
(2.30 GHz, Intel Xeon Gold 6252N)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>9.70</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_int_peak</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

<table>
<thead>
<tr>
<th>Threads</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
</tr>
<tr>
<td>0</td>
</tr>
<tr>
<td>2.00</td>
</tr>
<tr>
<td>4.00</td>
</tr>
<tr>
<td>6.00</td>
</tr>
<tr>
<td>8.00</td>
</tr>
<tr>
<td>10.0</td>
</tr>
<tr>
<td>12.0</td>
</tr>
<tr>
<td>14.0</td>
</tr>
<tr>
<td>16.0</td>
</tr>
<tr>
<td>18.0</td>
</tr>
<tr>
<td>20.0</td>
</tr>
<tr>
<td>22.0</td>
</tr>
<tr>
<td>24.0</td>
</tr>
<tr>
<td>25.0</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base (9.70)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.0</td>
</tr>
<tr>
<td>2.0</td>
</tr>
<tr>
<td>4.0</td>
</tr>
<tr>
<td>6.0</td>
</tr>
<tr>
<td>8.0</td>
</tr>
<tr>
<td>10.0</td>
</tr>
<tr>
<td>12.0</td>
</tr>
<tr>
<td>14.0</td>
</tr>
<tr>
<td>16.0</td>
</tr>
<tr>
<td>18.0</td>
</tr>
<tr>
<td>20.0</td>
</tr>
<tr>
<td>22.0</td>
</tr>
<tr>
<td>24.0</td>
</tr>
<tr>
<td>25.0</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name: Intel Xeon Gold 6252N</td>
</tr>
<tr>
<td>Max MHz: 3600</td>
</tr>
<tr>
<td>Nominal: 2300</td>
</tr>
<tr>
<td>Enabled: 96 cores, 4 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable: 2,4 chips</td>
</tr>
<tr>
<td>Cache L1: 32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2: 1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3: 35.75 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other: None</td>
</tr>
<tr>
<td>Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2933Y-R)</td>
</tr>
<tr>
<td>Storage: 1 x 800 GB SATA SSD</td>
</tr>
<tr>
<td>Other: None</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS: SUSE Linux Enterprise Server 12 SP4 (x86_64)</td>
</tr>
<tr>
<td>Kernel 4.12.14-94.41-default</td>
</tr>
<tr>
<td>Compiler: C/C++: Version 19.0.4.227 of Intel</td>
</tr>
<tr>
<td>C/C++ Compiler for Linux;</td>
</tr>
<tr>
<td>Fortran: Version 19.0.4.227 of Intel Fortran</td>
</tr>
<tr>
<td>Compiler for Linux</td>
</tr>
<tr>
<td>Parallel: Yes</td>
</tr>
<tr>
<td>Firmware: Lenovo BIOS Version TEE142E 2.30 released Aug-2019 tested as TEE141E 2.30 Jul-2019</td>
</tr>
<tr>
<td>File System: btrfs</td>
</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>Peak Pointers: Not Applicable</td>
</tr>
<tr>
<td>Other: jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management: --</td>
</tr>
</tbody>
</table>
Lenovo Global Technology
ThinkSystem SR850
(2.30 GHz, Intel Xeon Gold 6252N)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Aug-2019
Hardware Availability: Jul-2019
Software Availability: May-2019

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Base Threads</th>
<th>Base Seconds</th>
<th>Base Ratio</th>
<th>Peak Seconds</th>
<th>Peak Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>192</td>
<td>281</td>
<td>279</td>
<td>6.33</td>
<td>280</td>
<td>6.34</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>192</td>
<td>427</td>
<td>428</td>
<td>9.33</td>
<td>419</td>
<td>9.49</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>192</td>
<td>401</td>
<td>400</td>
<td>11.8</td>
<td>401</td>
<td>11.8</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>192</td>
<td>192</td>
<td>191</td>
<td>8.50</td>
<td>191</td>
<td>8.54</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>192</td>
<td>124</td>
<td>123</td>
<td>11.4</td>
<td>123</td>
<td>11.5</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>192</td>
<td>132</td>
<td>132</td>
<td>13.4</td>
<td>131</td>
<td>13.4</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>192</td>
<td>288</td>
<td>287</td>
<td>4.98</td>
<td>287</td>
<td>4.99</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>192</td>
<td>388</td>
<td>388</td>
<td>4.40</td>
<td>388</td>
<td>4.40</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>192</td>
<td>192</td>
<td>193</td>
<td>15.3</td>
<td>193</td>
<td>15.2</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>192</td>
<td>258</td>
<td>257</td>
<td>24.0</td>
<td>258</td>
<td>24.0</td>
</tr>
</tbody>
</table>

SPECspeed®2017_int_base = 9.70
SPECspeed®2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

General Notes
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0u4/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017-1.0.5-ic19.0u4/je5.0.1-64"
OMP_STACKSIZE = "192M"
Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3> /proc/sys/vm/drop_caches
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Lenovo Global Technology
ThinkSystem SR850
(2.30 GHz, Intel Xeon Gold 6252N)

SPECspeed®2017_int_base = 9.70
SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Aug-2019
Hardware Availability: Jul-2019
Software Availability: May-2019

General Notes (Continued)


Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Choose Operating Mode set to Custom Mode
C-states set to Legacy
Trusted Execution Technology set to Enable
Sysinfo program /home/cpu2017-1.0.5-ic19.0u4/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-9o83 Thu Aug 15 18:10:57 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6252N CPU @ 2.30GHz
 4 "physical id"s (chips)
 192 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 48
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 192
On-line CPU(s) list: 0-191
Thread(s) per core: 2
Core(s) per socket: 24
Socket(s): 4
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6252N CPU @ 2.30GHz
Stepping: 7
CPU MHz: 2300.000

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR850
(2.30 GHz, Intel Xeon Gold 6252N)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

SPECspeed®2017_int_base = 9.70
SPECspeed®2017_int_peak = Not Run

Test Date: Aug-2019
Hardware Availability: Jul-2019
Software Availability: May-2019

---

Platform Notes (Continued)

CPU max MHz: 3600.0000
CPU min MHz: 1000.0000
BogoMIPS: 4600.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0-23,96-119
NUMA node1 CPU(s): 24-47,120-143
NUMA node2 CPU(s): 48-71,144-167
NUMA node3 CPU(s): 72-95,168-191
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtps
lm constant_tsc art arch_perfmon pebs rep_good nopl xtopology nonstop_tsc cpuid
aperture_sizes pi pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcml pcid dca sse4_1 l sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_ppin ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept
vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erts invpcid rtm cmq mxr rdt_a
avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
xsaveopt xsavec xsaves cmq_llc cmq_occum_llc cmq_mbm_total cmq_mbm_local
dtherm ida arat pln pks pkup ospe avx512_vnni flush_lld arch_capabilities

/proc/cpuinfo cache data
  cache size : 36608 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 96 97 98 99
100 101 102 103 104 105 106 107 108 109 110 111 112 113 114 115 116 117 118 119
node 0 size: 386657 MB
node 0 free: 386128 MB
node 1 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47
120 121 122 123 124 125 126 127 128 129 130 131 132 133 134 135 136 137 138 139 140 141
142 143
node 1 size: 387052 MB
node 1 free: 386253 MB
node 2 cpus: 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71
144 145 146 147 148 149 150 151 152 153 154 155 156 157 158 159 160 161 162 163 164 165
166 167
node 2 size: 387023 MB
node 2 free: 386714 MB
node 3 cpus: 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95
168 169 170 171 172 173 174 175 176 177 178 179 180 181 182 183 184 185 186 187 188 189
190 191

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR850
(2.30 GHz, Intel Xeon Gold 6252N)

SPECspeed®2017_int_base = 9.70
SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Platform Notes (Continued)

node 3 size: 387049 MB
node 3 free: 386614 MB
node distances:
node 0 1 2 3
 0: 10 21 21 31
 1: 21 10 31 21
 2: 21 31 10 21
 3: 31 21 21 10

From /proc/meminfo
MemTotal: 1584930108 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 4
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP4"
    VERSION_ID="12.4"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP4"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp4"

uname -a:
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 Aug 15 18:06

SPEC is set to: /home/cpu2017-1.0.5-ic19.0u4

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR850
(2.30 GHz, Intel Xeon Gold 6252N)

SPECSpeed®2017_int_base = 9.70
SPECSpeed®2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Platform Notes (Continued)
Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
BIOS Lenovo -[TEE141E-2.30]- 07/02/2019
Memory:
48x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

Compiler Version Notes
==============================================================================
C | 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base)
   625.x264_s(base) 657.xz_s(base)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
C++ | 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
    641.leela_s(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
Fortran | 648.exchange2_s(base)
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
   64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Base Compiler Invocation
C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

(Continued on next page)
SPEC CPU®2017 Integer Speed Result
Copyright 2017-2019 Standard Performance Evaluation Corporation

Lenovo Global Technology
ThinkSystem SR850
(2.30 GHz, Intel Xeon Gold 6252N)

SPECspeed®2017_int_base = 9.70
SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology
Test Date: Aug-2019
Hardware Availability: Jul-2019
Software Availability: May-2019

Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort -m64

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-D.html
Lenovo Global Technology
ThinkSystem SR850
(2.30 GHz, Intel Xeon Gold 6252N)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

SPECs®2017_int_base = 9.70
SPECs®2017_int_peak = Not Run

Test Date: Aug-2019
Hardware Availability: Jul-2019
Software Availability: May-2019

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ClX-D.xml

SPEC CPU and SPECs® are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.0.5 on 2019-08-15 06:10:57-0400.
Report generated on 2019-09-03 14:47:00 by CPU2017 PDF formatter v6255.
Originally published on 2019-09-03.