# SPEC CPU®2017 Integer Rate Result

## ASUSTeK Computer Inc.

ASUS RS100-E10(P11C-M/4L) Server System  
(3.50 GHz, Intel Xeon E-2146G)

**SPECrate®2017_int_base = 47.0**  
**SPECrate®2017_int_peak = 49.2**

### Hardware
- **CPU Name:** Intel Xeon E-2146G
- **Max MHz:** 4500
- **Nominal:** 3500
- **Enabled:** 6 cores, 1 chip, 2 threads/core
- **Orderable:** 1 chip
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 256 KB I+D on chip per core
- **L3:** 12 MB I+D on chip per chip
- **Other:** None
- **Memory:** 64 GB (4 x 16 GB 2Rx8 PC4-2666V-E)
- **Storage:** 1 x 500 GB SATA HDD, 7200RPM
- **Other:** None

### Software
- **OS:** SUSE Linux Enterprise Server 15
- **Kernel:** 4.12.14-150.17-default
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++ Compiler Build 20181018 for Linux; Fortran: Version 19.0.1.144 of Intel Fortran Compiler Build 20181018 for Linux
- **Parallel:** No
- **Firmware:** Version 0703 released Jun-2019
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 32/64-bit
- **Other:** jemalloc: jemalloc memory allocator library V5.0.1
- **Power Management:** --

---

Test Date: Jun-2019  
Hardware Availability: Jun-2019  
Software Availability: May-2019

The table below shows the results for each SPEC benchmark:

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>12</td>
<td>40.3</td>
<td>47.1</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>12</td>
<td>40.4</td>
<td>48.4</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>12</td>
<td>48.4</td>
<td>58.0</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>12</td>
<td>24.1</td>
<td>57.9</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>12</td>
<td>24.1</td>
<td>52.4</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>12</td>
<td>24.1</td>
<td>59.6</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>12</td>
<td>41.6</td>
<td>109</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>12</td>
<td>37.4</td>
<td>113</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>12</td>
<td>29.7</td>
<td>86.8</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>12</td>
<td>29.7</td>
<td>86.7</td>
</tr>
</tbody>
</table>

Note: SPECrate®2017_int_base = 47.0  
SPECrate®2017_int_peak = 49.2

---

SPEC CPU®2017 License: 9016  
Test Sponsor: ASUSTeK Computer Inc.  
Tested by: ASUSTeK Computer Inc.

---

Copyright 2017-2019 Standard Performance Evaluation Corporation
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>12</td>
<td>474</td>
<td>40.3</td>
<td>472</td>
<td>40.4</td>
<td>477</td>
<td>40.1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>12</td>
<td>418</td>
<td>40.7</td>
<td>423</td>
<td>40.2</td>
<td>421</td>
<td>40.4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>12</td>
<td>355</td>
<td>58.0</td>
<td>336</td>
<td>57.7</td>
<td>334</td>
<td>58.1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>12</td>
<td>653</td>
<td>24.1</td>
<td>653</td>
<td>24.1</td>
<td>654</td>
<td>24.1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>12</td>
<td>241</td>
<td>52.6</td>
<td>242</td>
<td>52.4</td>
<td>243</td>
<td>52.1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>525.x264_r</td>
<td>12</td>
<td>192</td>
<td>109</td>
<td>193</td>
<td>109</td>
<td>191</td>
<td>110</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>12</td>
<td>330</td>
<td>41.7</td>
<td>331</td>
<td>41.6</td>
<td>330</td>
<td>41.6</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>541.leela_r</td>
<td>12</td>
<td>531</td>
<td>37.4</td>
<td>531</td>
<td>37.4</td>
<td>533</td>
<td>37.3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>12</td>
<td>363</td>
<td>86.7</td>
<td>361</td>
<td>87.2</td>
<td>362</td>
<td>86.8</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>557.xz_r</td>
<td>12</td>
<td>436</td>
<td>29.7</td>
<td>436</td>
<td>29.7</td>
<td>435</td>
<td>29.8</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/spec2017/lib/ia32:/spec2017/lib/intel64:/spec2017/je5.0.1-32:
/spec2017/je5.0.1-64"
```

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

jemalloc: configured and built at default for

32bit (i686) and 64bit (x86_64) targets;

jemalloc: built with the RedHat Enterprise 7.4,

and the system compiler gcc 4.8.5;

jemalloc: sources available from jemalloc.net or


Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configuration:
VT-d = Disabled
AES = Disabled
Hardware Prefetcher = Disabled
Adjacent Cache Line Prefetch = Disabled
Race to Halt (RTH) = Disabled

Sysinfo program /spec2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-ngvl Fri Jun 21 17:46:33 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) E-2146G CPU @ 3.50GHz
 1 "physical id"s (chips)
 12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 6
  siblings : 12
  physical 0: cores 0 1 2 3 4 5

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 12
On-line CPU(s) list: 0-11
Thread(s) per core: 2
Core(s) per socket: 6
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 158
Model name: Intel(R) Xeon(R) E-2146G CPU @ 3.50GHz
Stepping: 10

(Continued on next page)
## Platform Notes (Continued)

```plaintext
CPU MHz:             3500.000
CPU max MHz:         4500.0000
CPU min MHz:         800.0000
BogoMIPS:            7008.00
Virtualization:      VT-x
L1d cache:           32K
L1i cache:           32K
L2 cache:            256K
L3 cache:            12288K
NUMA node0 CPU(s):   0-11
Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3
sdmb fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer
xsaves avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb invpcid_single pti
ssbd ibrs ibpb stibp tpr_shadow vmi flexpriority ept vpid fsgsbase tsc_adjust bmil
hle avx2 smep bmi2 erms invpcid rtm mpx rdseed adx smap clflushopt intel_pt xsaveopt
xsavec xgetbv1 xsaves dtherm ida arat pln pts hwp hwp_notify hwp_act_window hwp_epp
flush_lld

/proc/cpuinfo cache data
  cache size : 12288 KB

From numactl --hardware
  WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11
  node 0 size: 64320 MB
  node 0 free: 63052 MB
  node distances:
    node 0
      0:  10

From /proc/meminfo
  MemTotal:       65864512 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="SLES"
    VERSION="15"
    VERSION_ID="15"
    PRETTY_NAME="SUSE Linux Enterprise Server 15"
    ID="sles"
    ID_LIKE="suse"
```

(Continued on next page)
ASUSTeK Computer Inc.
ASUS RS100-E10(P11C-M/4L) Server System
(3.50 GHz, Intel Xeon E-2146G)

<table>
<thead>
<tr>
<th>CPU2017 License: 9016</th>
<th>Test Date: Jun-2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: ASUSTeK Computer Inc.</td>
<td>Hardware Availability: Jun-2019</td>
</tr>
<tr>
<td>Tested by: ASUSTeK Computer Inc.</td>
<td>Software Availability: May-2019</td>
</tr>
</tbody>
</table>

**SPECrate®2017_int_base = 47.0**

**SPECrate®2017_int_peak = 49.2**

### Platform Notes

ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
    Linux linux-ngvl 4.12.14-150.17-default #1 SMP Thu May 2 15:15:46 UTC 2019 (bf13fb8) x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full generic retpoline, IBPB: conditional, IBRS_FW, STIBP: conditional, RSB filling

run-level 3 Jun 21 11:04

SPEC is set to: /spec2017

```
Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda4      xfs   442G   19G  423G   5% /
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

- BIOS American Megatrends Inc. 0703 06/13/2019
- Memory:
  4x Samsung M391A2K43BB1-CTD 16 GB 2 rank 2667, configured at 2666

(End of data from sysinfo program)

### Compiler Version Notes

--------------------------
C         | 502.gcc_r(peak)
--------------------------

Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

--------------------------
C         | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak)
--------------------------

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018

(Continued on next page)
## Compiler Version Notes (Continued)

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

---

<table>
<thead>
<tr>
<th>C</th>
<th>502.gcc_r(peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.1.144 Build 20181018</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2018 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

---

<table>
<thead>
<tr>
<th>C</th>
<th>500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2018 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

---

<table>
<thead>
<tr>
<th>C++</th>
<th>523.xalancbmk_r(peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.1.144 Build 20181018</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2018 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

---

<table>
<thead>
<tr>
<th>C++</th>
<th>520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2018 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

---

<table>
<thead>
<tr>
<th>C++</th>
<th>523.xalancbmk_r(peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.1.144 Build 20181018</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2018 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

---

<table>
<thead>
<tr>
<th>C++</th>
<th>520.omnetpp_r(base, peak) 523.xalancbmk_r(base)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.1.144 Build 20181018</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2018 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

---

(Continued on next page)
ASUSTeK Computer Inc.  
ASUS RS100-E10(P11C-M/4L) Server System  
(3.50 GHz, Intel Xeon E-2146G)

Copyright 2017-2019 Standard Performance Evaluation Corporation

Compiler Version Notes (Continued)

<table>
<thead>
<tr>
<th>531.deepsjeng_r(base, peak) 541.leela_r(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018</td>
</tr>
<tr>
<td>Copyright (C) 1985-2018 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>------------------------------------------------------</td>
</tr>
</tbody>
</table>

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r -DSPEC_LP64
505.mcf_r -DSPEC_LP64
520.omnetpp_r -DSPEC_LP64
523.xalancmk_r -DSPEC_LP64 -DSPEC_LINUX
525.x264_r -DSPEC_LP64
531.deepsjeng_r -DSPEC_LP64
541.leela_r -DSPEC_LP64
548.exchange2_r -DSPEC_LP64
557.xz_r -DSPEC_LP64
ASUSTeK Computer Inc.
ASUS RS100-E10(P11C-M/4L) Server System
(3.50 GHz, Intel Xeon E-2146G)

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

SPECrater®2017_int_base = 47.0
SPECrater®2017_int_peak = 49.2

CPU2017 License: 9016
Test Sponsor: ASUSTeK Computer Inc.
Test Date: Jun-2019
Hardware Availability: Jun-2019
Tested by: ASUSTeK Computer Inc.
Software Availability: May-2019

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64 -std=c11


C++ benchmarks (except as noted below):
icpc -m64

523.xalancbmk_r: icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

ASUSTeK Computer Inc.
ASUS RS100-E10(P11C-M/4L) Server System (3.50 GHz, Intel Xeon E-2146G)

SPECrate®2017_int_base = 47.0
SPECrate®2017_int_peak = 49.2

ASUSTeK Computer Inc.

CPU2017 License: 9016
Test Sponsor: ASUSTeK Computer Inc.
Test Date: Jun-2019
Hardware Availability: Jun-2019
Tested by: ASUSTeK Computer Inc.
Software Availability: May-2019

Peak Portability Flags (Continued)

531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -03 -no-prec-div -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -03 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -03 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -03 -no-prec-div
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -03 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -03 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

(Continued on next page)
ASUSTeK Computer Inc.
ASUS RS100-E10(P11C-M/4L) Server System
(3.50 GHz, Intel Xeon E-2146G)

SPECrate®2017_int_base = 47.0
SPECrate®2017_int_peak = 49.2

ASUSTeK Computer Inc.

CPU2017 License: 9016
Test Sponsor: ASUSTeK Computer Inc.
Test Date: Jun-2019
Tested by: ASUSTeK Computer Inc.
Hardware Availability: Jun-2019
Software Availability: May-2019

Peak Optimization Flags (Continued)

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.0.5 on 2019-06-21 05:46:32-0400.
Report generated on 2019-09-17 16:05:30 by CPU2017 PDF formatter v6255.
Originally published on 2019-09-17.