New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 6140)  

<table>
<thead>
<tr>
<th>SPECrate®2017 int_base</th>
<th>SPECrate®2017 int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>203</td>
<td>212</td>
</tr>
</tbody>
</table>

CPU2017 License: 9066  
Test Sponsor: New H3C Technologies Co., Ltd.  
Tested by: New H3C Technologies Co., Ltd.  

<table>
<thead>
<tr>
<th>SPECrate®2017_int_peak</th>
<th>SPECrate®2017_int_base</th>
</tr>
</thead>
<tbody>
<tr>
<td>(212)</td>
<td>(203)</td>
</tr>
</tbody>
</table>

### Hardware

**CPU Name:** Intel Xeon Gold 6140  
**Max MHz:** 3700  
**Nominal:** 2300  
**Enabled:** 36 cores, 2 chips, 2 threads/core  
**Orderable:** 1.2 chips  
**Cache L1:** 32 KB I + 32 KB D on chip per core  
**L2:** 1 MB I+D on chip per core  
**L3:** 24.75 MB I+D on chip per chip  
**Other:** None  
**Memory:** 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R)  
**Storage:** 1 x 240 GB SATA SSD  
**Other:** None

### Software

**OS:** Red Hat Enterprise Linux Server release 7.6  
(Maipo)  
3.10.0-957.el7.x86_64  

**Compiler:** C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux;  
Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux

**Parallel:** No  
**Firmware:** Version 2.00.30 released Jun-2019 BIOS  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 64-bit  
**Peak Pointers:** 32/64-bit  
**Other:** jemalloc memory allocator V5.0.1  
**Power Management:** --
## New H3C Technologies Co., Ltd.

**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<tr>
<td>500.perlbench_r</td>
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<td>520.omnetpp_r</td>
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<td>706</td>
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<td>523.xalancbmk_r</td>
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</tr>
<tr>
<td>548.exchange2_r</td>
<td>72</td>
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<td>72</td>
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<td>72</td>
<td>575</td>
<td>135</td>
<td>573</td>
<td>136</td>
</tr>
</tbody>
</table>

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/lib/ia32:/home/speccpu/je5.0.1-32"
```

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM

Memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3 > /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu <etc>
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
# SPEC CPU®2017 Integer Rate Result

## New H3C Technologies Co., Ltd.

**H3C UniServer R4900 G3 (Intel Xeon Gold 6140)**

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base = 203</th>
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<td><strong>Test Date</strong>: Aug-2019</td>
</tr>
<tr>
<td><strong>Test Sponsor</strong>: New H3C Technologies Co., Ltd.</td>
<td><strong>Hardware Availability</strong>: Jul-2017</td>
</tr>
<tr>
<td><strong>Tested by</strong>: New H3C Technologies Co., Ltd.</td>
<td><strong>Software Availability</strong>: May-2019</td>
</tr>
</tbody>
</table>

### General Notes (Continued)

is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5


### Platform Notes

**BIOS Settings:**
- Set SNC to Enabled
- Set IMC Interleaving to 1-way Interleave
- Set DCU Streamer Prefetch to Disabled
- Set XPT Prefetch to Enabled

Sysinfo program /home/speccpu/bin/sysinfo

Rev: r5974 of 2018-05-19 9bcede8f2999c33d61f64985e45859ea9

running on localhost.localdomain Mon Aug 26 04:35:14 2019

**SUT (System Under Test) info as seen by some common utilities.**

For more information on this section, see

https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 6140 CPU @ 2.30GHz
  2 "physical id"s (chips)
    72 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 18
  siblings : 36
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
```

From lscpu:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                72
On–line CPU(s) list:   0–71
Thread(s) per core:    2
Core(s) per socket:    18
Socket(s):             2
NUMA node(s):          4
Vendor ID:             GenuineIntel
CPU family:            6
Model:                 85
Model name:            Intel(R) Xeon(R) Gold 6140 CPU @ 2.30GHz
Stepping:              4
```

(Continued on next page)
### SPEC CPU®2017 Integer Rate Result

New H3C Technologies Co., Ltd.

H3C UniServer R4900 G3 (Intel Xeon Gold 6140)

| SPECrate®2017_int_base = 203 |
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**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.

**Test Date:** Aug-2019  
**Hardware Availability:** Jul-2017  
**Software Availability:** May-2019

| CPU MHz:               | 999.932 |
| CPU max MHz:           | 3700.0000 |
| CPU min MHz:           | 1000.0000 |
| BogoMIPS:              | 4600.00 |
| Virtualization:        | VT-x |
| L1d cache:             | 32K |
| L1i cache:             | 32K |
| L2 cache:              | 1024K |
| L3 cache:              | 25344K |

**NUMA node0 CPU(s):**  0-2, 5, 6, 9, 10, 14, 15, 36-38, 41, 42, 45, 46, 50, 51

**NUMA node1 CPU(s):**  3, 4, 7, 8, 11-13, 16, 17, 39, 40, 43, 44, 47-49, 52, 53

**NUMA node2 CPU(s):**  18-20, 23, 24, 27, 28, 32, 33, 34, 35, 57, 58, 61, 62, 65-67, 70, 71

**NUMA node3 CPU(s):**  21, 22, 25, 26, 29-31, 34, 35, 57, 58, 61, 62, 65-67, 70, 71

**Flags:**  
- fpu 
- vme 
- de 
- pse 
- tsc 
- msr 
- pae 
- mce 
- cx8 
- apic 
- sep 
- mtrr 
- pge 
- mca 
- cmov 
- pat 
- pse36 
- clflush 
- dts 
- acpi 
- mmx 
- fxsr 
- sse 
- sse2 
- ht 
- tm 
- pbe 
- syscall 
- nx 
- pdpe1gb 
- rdtscp 
- lm 
- constant_tsc 
- art 
- arch_perfmon 
- pebs 
- bts 
- rep_good 
- nopl 
- xtopology 
- nonstop_tsc 
- aperfmperf 
- eagerfpu 
- pni 
- pclmulqdq 
- dtes64 
- monitor 
- ds_cpl 
- vmx 
- smx 
- est 
- tm2 
- ssse3 
- sdbg 
- fma 
- cx16 
- xtrp 
- pdcm 
- pcid 
- dca 
- sse4_1 
- sse4_2 
- x2apic 
- movbe 
- popcnt 
- tsc_deadline_timer 
- aes 
- xsave 
- avx 
- f16c 
- rdrand 
- lahf_lm 
- abm 
- 3nowprefetch 
- epb 
- cat_13 
- cdp_13 
- intel_ppin 
- intel_pt 
- ssbd 
- mba 
- ibrs 
- ibpb 
- stibp 
- tpr_shadow 
- vmi 
- flexpriority 
- ept 
- vpid 
- fsqsb foundation 
- tsc_adjust 
- bmlvl 
- hle 
- avx2 
- smep 
- bmi2 
- erms 
- invpcid 
- rtm 
- cmq 
- mpx 
- rdt_a 
- avx512f 
- avx512dq 
- rdseed 
- adx 
- smap 
- clflushopt 
- clwb 
- avx512cd 
- avx512bw 
- avx512vl 
- xsaveopt 
- xsavec 
- xgetbv1 
- cmq_l1c 
- cmq_occup_llc 
- cmq_mbm_total 
- cmq_mbm_local 
- dtherm 
- ida 
- arat 
- pln 
- pts 
- hwp 
- hwp_act_window 
- hwp_epp 
- hwp_pkg_req 
- pku 
- ospke 
- spec_ctrl 
- intel_stibp 
- flush_l1d 

/proc/cpuinfo cache data

```plaintext
 cache size : 25344 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```plaintext
 available: 4 nodes (0-3)
 node 0 cpus: 0 1 2 5 6 9 10 14 15 36 37 38 41 42 45 46 50 51
 node 0 size: 195224 MB
 node 0 free: 190082 MB
 node 1 cpus: 3 4 7 8 11 12 13 16 17 39 40 43 44 47 48 49 52 53
 node 1 size: 196608 MB
 node 1 free: 192078 MB
 node 2 cpus: 18 19 20 23 24 27 28 32 33 54 55 56 59 60 63 64 68 69
 node 2 size: 196608 MB
 node 2 free: 192150 MB
 node 3 cpus: 21 22 25 26 29 30 31 34 35 57 58 61 62 65 66 67 70 71
 node 3 size: 196608 MB
 node 3 free: 192079 MB
 node distances:
 node 0 1 2 3
 0: 10 11 21 21
 1: 11 10 21 21
```

(Continued on next page)
New H3C Technologies Co., Ltd.
H3C UniServer R4900 G3 (Intel Xeon Gold 6140)

**SPEC CPU®2017 Integer Rate Result**

**CPU2017 License:** 9066
**Test Sponsor:** New H3C Technologies Co., Ltd.
**Tested by:** New H3C Technologies Co., Ltd.

**SPECrate®2017_int_base = 203**
**SPECrate®2017_int_peak = 212**

**Test Date:** Aug-2019
**Hardware Availability:** Jul-2017
**Software Availability:** May-2019

### Platform Notes (Continued)

2: 21 21 10 11
3: 21 21 11 10

From /proc/meminfo

MemTotal: 790962048 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.6 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VARIANT="Server"
VARIANT_ID="server"
VERSION_ID="7.6"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.6 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)

uname -a:
Linux localhost.localdomain 3.10.0-957.el7.x86_64 #1 SMP Thu Oct 4 20:48:51 UTC 2018
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS (kernel)

run-level 3 Aug 26 04:24

SPEC is set to: /home/speccpu

Files

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/mapper/rhel-home</td>
<td>xfs</td>
<td>169G</td>
<td>48G</td>
<td>121G</td>
<td>29%</td>
<td>/home</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 2.00.30 06/20/2019
Memory:
24x Micron 36ASF4G72PZ-2G6D1 32 GB 2 rank 2666

(End of data from sysinfo program)
## Compiler Version Notes

<table>
<thead>
<tr>
<th>Language</th>
<th>Programs</th>
</tr>
</thead>
<tbody>
<tr>
<td>C</td>
<td>502.gcc_r(peak)</td>
</tr>
<tr>
<td></td>
<td>Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.4.227 Build 20190416</td>
</tr>
<tr>
<td></td>
<td>Copyright (C) 1985-2019 Intel Corporation. All rights reserved.</td>
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<td>523.xalancbmk_r(peak)</td>
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<td>Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,</td>
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</table>
Compiler Version Notes (Continued)

Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
| C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak) |
------------------------------------------------------------------------------
| Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, | Version 19.0.4.227 Build 20190416 | Copyright (C) 1985-2019 Intel Corporation. All rights reserved. |
------------------------------------------------------------------------------

------------------------------------------------------------------------------
| Fortran | 548.exchange2_r(base, peak) |
------------------------------------------------------------------------------
| Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, | Version 19.0.4.227 Build 20190416 | Copyright (C) 1985-2019 Intel Corporation. All rights reserved. |
------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
New H3C Technologies Co., Ltd.
H3C UniServer R4900 G3 (Intel Xeon Gold 6140)

**SPECRate®2017_int_base = 203**

**SPECRate®2017_int_peak = 212**

**CPU2017 License:** 9066

**Test Sponsor:** New H3C Technologies Co., Ltd.

**Tested by:** New H3C Technologies Co., Ltd.

**Test Date:** Aug-2019

**Hardware Availability:** Jul-2017

**Software Availability:** May-2019

### Base Portability Flags (Continued)

- 502.gcc_r: -DSPEC_LP64
- 505.mcf_r: -DSPEC_LP64
- 520.omnetpp_r: -DSPEC_LP64
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
- 525.x264_r: -DSPEC_LP64
- 531.deepsjeng_r: -DSPEC_LP64
- 541.leela_r: -DSPEC_LP64
- 548.exchange2_r: -DSPEC_LP64
- 557.xz_r: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4
- -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
- -lqkmalloc

**C++ benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4
- -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
- -lqkmalloc

**Fortran benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
- -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
- -lqkmalloc

### Peak Compiler Invocation

**C benchmarks (except as noted below):**
- icc -m64 -std=c11
  

**C++ benchmarks (except as noted below):**
- icpc -m64
  
- 523.xalancbmk_r: icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/ia32_lin

(Continued on next page)
SPECCPU®2017 Integer Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.  SPECrate®2017_int_base = 203
H3C UniServer R4900 G3 (Intel Xeon Gold 6140)  SPECrate®2017_int_peak = 212

CPU2017 License: 9066  Test Date: Aug-2019
Test Sponsor: New H3C Technologies Co., Ltd.  Hardware Availability: Jul-2017
Tested by: New H3C Technologies Co., Ltd.  Software Availability: May-2019

Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifort -m64

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

(Continued on next page)
New H3C Technologies Co., Ltd.

H3C UniServer R4900 G3 (Intel Xeon Gold 6140)

**SPECrater2017_int_base = 203**

**SPECrater2017_int_peak = 212**

<table>
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<th>Test Date</th>
<th>Test Sponsor</th>
<th>Hardware Availability</th>
<th>Software Availability</th>
</tr>
</thead>
</table>

**Peak Optimization Flags (Continued)**

C++ benchmarks:

520.omnetpp_r -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64 -lqkmalloc

523.xalancbmk_r -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4 -L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:


The flags files that were used to format this result can be browsed at


http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.3-SKL-RevD.2019-09-03.html

You can also download the XML flags sources by saving the following links:


http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.3-SKL-RevD.2019-09-03.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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