# SPEC CPU®2017 Integer Rate Result

## Dell Inc.
**PowerEdge R540 (Intel Xeon Gold 5215, 2.50 GHz)**

**SPECrate®2017_int_base = 117**  
**SPECrate®2017_int_peak = 122**

### Hardware

<table>
<thead>
<tr>
<th>CPU Name</th>
<th>Intel Xeon Gold 5215</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz</td>
<td>3400</td>
</tr>
<tr>
<td>Nominal</td>
<td>2500</td>
</tr>
<tr>
<td>Enabled</td>
<td>20 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable</td>
<td>1.2 chips</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3</td>
<td>13.75 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>384 GB (12 x 32 GB 2Rx4 PC4-2933P-R, running at 2666)</td>
</tr>
<tr>
<td>Storage</td>
<td>480 GB SATA SSD</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>OS</th>
<th>Ubuntu 18.04.2 LTS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler</td>
<td>C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux: Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux</td>
</tr>
<tr>
<td>Parallel</td>
<td>No</td>
</tr>
<tr>
<td>Firmware</td>
<td>Version 2.2.11 released Jun-2019</td>
</tr>
<tr>
<td>File System</td>
<td>xfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 5 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other</td>
<td>Ubuntu 18.04.2 LTS</td>
</tr>
<tr>
<td>Power Management</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
</tbody>
</table>

### Test Information

| CPU2017 License       | 55                                    |
| Test Sponsor          | Dell Inc.                             |
| Tested by             | Dell Inc.                             |
| Test Date             | Aug-2019                              |
| Hardware Availability | Apr-2019                              |
| Software Availability | May-2019                              |

### Benchmarks

```plaintext
<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench_r</td>
<td>40</td>
<td>102</td>
<td>234</td>
</tr>
<tr>
<td>gcc_r</td>
<td>40</td>
<td>107</td>
<td>244</td>
</tr>
<tr>
<td>mcf_r</td>
<td>40</td>
<td>74.1</td>
<td>243</td>
</tr>
<tr>
<td>omnetpp_r</td>
<td>40</td>
<td>127</td>
<td>243</td>
</tr>
<tr>
<td>xalancbmk_r</td>
<td>40</td>
<td>139</td>
<td>244</td>
</tr>
<tr>
<td>x264_r</td>
<td>40</td>
<td>154</td>
<td>234</td>
</tr>
<tr>
<td>deepsjeng_r</td>
<td>40</td>
<td>98.7</td>
<td>243</td>
</tr>
<tr>
<td>leela_r</td>
<td>40</td>
<td>93.7</td>
<td>241</td>
</tr>
<tr>
<td>exchange2_r</td>
<td>40</td>
<td>77.4</td>
<td>243</td>
</tr>
<tr>
<td>xz_r</td>
<td>40</td>
<td>77.4</td>
<td>243</td>
</tr>
</tbody>
</table>
```

---

**Note:** The results are generated based on the specifications and configurations provided by Dell Inc. for the PowerEdge R540 server with Intel Xeon Gold 5215 processor. The SPEC CPU®2017 benchmarks are used to evaluate the performance of the system. The values represent the integer rate results in SPECrate® units.
SPECCPU®2017 Integer Rate Result
Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.
PowerEdge R540 (Intel Xeon Gold 5215, 2.50 GHz)  
SPECraten®2017_int_base = 117  
SPECraten®2017_int_peak = 122

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>test_int()</td>
<td>40</td>
<td>716</td>
<td>88.9</td>
<td>717</td>
<td>88.8</td>
<td>717</td>
<td>88.8</td>
</tr>
<tr>
<td>test_int()</td>
<td>40</td>
<td>609</td>
<td>92.9</td>
<td>609</td>
<td>93.0</td>
<td>609</td>
<td>93.0</td>
</tr>
<tr>
<td>test_int()</td>
<td>40</td>
<td>418</td>
<td>155</td>
<td>419</td>
<td>154</td>
<td>419</td>
<td>154</td>
</tr>
<tr>
<td>test_int()</td>
<td>40</td>
<td>708</td>
<td>74.1</td>
<td>708</td>
<td>74.2</td>
<td>708</td>
<td>74.1</td>
</tr>
<tr>
<td>test_int()</td>
<td>40</td>
<td>332</td>
<td>127</td>
<td>331</td>
<td>127</td>
<td>331</td>
<td>127</td>
</tr>
<tr>
<td>test_int()</td>
<td>40</td>
<td>300</td>
<td>234</td>
<td>300</td>
<td>234</td>
<td>300</td>
<td>234</td>
</tr>
<tr>
<td>test_int()</td>
<td>40</td>
<td>464</td>
<td>98.7</td>
<td>463</td>
<td>99.0</td>
<td>463</td>
<td>99.0</td>
</tr>
<tr>
<td>test_int()</td>
<td>40</td>
<td>722</td>
<td>91.7</td>
<td>702</td>
<td>94.4</td>
<td>702</td>
<td>94.4</td>
</tr>
<tr>
<td>test_int()</td>
<td>40</td>
<td>432</td>
<td>243</td>
<td>431</td>
<td>243</td>
<td>431</td>
<td>243</td>
</tr>
<tr>
<td>test_int()</td>
<td>40</td>
<td>558</td>
<td>77.5</td>
<td>558</td>
<td>77.4</td>
<td>558</td>
<td>77.4</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcd8f2999c33d61f64985e45859ea9
running on intel-sut Fri Aug 23 16:46:51 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5215 CPU @ 2.50GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 40
On-line CPU(s) list: 0-39
Thread(s) per core: 2
Core(s) per socket: 10
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5215 CPU @ 2.50GHz
Stepping: 6
CPU MHz: 2705.979
BogoMIPS: 5000.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K

(Continued on next page)
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| SPECrate®2017_int_base = 117 |
| SPECrate®2017_int_peak = 122 |

**CPU2017 License:** 55
**Test Sponsor:** Dell Inc.
**Tested by:** Dell Inc.
**Test Date:** Aug-2019
**Hardware Availability:** Apr-2019
**Software Availability:** May-2019

---

**Platform Notes (Continued)**

L2 cache: 1024K
L3 cache: 14080K
NUMA node0 CPU(s): 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38
NUMA node1 CPU(s): 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39
Flags: fpu vme de pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_pinn ssbd mba ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaves opt xsaveopt xsave xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pts pku ospke avx512_vnni flush_l1d arch_capabilities

/proc/cpuinfo cache data
  cache size: 14080 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38
  node 0 size: 192837 MB
  node 0 free: 192214 MB
  node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39
  node 1 size: 193531 MB
  node 1 free: 193160 MB
  node distances:
    node 0 1
    0: 10 21
    1: 21 10

From /proc/meminfo
  MemTotal: 395641848 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
  Ubuntu 18.04.2 LTS

From /etc/*release* /etc/*version*
  debian_version: buster/sid
  os-release:
    NAME="Ubuntu"
    VERSION="18.04.2 LTS (Bionic Beaver)"
  ID=ubuntu

(Continued on next page)
Dell Inc.
PowerEdge R540 (Intel Xeon Gold 5215, 2.50 GHz)

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**Hardware Availability:** Apr-2019
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**Software Availability:** May-2019

---

**Platform Notes (Continued)**

```plaintext
ID_LIKE=debian
PRETTY_NAME="Ubuntu 18.04.2 LTS"
VERSION_ID="18.04"
HOME_URL="https://www.ubuntu.com/
SUPPORT_URL="https://help.ubuntu.com/

uname -a:
    Linux intel-sut 4.15.0-45-generic #48-Ubuntu SMP Tue Jan 29 16:28:13 UTC 2019 x86_64
    x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB

run-level 5 Aug 23 16:44

SPEC is set to: /home/cpu2017
    Filesystem    Type  Size  Used Avail Use% Mounted on
    /dev/sda2      ext4  439G   21G  396G   5% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS Dell Inc. 2.2.11 06/14/2019
    Memory:
        12x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933, configured at 2666
        4x Not Specified Not Specified

(End of data from sysinfo program)
```

---

**Compiler Version Notes**

```plaintext
C       | 502.gcc_r(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version
    19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

C       | 500.perlibench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
        | 525.x264_r(base, peak) 557.xz_r(base, peak)
```

(Continued on next page)

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Standard Performance Evaluation Corporation (info@spec.org) https://www.spec.org/
## Dell Inc.

**PowerEdge R540 (Intel Xeon Gold 5215, 2.50 GHz)**

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>55</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Tested by</td>
<td>Dell Inc.</td>
</tr>
</tbody>
</table>

---

### SPEC CPU®2017 Integer Rate Result

**SPECrate®2017_int_base = 117**

**SPECrate®2017_int_peak = 122**

---

### Compiler Version Notes (Continued)

Intel(R) C 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416

Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

---

C 502.gcc_r(peak)

---

Intel(R) C 64 Compiler for applications running on IA-32, Version 19.0.4.227 Build 20190416

Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

---

C 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak)

---

Intel(R) C 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416

Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

---

C++ 523.xalancbmk_r(peak)

---

Intel(R) C++ 64 Compiler for applications running on IA-32, Version 19.0.4.227 Build 20190416

Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

---

C++ 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)

---

Intel(R) C++ 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416

Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

---

C++ 523.xalancbmk_r(peak)

---

Intel(R) C++ 64 Compiler for applications running on IA-32, Version 19.0.4.227 Build 20190416

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(Continued on next page)
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Compiler Version Notes (Continued)

==============================================================================
C++  | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
     | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
   Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
Fortran | 548.exchange2_r(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
   64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
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### Base Optimization Flags

**C benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`  
- `-qopt-mem-layout-trans=4`  
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64 -lqkmalloc`

**C++ benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`  
- `-qopt-mem-layout-trans=4`  
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64 -lqkmalloc`

**Fortran benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`  
- `-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte`  
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64 -lqkmalloc`

### Peak Compiler Invocation

**C benchmarks (except as noted below):**
- `icc `-m64 `-std=c11`
- `502.gcc_r icc `-m32 `-std=c11 `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/ia32_lin`

**C++ benchmarks (except as noted below):**
- `icpc `-m64`
- `523.xalancbmk_r icpc `-m32 `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/ia32_lin`

**Fortran benchmarks:**
- `ifort `-m64`

### Peak Portability Flags

- `500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64`
- `502.gcc_r: -D_FILE_OFFSET_BITS=64`
- `505.mcf_r: -DSPEC_LP64`
- `520.omnetpp_r: -DSPEC_LP64`
- `523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX`
- `525.x264_r: -DSPEC_LP64`

(Continued on next page)
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Peak Portability Flags (Continued)

531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -03 -no-prec-div -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/usr/local/IntelCompiler19/compilers_andLibraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -03 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/jf5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -03 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_andLibraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -03 -no-prec-div
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/IntelCompiler19/compilers_andLibraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -03 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_andLibraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -03 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/jf5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

(Continued on next page)
Dell Inc.  
PowerEdge R540 (Intel Xeon Gold 5215, 2.50 GHz)  

SPECrate®2017_int_base = 117  
SPECrate®2017_int_peak = 122

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.  
Test Date: Aug-2019  
Hardware Availability: Apr-2019  
Software Availability: May-2019

Peak Optimization Flags (Continued)

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:
-Wl, -z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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