## Lenovo Global Technology

**ThinkSystem SR950**  
(2.70 GHz, Intel Xeon Gold 6226)

| SPECspeed®2017_int_base = 9.59 | SPECspeed®2017_int_peak = Not Run |

### Hardware

<table>
<thead>
<tr>
<th>Spec</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name:</td>
<td>Intel Xeon Gold 6226</td>
</tr>
<tr>
<td>Max MHz:</td>
<td>3700</td>
</tr>
<tr>
<td>Nominal:</td>
<td>2700</td>
</tr>
<tr>
<td>Enabled:</td>
<td>48 cores, 4 chips</td>
</tr>
<tr>
<td>Orderable:</td>
<td>2,3,4 chips</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3:</td>
<td>19.25 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>768 GB (48 x 16 GB 2Rx8 PC4-2933Y-R)</td>
</tr>
<tr>
<td>Storage:</td>
<td>1 x 800 GB SATA SSD</td>
</tr>
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<td>None</td>
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### Software

<table>
<thead>
<tr>
<th>Spec</th>
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<tr>
<td>OS:</td>
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<td>Compiler:</td>
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</tr>
<tr>
<td>Parallel:</td>
<td>Yes</td>
</tr>
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<td>Firmware:</td>
<td>Lenovo BIOS Version PSE122R 1.53 released Aug-2019 tested as PSE121R 1.53 Jul-2019</td>
</tr>
<tr>
<td>File System:</td>
<td>xfs</td>
</tr>
<tr>
<td>System State:</td>
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<td>Base Pointers:</td>
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<tr>
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<tr>
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<td>jemalloc memory allocator V5.0.1</td>
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---

**Lenovo Global Technology**

**ThinkSystem SR950**  
(2.70 GHz, Intel Xeon Gold 6226)

| speculation=2017_int_base = 9.59 | speculation=2017_int_peak = Not Run |

### Threads

<table>
<thead>
<tr>
<th>Spec</th>
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<tbody>
<tr>
<td>600.perlbench_s</td>
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</tr>
<tr>
<td>602.gcc_s</td>
<td>48</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>48</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>48</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>48</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>48</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>48</td>
</tr>
<tr>
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</tr>
<tr>
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### SPECspeed®2017_int_base (9.59)

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ThinkSystem SR950  
(2.70 GHz, Intel Xeon Gold 6226)

---

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Aug-2019  
**Hardware Availability:** Jul-2019  
**Software Availability:** May-2019

### Results Table

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</tr>
</tbody>
</table>

**SPECspeed®2017_int_base =** 9.59  
**SPECspeed®2017_int_peak =** Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:

- KMP_AFFINITY = "granularity=fine,scatter"
- LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0u4/lib/intel64"
- LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017-1.0.5-ic19.0u4/je5.0.1-64"
- OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation:

- Filesystem page cache synced and cleared with: `sync; echo 3>/proc/sys/vm/drop_caches`
- NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
- Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
- Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
- Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
- Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

(Continued on next page)
**SPEC CPU®2017 Integer Speed Result**

Lenovo Global Technology  
ThinkSystem SR950  
(2.70 GHz, Intel Xeon Gold 6226)  

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

<table>
<thead>
<tr>
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</tr>
</tbody>
</table>

**Test Date:** Aug-2019  
**Hardware Availability:** Jul-2019  
**Software Availability:** May-2019

---

**General Notes (Continued)**


---

**Platform Notes**

BIOS configuration:
Choose Operating Mode set to Maximum Performance  
Choose Operating Mode set to Custom Mode  
CPU P-state Control set to Autonomous  
Hyper-Threading set to Disable  
Trusted Execution Technology set to Enable  
DCU Streamer Prefetcher set to Disable  
MONITOR/MWAIT set to Enable  
Sysinfo program /home/cpu2017-1.0.5-ic19.0u4/bin/sysinfo  
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9  
running on linux-u1b8 Tue Aug 13 10:51:40 2019

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo:

model name : Intel(R) Xeon(R) Gold 6226 CPU @ 2.70GHz  
4 "physical id"s (chips)  
48 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 12  
siblings : 12  
physical 0: cores 0 2 3 5 6 8 9 10 11 12 13 14  
physical 1: cores 0 2 3 4 5 8 9 10 11 12 13 14  
physical 2: cores 0 2 3 4 5 8 9 10 11 12 13 14  
physical 3: cores 0 1 2 3 4 5 6 8 9 11 12 13

From lscpu:

Architecture:       x86_64  
CPU op-mode(s):     32-bit, 64-bit  
Byte Order:         Little Endian  
CPU(s):             48  
On-line CPU(s) list: 0-47  
Thread(s) per core: 1  
Core(s) per socket: 12  
Socket(s):          4  
NUMA node(s):       4  
Vendor ID:          GenuineIntel  
CPU family:         6  
Model:              85

(Continued on next page)
Lenovo Global Technology
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SPECspeed®2017_int_base = 9.59
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Test Date: Aug-2019
Hardware Availability: Jul-2019
Software Availability: May-2019

Platform Notes (Continued)

Model name: Intel(R) Xeon(R) Gold 6226 CPU @ 2.70GHz
Stepping: 7
CPU MHz: 2700.000
BogoMIPS: 5400.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 19712K
NUMA node0 CPU(s): 0-11
NUMA node1 CPU(s): 12-23
NUMA node2 CPU(s): 24-35
NUMA node3 CPU(s): 36-47
Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abtm mcm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single ssbd mba ibrs ibpb tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rdt_a avx512if avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pin pts hwp_epp pku ospke avx512_vnni flush_l1d arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

Available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11
node 0 size: 193118 MB
node 0 free: 189781 MB
node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23
node 1 size: 193486 MB
node 1 free: 193217 MB
node 2 cpus: 24 25 26 27 28 29 30 31 32 33 34 35
node 2 size: 193515 MB
node 2 free: 193270 MB
node 3 cpus: 36 37 38 39 40 41 42 43 44 45 46 47
node 3 size: 193512 MB
node 3 free: 192927 MB
node distances:
node 0 1 2 3
0: 10 21 21 21
1: 21 10 21 21

(Continued on next page)
**Platform Notes (Continued)**

2: 21 21 10 21  
3: 21 21 21 10

From /proc/meminfo

MemTotal: 792200076 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

os-release:
NAME="SLES"
VERSION="15"
VERSION_ID="15"
PRETTY_NAME="SUSE Linux Enterprise Server 15"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Not affected  
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization  
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_PW

run-level 3 Aug 13 10:50

SPEC is set to: /home/cpu2017-1.0.5-ic19.0u4

    Filesystem  Type   Size  Used Avail Use% Mounted on
/dev/sda2    xfs   744G  45G  700G   6%  /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[PSE121R-1.53]- 07/03/2019  
Memory:
48x NO DIMM NO DIMM  
48x Samsung M393A2K43CB2-CVF 16 GB 2 rank 2933

(End of data from sysinfo program)
Lenovo Global Technology
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Compiler Version Notes

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| C      | 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 
|        | 625.x264_s(base) 657.xz_s(base) 
==============================================================================

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, 
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
| C++     | 620.omnetpp_s(base) 623.xalanchbmk_s(base) 631.deepsjeng_s(base) 
|         | 641.leela_s(base) 
==============================================================================

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, 
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
| Fortran | 648.exchange2_s(base) 
==============================================================================

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64

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**Base Portability Flags (Continued)**

- 623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
- 625.x264_s: -DSPEC_LP64
- 631.deepsjeng_s: -DSPEC_LP64
- 641.leela_s: -DSPEC_LP64
- 648.exchange2_s: -DSPEC_LP64
- 657.xz_s: -DSPEC_LP64

**Base Optimization Flags**

**C benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
- -L/usr/local/je5.0.1-64/lib -ljemalloc

**C++ benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4
- -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64 -lqkmalloc

**Fortran benchmarks:**
- -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
- -nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-D.html](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-D.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-D.xml](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-D.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.0.5 on 2019-08-12 22:51:39-0400.
Originally published on 2019-09-17.