## SPEC CPU®2017 Floating Point Rate Result

**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)  
ProLiant DL360 Gen10  
(2.40 GHz, Intel Xeon Platinum 8260)  

---

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base</th>
<th>248</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_fp_peak</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

---

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate®2017_fp_base</th>
<th>248</th>
</tr>
</thead>
</table>

### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon Platinum 8260</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz:</td>
<td>3900</td>
</tr>
<tr>
<td>Nominal:</td>
<td>2400</td>
</tr>
<tr>
<td>Enabled:</td>
<td>48 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1, 2 chip(s)</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3:</td>
<td>35.75 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>384 GB (24 x 16 GB 2Rx8 PC4-2933Y-R)</td>
</tr>
<tr>
<td>Storage:</td>
<td>1 x 400 GB SAS SSD, RAID 0</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>OS:</th>
<th>SUSE Linux Enterprise Server 15 (x86_64)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 19.0.2.187 of Intel C/C++</td>
</tr>
<tr>
<td>Parallel:</td>
<td>No</td>
</tr>
<tr>
<td>Firmware:</td>
<td>HPE BIOS Version U32 05/21/2019 released Jun-2019</td>
</tr>
<tr>
<td>File System:</td>
<td>xfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>Not Applicable</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>Power Management:</td>
<td>--</td>
</tr>
</tbody>
</table>

---

**Test Sponsor:** HPE  
**CPU2017 License:** 3  
**Test Date:** Jul-2019  
**Hardware Availability:** Jun-2019  
**Tested by:** HPE  
**Software Availability:** Feb-2019  
**Tested by:** HPE  
**Software:** SUSE Linux Enterprise Server 15 (x86_64)  
**Kernel:** 4.12.14-23-default  
**Compiler:** C/C++: Version 19.0.2.187 of Intel C/C++  
**Compiler Build:** 20190117 for Linux; Fortran: Version 19.0.2.187 of Intel Fortran  
**Compiler Build:** 20190117 for Linux  
**Firmware:** HPE BIOS Version U32 05/21/2019 released Jun-2019  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 64-bit  
**Peak Pointers:** Not Applicable  
**Other:** None  
**Power Management:** --
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**CPU2017 License:** 3  
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**Tested by:** HPE  
**Test Date:** Jul-2019  
**Hardware Availability:** Jun-2019  
**Software Availability:** Feb-2019

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>96</td>
<td>1825</td>
<td>528</td>
<td>1824</td>
<td>528</td>
<td>1828</td>
<td>527</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>96</td>
<td>549</td>
<td>221</td>
<td>549</td>
<td>221</td>
<td>548</td>
<td>222</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>96</td>
<td>454</td>
<td>201</td>
<td>453</td>
<td>201</td>
<td>452</td>
<td>202</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>96</td>
<td>1899</td>
<td>132</td>
<td>1913</td>
<td>131</td>
<td><strong>1901</strong></td>
<td><strong>132</strong></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>96</td>
<td>723</td>
<td>310</td>
<td>731</td>
<td>307</td>
<td>732</td>
<td>306</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>96</td>
<td>800</td>
<td>126</td>
<td>800</td>
<td>126</td>
<td>800</td>
<td>126</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>96</td>
<td>917</td>
<td>234</td>
<td><strong>920</strong></td>
<td><strong>234</strong></td>
<td>926</td>
<td>232</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>96</td>
<td>480</td>
<td>304</td>
<td>480</td>
<td>305</td>
<td><strong>480</strong></td>
<td><strong>304</strong></td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>96</td>
<td>553</td>
<td>304</td>
<td>555</td>
<td>303</td>
<td>552</td>
<td>304</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>96</td>
<td>370</td>
<td>646</td>
<td>360</td>
<td>663</td>
<td><strong>370</strong></td>
<td><strong>646</strong></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>96</td>
<td>342</td>
<td>472</td>
<td>340</td>
<td>475</td>
<td><strong>342</strong></td>
<td><strong>473</strong></td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>96</td>
<td>2164</td>
<td>173</td>
<td><strong>2162</strong></td>
<td><strong>173</strong></td>
<td>2160</td>
<td>173</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>96</td>
<td>1537</td>
<td>99.2</td>
<td><strong>1535</strong></td>
<td><strong>99.4</strong></td>
<td>1528</td>
<td>99.8</td>
</tr>
</tbody>
</table>

**SPECrate®2017_fp_base = 248**  
**SPECrate®2017_fp_peak = Not Run**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
```
sync; echo 3 > /proc/sys/vm/drop_caches
```
  
runcpu command invoked through numactl i.e.:  
```
numactl --interleave=all runcpu <etc>
```

### General Notes

Environment variables set by runcpu before the start of the run:  
```
LD_LIBRARY_PATH = "/home/cpu2017_u2/lib/ia32/:/home/cpu2017_u2/lib/intel64"
```

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.5  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
SPEC CPU®2017 Floating Point Rate Result
Copyright 2017-2019 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL360 Gen10
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Test Date: Jul-2019
Hardware Availability: Jun-2019
Software Availability: Feb-2019

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configuration:
- Thermal Configuration set to Maximum Cooling
- Memory Patrol Scrubbing set to Disabled
- LLC Prefetch set to Enabled
- LLC Dead Line Allocation set to Disabled
- Enhanced Processor Performance set to Enabled
- Workload Profile set to General Throughput Compute
- Workload Profile set to Custom
- Energy/Performance Bias set to Balanced Performance

Sysinfo program /home/cpu2017_u2/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-nub3 Thu Jul 4 12:17:39 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8260 CPU @ 2.40GHz
  2 "physical id"s (chips)
  96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 48
physical 0: cores 0 1 2 3 4 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
physical 1: cores 0 1 2 3 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95
Thread(s) per core: 2
Core(s) per socket: 24
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel

(Continued on next page)
### Platform Notes (Continued)

- **CPU family:** 6
- **Model:** 85
- **Model name:** Intel(R) Xeon(R) Platinum 8260 CPU @ 2.40GHz
- **Stepping:** 6
- **CPU MHz:** 2400.000
- **BogoMIPS:** 4800.00
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 1024K
- **L3 cache:** 36608K
- **NUMA node0 CPU(s):** 0-11,48-59
- **NUMA node1 CPU(s):** 12-23,60-71
- **NUMA node2 CPU(s):** 24-35,72-83
- **NUMA node3 CPU(s):** 36-47,84-95

**Flags:**
- fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperf perf_event idx pti msr cwd rtm pdm prf return mcmi cmovpat cmovapic movbe popcnt
- tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_ppi mba tpr_shadow vmx vmm est tm2 ssse3 sdbg fma cx16 xtrr pdcm pctl dca sse4_1 sse4_2 x2apic movbe popcnt
- tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_ppi mba tpr_shadow vmx vmm est tm2 ssse3 sdbg fma cx16 xtrr pdcm pctl dca sse4_1 sse4_2 x2apic movbe popcnt
- tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_ppi mba tpr_shadow vmx vmm est tm2 ssse3 sdbg fma cx16 xtrr pdcm pctl dca sse4_1 sse4_2 x2apic movbe popcnt

/proc/cpuinfo cache data
- cache size : 36608 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
- available: 4 nodes (0-3)
- node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 48 49 50 51 52 53 54 55 56 57 58 59
- node 0 size: 96349 MB
- node 0 free: 96046 MB
- node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23 60 61 62 63 64 65 66 67 68 69 70 71
- node 1 size: 96734 MB
- node 1 free: 96498 MB
- node 2 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 72 73 74 75 76 77 78 79 80 81 82 83
- node 2 size: 96763 MB
- node 2 free: 96553 MB
- node 3 cpus: 36 37 38 39 40 41 42 43 44 45 46 47 84 85 86 87 88 89 90 91 92 93 94 95
- node 3 size: 96761 MB
- node 3 free: 96613 MB
- node distances:
  - node 0 1 2 3

(Continued on next page)
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SPECrate®2017_fp_base = 248
SPECrate®2017_fp_peak = Not Run

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Jul-2019
Hardware Availability: Jun-2019
Software Availability: Feb-2019

Platform Notes (Continued)

0:  10  21  31  31
1:  21  10  31  31
2:  31  31  10  21
3:  31  31  21  10

From /proc/meminfo
MemTotal:       395888132 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="SLES"
VERSION="15"
VERSION_ID="15"
PRETTY_NAME="SUSE Linux Enterprise Server 15"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
Linux linux-nub3 4.12.14-23-default #1 SMP Tue May 29 21:04:44 UTC 2018 (cd0437b)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown):          Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 Jul 4 12:15

SPEC is set to: /home/cpu2017_u2
Filesysterm Type Size Used Avail Use% Mounted on
/dev/sda1     xfs  373G  134G  239G 36% /home

Additional information from dmidecode follows.  WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U32 05/21/2019
Memory:
24x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2933

(End of data from sysinfo program)
Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL360 Gen10
(2.40 GHz, Intel Xeon Platinum 8260)

SPECrate®2017_fp_base = 248
SPECrate®2017_fp_peak = Not Run

Compiler Version Notes

==============================================================================
C     | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C++   | 508.namd_r(base) 510.parest_r(base)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C++, C | 511.povray_r(base) 526.blender_r(base)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C++, C, Fortran | 507.cactuBSSN_r(base)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
Fortran | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
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Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
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(Continued on next page)
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CPU2017 License: 3
Test Sponsor: HPE
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Test Date: Jul-2019
Hardware Availability: Jun-2019
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Compiler Version Notes (Continued)

==============================================================================
Fortran, C      | 521.wrf_r(base) 527.cam4_r(base)
==============================================================================

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsIGNED-char

(Continued on next page)
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Base Portability Flags (Continued)

527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.xml
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2019-04-03.xml
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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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