## SPEC CPU®2017 Integer Speed Result

### Specification

Hewlett Packard Enterprise  
(Test Sponsor: HPE)  
ProLiant DL380 Gen10  
(2.30 GHz, Intel Xeon Gold 6230N)  

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>3</th>
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</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>HPE</td>
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<tr>
<td>Tested by:</td>
<td>HPE</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Jun-2019</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Apr-2019</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Nov-2018</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 6230N  
  - Max MHz: 3900  
  - Nominal: 2300  
  - Enabled: 40 cores, 2 chips  
  - Orderable: 1, 2 chip(s)  
  - Cache L1: 32 KB I + 32 KB D on chip per core  
  - L2: 1 MB I+D on chip per core  
  - L3: 27.5 MB I+D on chip per chip  
  - Other: None  
- **Memory:** 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R)  
- **Storage:** 1 x 960 GB SATA SSD, RAID 0  
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 15 (x86_64)  
  - Kernel 4.12.14-23-default  
  - Compiler: C/C++: Version 19.0.1.144 of Intel C/C++ Compiler Build 20181018 for Linux; Fortran: Version 19.0.1.144 of Intel Fortran Compiler Build 20181018 for Linux;  
  - Parallel: Yes  
  - Firmware: HPE BIOS Version U30 04/18/2019 released Apr-2019  
  - System State: Run level 3 (multi-user)  
  - Base Pointers: 64-bit  
  - Other: jemalloc memory allocator V5.0.1  
  - Power Management: --  

### Benchmark Results

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeak®2017_int_peak</th>
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</thead>
<tbody>
<tr>
<td>perlbench_s</td>
<td>40</td>
<td>6.12</td>
<td>Not Run</td>
</tr>
<tr>
<td>gcc_s</td>
<td>40</td>
<td>8.11</td>
<td>Not Run</td>
</tr>
<tr>
<td>mcf_s</td>
<td>40</td>
<td>10.9</td>
<td>Not Run</td>
</tr>
<tr>
<td>omnetpp_s</td>
<td>40</td>
<td>7.72</td>
<td>Not Run</td>
</tr>
<tr>
<td>xalancbmk_s</td>
<td>40</td>
<td>11.2</td>
<td>Not Run</td>
</tr>
<tr>
<td>x264_s</td>
<td>40</td>
<td>13.0</td>
<td>Not Run</td>
</tr>
<tr>
<td>deepsjeng_s</td>
<td>40</td>
<td>4.94</td>
<td>Not Run</td>
</tr>
<tr>
<td>leela_s</td>
<td>40</td>
<td>4.28</td>
<td>Not Run</td>
</tr>
<tr>
<td>exchange2_s</td>
<td>40</td>
<td>12.6</td>
<td>Not Run</td>
</tr>
<tr>
<td>xz_s</td>
<td>40</td>
<td>20.9</td>
<td>Not Run</td>
</tr>
</tbody>
</table>
### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches

### General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:
/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Platform Notes

BIOS Configuration:
Hyper-Threading set to Disabled
Thermal Configuration set to Maximum Cooling
Memory Patrol Scrubbing set to Disabled
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
Enhanced Processor Performance set to Enabled
Workload Profile set to General Peak Frequency Compute
Minimum Processor Idle Power Core C-State set to C1E State
Energy/Performance Bias set to Balanced Power
Workload Profile set to Custom
Numa Group Size Optimization set to Flat
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on dl380-clx-sles15 Thu Jun 20 13:20:43 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6230N CPU @ 2.30GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 20
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 40
On-line CPU(s) list: 0-39
Thread(s) per core: 1
Core(s) per socket: 20
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6230N CPU @ 2.30GHz
Stepping: 7
CPU MHz: 2300.000

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SPECspeed®2017_int_base = 8.98
SPECspeed®2017_int_peak = Not Run

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Platform Notes (Continued)

BogoMIPS: 4600.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 28160K
NUMA node0 CPU(s): 0-19
NUMA node1 CPU(s): 20-39
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3
sdmb fma cx16 xtpmr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault
epb cat_l3 cdp_l3 invpcid_single intel_pni mba tpr_shadow vnmi flexpriority ept
vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 ermv invpcid rtm cqm mpx rdt_a
avx512f avx512dq rdseed adx clflushopt clwb intel_pt avx512cd avx512bw avx512vl
xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
ibpb ibrs stibp dtherm ida arat pin pts pku ospke avx512_vnni arch_capabilities ssbd

/proc/cpuinfo cache data
  cache size : 28160 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19
  node 0 size: 386581 MB
  node 0 free: 385959 MB
  node 1 cpus: 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39
  node 1 size: 386842 MB
  node 1 free: 386555 MB
  node distances:
    node 0 1
      0: 10 21
      1: 21 10

From /proc/meminfo
  MemTotal: 791985760 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="SLES"
    VERSION="15"
    VERSION_ID="15"

(Continued on next page)
### Platform Notes (Continued)

```plaintext
PRETTY_NAME="SUSE Linux Enterprise Server 15"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15"
```

```
uname -a:
    Linux dl380-clx-sles15 4.12.14-23-default #1 SMP Tue May 29 21:04:44 UTC 2018
    (cd0437b) x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

- **CVE-2017-5754 (Meltdown):** Not affected
- **CVE-2017-5753 (Spectre variant 1):** Mitigation: __user pointer sanitization
- **CVE-2017-5715 (Spectre variant 2):** Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

```
run-level 3 Jun 20 13:19
```

```
SPEC is set to: /home/cpu2017
    Filesystem     Type      Size  Used  Avail  Use% Mounted on
    /dev/sda3      btrfs     407G  107G  300G  27%  /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

- BIOS: HPE U30 04/18/2019
- Memory: 24x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2933

(End of data from sysinfo program)

### Compiler Version Notes

```
==============================================================================
C       | 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base)
        | 625.x264_s(base) 657.xz_s(base)
==============================================================================
```

```
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
   Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
==============================================================================
C++     | 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
```

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## Compiler Version Notes (Continued)

<table>
<thead>
<tr>
<th>641.leela_s(base)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018</td>
</tr>
<tr>
<td>Copyright (C) 1985-2018 Intel Corporation. All rights reserved.</td>
</tr>
</tbody>
</table>

==============================================================================
Fortran | 648.exchange2_s(base)
==============================================================================
| Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018 |
| Copyright (C) 1985-2018 Intel Corporation. All rights reserved. |

## Base Compiler Invocation

C benchmarks:
```
icc -m64 -std=c11
```

C++ benchmarks:
```
icpc -m64
```

Fortran benchmarks:
```
ifort -m64
```

## Base Portability Flags

- 600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
- 602.gcc_s: -DSPEC_LP64
- 605.mcf_s: -DSPEC_LP64
- 620.omnetpp_s: -DSPEC_LP64
- 623.xalanchmk_s: -DSPEC_LP64 -DSPEC_LINUX
- 625.x264_s: -DSPEC_LP64
- 631.deepsjeng_s: -DSPEC_LP64
- 641.leela_s: -DSPEC_LP64
- 648.exchange2_s: -DSPEC_LP64
- 657.xz_s: -DSPEC_LP64
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CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -gopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.xml

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