Dell Inc.  

PowerEdge R940 (Intel Xeon Platinum 8260, 2.40GHz)  

**SPECspeed®2017_fp_base = 197**  
**SPECspeed®2017_fp_peak = 198**

<table>
<thead>
<tr>
<th>Threads</th>
<th>Caveats</th>
<th>SPECspeed®2017_fp_base (197)</th>
<th>SPECspeed®2017_fp_peak (198)</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>96</td>
<td>215</td>
<td></td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>96</td>
<td>168</td>
<td></td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>96</td>
<td>140</td>
<td></td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>96</td>
<td>160</td>
<td></td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>96</td>
<td>52.6</td>
<td></td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>96</td>
<td>76.9</td>
<td></td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>96</td>
<td>248</td>
<td></td>
</tr>
<tr>
<td>644.nab_s</td>
<td>96</td>
<td>428</td>
<td></td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>96</td>
<td>281</td>
<td></td>
</tr>
<tr>
<td>654.roms_s</td>
<td>96</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon Platinum 8260  
- **Max MHz:** 3900  
- **Nominal:** 2400  
- **Enabled:** 96 cores, 4 chips  
- **Orderable:** 2.4 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 35.75 MB I+D on chip per chip  
- **Memory:** 768 GB (48 x 16 GB 2Rx8 PC4-2933Y-R)  
- **Storage:** 1 x 240 GB SATA M.2 SSD  
- **Other:** None

**Software**

- **OS:** SUSE Linux Enterprise Server 12 SP4  
  - kernel 4.12.14-94.41-default  
- **Compiler:** C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux;  
  - Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux  
- **Parallel:** Yes  
- **Firmware:** Version 2.4.3 released Aug-2019  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** None  
- **Power Management:** --
Dell Inc.

PowerEdge R940 (Intel Xeon Platinum 8260, 2.40GHz)

SPECspeed®2017_fp_base = 197
SPECspeed®2017_fp_peak = 198

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>96</td>
<td>64.0</td>
<td>922</td>
<td>64.3</td>
<td>917</td>
<td>64.5</td>
<td>915</td>
<td>96</td>
<td>64.0</td>
<td>922</td>
<td>64.3</td>
<td>917</td>
<td>64.5</td>
<td>915</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>96</td>
<td>77.3</td>
<td>216</td>
<td>77.6</td>
<td>215</td>
<td>77.6</td>
<td>215</td>
<td>96</td>
<td>77.3</td>
<td>216</td>
<td>77.6</td>
<td>215</td>
<td>77.6</td>
<td>215</td>
</tr>
<tr>
<td>619.libm_s</td>
<td>96</td>
<td>29.7</td>
<td>176</td>
<td>32.9</td>
<td>159</td>
<td>31.2</td>
<td>168</td>
<td>96</td>
<td>29.7</td>
<td>176</td>
<td>32.9</td>
<td>159</td>
<td>31.2</td>
<td>168</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>96</td>
<td>94.8</td>
<td>140</td>
<td>94.8</td>
<td>140</td>
<td>94.8</td>
<td>140</td>
<td>96</td>
<td>94.8</td>
<td>140</td>
<td>94.8</td>
<td>140</td>
<td>94.8</td>
<td>140</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>96</td>
<td>55.4</td>
<td>160</td>
<td>55.5</td>
<td>160</td>
<td>55.2</td>
<td>161</td>
<td>96</td>
<td>55.7</td>
<td>159</td>
<td>55.3</td>
<td>160</td>
<td>55.3</td>
<td>160</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>96</td>
<td>222</td>
<td>53.5</td>
<td>228</td>
<td>52.0</td>
<td>226</td>
<td>52.6</td>
<td>96</td>
<td>216</td>
<td>54.9</td>
<td>238</td>
<td>49.9</td>
<td>223</td>
<td>53.3</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>96</td>
<td>59.6</td>
<td>242</td>
<td>58.3</td>
<td>248</td>
<td>58.1</td>
<td>248</td>
<td>96</td>
<td>58.2</td>
<td>248</td>
<td>62.3</td>
<td>232</td>
<td>58.2</td>
<td>248</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>96</td>
<td>40.8</td>
<td>428</td>
<td>40.8</td>
<td>428</td>
<td>40.8</td>
<td>428</td>
<td>96</td>
<td>40.8</td>
<td>428</td>
<td>40.9</td>
<td>428</td>
<td>40.8</td>
<td>428</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>96</td>
<td>119</td>
<td>76.9</td>
<td>120</td>
<td>75.9</td>
<td>111</td>
<td>81.8</td>
<td>96</td>
<td>122</td>
<td>74.6</td>
<td>118</td>
<td>77.4</td>
<td>118</td>
<td>77.2</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>96</td>
<td>55.1</td>
<td>286</td>
<td>56.1</td>
<td>281</td>
<td>58.8</td>
<td>268</td>
<td>96</td>
<td>55.1</td>
<td>286</td>
<td>56.1</td>
<td>281</td>
<td>58.8</td>
<td>268</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

Platform Notes

BIOS settings:
ADDDC setting disabled
Virtualization Technology disabled

(Continued on next page)
<table>
<thead>
<tr>
<th>Dell Inc.</th>
<th>SPEC CPU®2017 Floating Point Speed Result</th>
</tr>
</thead>
<tbody>
<tr>
<td>PowerEdge R940 (Intel Xeon Platinum 8260, 2.40GHz)</td>
<td>SPECspeed®2017_fp_base = 197</td>
</tr>
<tr>
<td>SPECspeed®2017_fp_peak = 198</td>
<td></td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Sep-2019  
**Hardware Availability:** Sep-2019  
**Software Availability:** May-2019

---

### Platform Notes (Continued)

- System Profile set to Custom
- CPU Performance set to Maximum Performance
- C States set to Autonomous
- C1E disabled
- Uncore Frequency set to Dynamic
- Energy Efficiency Policy set to Performance
- Memory Patrol Scrub disabled
- Logical Processor disabled
- CPU Interconnect Bus Link Power Management disabled
- PCI ASPM L1 Link Power Management disabled
- Sysinfo program /home/cpu2017/bin/sysinfo
- Rev: r5974 of 2018-05-19 9bcede8f2999c33d61f64985e45859ea9
- running on linux-ojzl Mon Sep 9 13:55:15 2019

**SUT (System Under Test) info as seen by some common utilities.**

For more information on this section, see [https://www.spec.org/cpu2017/Docs/config.html#sysinfo](https://www.spec.org/cpu2017/Docs/config.html#sysinfo)

**From /proc/cpuinfo**

- model name : Intel(R) Xeon(R) Platinum 8260 CPU @ 2.40GHz
- 4 "physical id"s (chips)
- 96 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
- cpu cores : 24
- siblings : 24
- physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
- physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
- physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
- physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29

**From lscpu:**

- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 96
- On–line CPU(s) list: 0–95
- Thread(s) per core: 1
- Core(s) per socket: 24
- Socket(s): 4
- NUMA node(s): 4
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Platinum 8260 CPU @ 2.40GHz
- Stepping: 7
- CPU MHz: 2400.000

(Continued on next page)
SPEC CPU®2017 Floating Point Speed Result

Dell Inc.

PowerEdge R940 (Intel Xeon Platinum 8260, 2.40GHz)  

Dell Inc.

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

SPECspeed®2017_fp_base = 197  
SPECspeed®2017_fp_peak = 198

**Platform Notes (Continued)**

```plaintext
CPU max MHz: 3900.0000  
CPU min MHz: 1000.0000  
BogoMIPS: 4800.00  
Virtualization: VT-x  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 1024K  
L3 cache: 36608K  

NUMA node0 CPU(s):  
0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68,72,76,80,84,88,92  
NUMA node1 CPU(s):  
1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69,73,77,81,85,89,93  
NUMA node2 CPU(s):  
2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70,74,78,82,86,90,94  
NUMA node3 CPU(s):  

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov 
apat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdemgb rdtscp 
li constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid 
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave 
avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 cdp_l3 
invpclid single intel_p6pm ssbd mba ibrs ibpb tpr_shadow vnmi flexpriority ept 
vpid fsgsbase tsc_adjust bmon hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a 
avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl 
xsavesopt xsaveopt xsave xsavec cqmm Mull cqmm_occupp lloc cqmm_mbb_total cqmm_mbb_local 
dtherm ida arat pln pts pku ospke avx512_vnni flush_l1d arch_capabilities 

/proc/cpuinfo cache data  
  cache size : 36608 KB
```

From numactl --hardware  
WARNING: a numactl 'node' might or might not correspond to a 
physical chip.  
available: 4 nodes (0-3)  
node 0 cpus: 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60 64 68 72 76 80 84 88 92  
node 0 size: 191873 MB  
node 0 free: 191625 MB  
node 1 cpus: 1 5 9 13 17 21 25 29 33 37 41 45 49 53 57 61 65 69 73 77 81 85 89 93  
node 1 size: 193521 MB  
node 1 free: 193330 MB  
node 2 cpus: 2 6 10 14 18 22 26 30 34 38 42 46 50 54 58 62 66 70 74 78 82 86 90 94  
node 2 size: 193521 MB  
node 2 free: 193319 MB  
node 3 cpus: 3 7 11 15 19 23 27 31 35 39 43 47 51 55 59 63 67 71 75 79 83 87 91 95  
node 3 size: 193518 MB  
node 3 free: 185782 MB  
node distances:

(Continued on next page)
Platform Notes (Continued)

node 0 1 2 3
0: 10 21 21 21
1: 21 10 21 21
2: 21 21 10 21
3: 21 21 21 10

From /proc/meminfo
MemTotal: 790972924 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP4

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 4
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP4"
  VERSION_ID="12.4"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP4"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp4"

uname -a:
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 Sep 9 09:24 last=5

SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 182G 17G 165G 9% /home

(Continued on next page)
Dell Inc.
PowerEdge R940 (Intel Xeon Platinum 8260, 2.40GHz)

SPECspeed®2017_fp_base = 197
SPECspeed®2017_fp_peak = 198

CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: Sep-2019
Hardware Availability: Sep-2019
Tested by: Dell Inc.
Software Availability: May-2019

Platform Notes (Continued)

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 2.4.3 08/28/2019
Memory:
4x 002C069D002C 18ASF2G72PDZ-2G9E1 16 GB 2 rank 2933
39x 00AD00B300AD HMA82GR7CJR8N-WM 16 GB 2 rank 2933
5x 00AD063200AD HMA82GR7CJR8N-WM 16 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

C 619.lbm_s(base, peak) 638.imagick_s(base, peak)
   | 644.nab_s(base, peak)
________________________________________________________________________
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
________________________________________________________________________
C++, C, Fortran 607.cactuBSSN_s(base, peak)
________________________________________________________________________
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
________________________________________________________________________
Fortran 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)
   | 654.roms_s(base, peak)
________________________________________________________________________
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

(Continued on next page)
## Compiler Version Notes (Continued)

<table>
<thead>
<tr>
<th>Fortran, C</th>
<th>621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)</th>
</tr>
</thead>
</table>

---

### Base Compiler Invocation

C benchmarks:

```
icc -m64 -std=c11
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

### Base Portability Flags

- 603.bwaves_s: -DSPEC_LP64
- 607.cactuBSSN_s: -DSPEC_LP64
- 619.lbm_s: -DSPEC_LP64
- 621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl
- 627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 638.imagick_s: -DSPEC_LP64
- 644.nab_s: -DSPEC_LP64
- 649.fotonik3d_s: -DSPEC_LP64
- 654.roms_s: -DSPEC_LP64
Dell Inc.  
PowerEdge R940 (Intel Xeon Platinum 8260, 2.40GHz)  

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base = 197</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_fp_peak = 198</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  

---

### Base Optimization Flags

- **C benchmarks:**
  - `xCORE-AVX512`  
  - `-ipo -O3 -no-prec-div -qopt-prefetch`  
  - `-ffinite-math-only -qopt-mem-layout-trans=4`  
  - `-qopenmp -DSPEC_OPENMP`

- **Fortran benchmarks:**
  - `-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch`  
  - `-ffinite-math-only -qopt-mem-layout-trans=4`  
  - `-qopenmp`  
  - `-nostandard-realloc-lhs`

- **Benchmarks using both Fortran and C:**
  - `-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch`  
  - `-ffinite-math-only -qopt-mem-layout-trans=4`  
  - `-qopenmp -DSPEC_OPENMP`  
  - `-nostandard-realloc-lhs`

- **Benchmarks using Fortran, C, and C++:**
  - `-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch`  
  - `-ffinite-math-only -qopt-mem-layout-trans=4`  
  - `-qopenmp -DSPEC_OPENMP`  
  - `-nostandard-realloc-lhs`

---

### Peak Compiler Invocation

- **C benchmarks:**
  - `icc -m64 -std=c11`

- **Fortran benchmarks:**
  - `ifort -m64`

- **Benchmarks using both Fortran and C:**
  - `ifort -m64 icc -m64 -std=c11`

- **Benchmarks using Fortran, C, and C++:**
  - `icpc -m64 icc -m64 -std=c11 ifort -m64`

---

### Peak Portability Flags

Same as Base Portability Flags
Dell Inc.
PowerEdge R940 (Intel Xeon Platinum 8260, 2.40GHz)

SPECspeed®2017_fp_base = 197
SPECspeed®2017_fp_peak = 198

CPU2017 License: 55  Test Date: Sep-2019
Test Sponsor: Dell Inc.  Hardware Availability: Sep-2019
Tested by: Dell Inc.  Software Availability: May-2019

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s

Fortran benchmarks:

603.bwaves_s: basepeak = yes

649.fotonik3d_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

627.cam4_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs

628.pop2_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
| Dell Inc. | SPECspeed®2017_fp_base = 197 |
| PowerEdge R940 (Intel Xeon Platinum 8260, 2.40GHz) | SPECspeed®2017_fp_peak = 198 |

| CPU2017 License: | 55 |
| Test Sponsor: | Dell Inc. |
| Tested by: | Dell Inc. |
| Test Date: | Sep-2019 |
| Hardware Availability: | Sep-2019 |
| Software Availability: | May-2019 |

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.0.5 on 2019-09-09 14:55:14-0400.
Originally published on 2019-10-01.