## Dell Inc.

**PowerEdge MX740c (Intel Xeon Gold 6212U, 2.40GHz)**

<table>
<thead>
<tr>
<th>SPEC CPU®2017 Integer Speed Result</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dell Inc.</td>
</tr>
<tr>
<td>SPEC CPU®2017 Integer Speed Result</td>
</tr>
</tbody>
</table>

### CPU2017 License: 55

<table>
<thead>
<tr>
<th>Test Sponsor: Dell Inc.</th>
<th>Tested by: Dell Inc.</th>
<th>Test Date: Aug-2019</th>
<th>Hardware Availability: Apr-2019</th>
</tr>
</thead>
</table>

### Software

OS: Ubuntu 18.04.2 LTS

Compiler: C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux;

Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux

Parallel: Yes

Firmware: Version 2.2.7 released Apr-2019

File System: ext4

System State: Run level 3 (multi-user)

Base Pointers: 64-bit

Peak Pointers: 64-bit

Other: jemalloc memory allocator V5.0.1

Power Management: --

### Hardware

<table>
<thead>
<tr>
<th>CPU Name: Intel Xeon Gold 6212U</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz: 3900</td>
</tr>
<tr>
<td>Nominal: 2400</td>
</tr>
<tr>
<td>Enabled: 24 cores, 1 chip</td>
</tr>
<tr>
<td>Orderable: 1 chips</td>
</tr>
<tr>
<td>Cache L1: 32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2: 1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3: 35.75 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other: None</td>
</tr>
<tr>
<td>Memory: 192 GB (6 x 32 GB 2Rx4 PC4-2933Y-R)</td>
</tr>
<tr>
<td>Storage: 1 x 480 GB SATA SSD</td>
</tr>
<tr>
<td>Other: None</td>
</tr>
</tbody>
</table>

### Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_int_peak</th>
<th>SPECspeed®2017_int_base</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>24</td>
<td>7.79</td>
<td>6.74</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>24</td>
<td>9.80</td>
<td>9.77</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>24</td>
<td>9.00</td>
<td>9.59</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>24</td>
<td>12.5</td>
<td>12.5</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>24</td>
<td>12.4</td>
<td>12.5</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>24</td>
<td>14.3</td>
<td>14.3</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>24</td>
<td>5.68</td>
<td>5.68</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>24</td>
<td>4.77</td>
<td>4.76</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>24</td>
<td>16.7</td>
<td>16.7</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>24</td>
<td>20.4</td>
<td>20.6</td>
</tr>
</tbody>
</table>
Dell Inc.

PowerEdge MX740c (Intel Xeon Gold 6212U, 2.40GHz)

SPEC CPU®2017 Integer Speed Result

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Aug-2019
Hardware Availability: Apr-2019
Software Availability: Aug-2019

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>24</td>
<td>261</td>
<td>6.79</td>
<td>263</td>
<td>6.74</td>
<td>263</td>
<td>6.74</td>
<td>263</td>
<td>6.74</td>
<td>263</td>
<td>6.74</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>24</td>
<td>405</td>
<td>9.83</td>
<td>406</td>
<td>9.80</td>
<td>409</td>
<td>9.73</td>
<td>399</td>
<td>9.97</td>
<td>394</td>
<td>10.1</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>24</td>
<td>379</td>
<td>12.5</td>
<td>377</td>
<td>12.5</td>
<td>375</td>
<td>12.6</td>
<td>380</td>
<td>12.4</td>
<td>378</td>
<td>12.5</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>24</td>
<td>183</td>
<td>8.90</td>
<td>179</td>
<td>9.12</td>
<td>181</td>
<td>9.00</td>
<td>176</td>
<td>9.29</td>
<td>170</td>
<td>9.57</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>24</td>
<td>114</td>
<td>12.5</td>
<td>114</td>
<td>12.5</td>
<td>114</td>
<td>12.4</td>
<td>114</td>
<td>12.5</td>
<td>114</td>
<td>12.5</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>24</td>
<td>124</td>
<td>14.3</td>
<td>123</td>
<td>14.3</td>
<td>123</td>
<td>14.3</td>
<td>123</td>
<td>14.3</td>
<td>123</td>
<td>14.3</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>24</td>
<td>252</td>
<td>5.68</td>
<td>252</td>
<td>5.68</td>
<td>252</td>
<td>5.68</td>
<td>252</td>
<td>5.68</td>
<td>252</td>
<td>5.68</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>24</td>
<td>359</td>
<td>4.76</td>
<td>358</td>
<td>4.77</td>
<td>358</td>
<td>4.77</td>
<td>358</td>
<td>4.76</td>
<td>358</td>
<td>4.76</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>24</td>
<td>177</td>
<td>16.7</td>
<td>176</td>
<td>16.7</td>
<td>177</td>
<td>16.7</td>
<td>176</td>
<td>16.7</td>
<td>176</td>
<td>16.7</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>24</td>
<td>303</td>
<td>20.4</td>
<td>303</td>
<td>20.4</td>
<td>303</td>
<td>20.4</td>
<td>301</td>
<td>20.6</td>
<td>301</td>
<td>20.6</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
Dell Inc. 

PowerEdge MX740c (Intel Xeon Gold 6212U, 2.40GHz) 

CPU2017 License: 55 
Test Sponsor: Dell Inc. 
Tested by: Dell Inc. 

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>= 10.2</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_int_peak</td>
<td>= 10.4</td>
</tr>
</tbody>
</table>

Test Date: Aug-2019 
Hardware Availability: Apr-2019 
Software Availability: Aug-2019

Platform Notes

BIOS settings: 
ADDDC setting disabled 
Sub NUMA Cluster disabled 
Virtualization Technology disabled 
DCU Streamer Prefetcher disabled 
System Profile set to Custom 
CPU Performance set to Maximum Performance 
C States set to Autonomous 
C1E disabled 
Uncore Frequency set to Dynamic 
Energy Efficiency Policy set to Performance 
Memory Patrol Scrub disabled 
Logical Processor disabled 
CPU Interconnect Bus Link Power Management disabled 
PCI ASPM L1 Link Power Management disabled 
Sysinfo program /home/cpu2017/bin/sysinfo 
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9 
running on intel-sut Thu Aug 22 23:58:01 2019

SUT (System Under Test) info as seen by some common utilities. 
For more information on this section, see 
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo 
model name : Intel(R) Xeon(R) Gold 6212U CPU @ 2.40GHz 
1  "physical id"s (chips) 
24 "processors" 
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.) 
cpu cores : 24 
siblings : 24 
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29

From lscpu: 
Architecture: x86_64 
CPU op-mode(s): 32-bit, 64-bit 
Byte Order: Little Endian 
CPU(s): 24 
On-line CPU(s) list: 0-23 
Thread(s) per core: 1 
Core(s) per socket: 24 
Socket(s): 1 
NUMA node(s): 1 
Vendor ID: GenuineIntel 
CPU family: 6 
Model: 85 
Model name: Intel(R) Xeon(R) Gold 6212U CPU @ 2.40GHz

(Continued on next page)
Dell Inc. PowerEdge MX740c (Intel Xeon Gold 6212U, 2.40GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Dell Inc.
PowerEdge MX740c (Intel Xeon Gold 6212U, 2.40GHz)

SPECspeed®2017_int_base = 10.2
SPECspeed®2017_int_peak = 10.4

Platform Notes (Continued)

Stepping: 6
CPU MHz: 2510.224
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0-23
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpes7gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrd pdcn pcd dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx lf6c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed advx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsavec sgx cqm_llc cqm_occup_llc cqm_mbb_total
cqm_mbb_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_lld
arch_capabilities

/proc/cpuinfo cache data
  cache size : 36608 KB

From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
  node 0 size: 191892 MB
  node 0 free: 191099 MB
  node distances:
    node 0
      0: 10

From /proc/meminfo
  MemTotal: 196498044 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
  Ubuntu 18.04.2 LTS

From /etc/*release* /etc/*version*
  debian_version: buster/sid
  os-release:
    NAME="Ubuntu"

(Continued on next page)
**SPEC CPU®2017 Integer Speed Result**

**Dell Inc.**

PowerEdge MX740c (Intel Xeon Gold 6212U, 2.40GHz)  

**SPECspeed®2017_int_base = 10.2**  
**SPECspeed®2017_int_peak = 10.4**

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>55</th>
<th>Test Date:</th>
<th>Aug-2019</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Dell Inc.</td>
<td>Hardware Availability:</td>
<td>Apr-2019</td>
<td></td>
</tr>
<tr>
<td>Tested by:</td>
<td>Dell Inc.</td>
<td>Software Availability:</td>
<td>Aug-2019</td>
<td></td>
</tr>
</tbody>
</table>

---

### Platform Notes (Continued)

```plaintext
VERSION="18.04.2 LTS (Bionic Beaver)"
ID=ubuntu
ID_LIKE=debian
PRETTY_NAME="Ubuntu 18.04.2 LTS"
VERSION_ID="18.04"
HOME_URL="https://www.ubuntu.com/"
SUPPORT_URL="https://help.ubuntu.com/"
```

```plaintext
uname -a:
Linux intel-sut 4.15.0-58-generic #64-Ubuntu SMP Tue Aug 6 11:12:41 UTC 2019 x86_64
x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

- CVE-2017-5754 (Meltdown): Not affected
- CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
- CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

```plaintext
run-level 3 Aug 22 23:54
```

```plaintext
SPEC is set to: /home/cpu2017
```

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda2</td>
<td>ext4</td>
<td>439G</td>
<td>32G</td>
<td>385G</td>
<td>8%</td>
<td>/</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is intended to allow hardware to be accurately determined, but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```plaintext
BIOS Dell Inc. 2.2.7 04/23/2019
Memory:
  5x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
  1x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
  18x Not Specified Not Specified
```

(End of data from sysinfo program)

---

### Compiler Version Notes

```
==============================================================================
C   | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
==============================================================================
```

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416

(Continued on next page)
### Dell Inc.

**PowerEdge MX740c (Intel Xeon Gold 6212U, 2.40GHz)**

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>55</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Dell Inc.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base = 10.2</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_int_peak = 10.4</td>
</tr>
</tbody>
</table>

#### Compiler Version Notes (Continued)

Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

---

<table>
<thead>
<tr>
<th>C++</th>
<th>620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)</th>
</tr>
</thead>
</table>

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416

Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

---

<table>
<thead>
<tr>
<th>Fortran</th>
<th>648.exchange2_s(base, peak)</th>
</tr>
</thead>
</table>

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416

Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

---

#### Base Compiler Invocation

**C benchmarks:**

```bash
c -m64 -std=c11
```

**C++ benchmarks:**

```bash
c -m64
```

**Fortran benchmarks:**

```bash
t -m64
```

#### Base Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```
Dell Inc.
PowerEdge MX740c (Intel Xeon Gold 6212U, 2.40GHz)

SPECs\textsuperscript{\textregistered}2017\textunderscore int\textunderscore base = 10.2
SPECs\textsuperscript{\textregistered}2017\textunderscore int\textunderscore peak = 10.4

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

## Base Optimization Flags

**C benchmarks:**
-\texttt{-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div}
-\texttt{-qopt-mem-layout-trans=4 -qopenmp -DSPEC\_OPENMP}
-\texttt{-L/usr/local/je5.0.1-64/lib -ljemalloc}

**C++ benchmarks:**
-\texttt{-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div}
-\texttt{-qopt-mem-layout-trans=4}
-\texttt{-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64 -lqkmallo}

**Fortran benchmarks:**
-\texttt{-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4}
-\texttt{-nostandard-realloc-lhs}

## Peak Compiler Invocation

**C benchmarks:**
\texttt{icc -m64 -std=c11}

**C++ benchmarks:**
\texttt{icpc -m64}

**Fortran benchmarks:**
\texttt{ifort -m64}

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

**C benchmarks:**
\texttt{600.perlbench\_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2}
-\texttt{-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3}
-\texttt{-no-prec-div -DSPEC\_SUPPRESS\_OPENMP -qopenmp}
-\texttt{-DSPEC\_OPENMP -fno-strict-overflow}
-\texttt{-L/usr/local/je5.0.1-64/lib -ljemalloc}

(Continued on next page)
Dell Inc.

PowerEdge MX740c (Intel Xeon Gold 6212U, 2.40GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Aug-2019
Hardware Availability: Apr-2019
Software Availability: Aug-2019

**Peak Optimization Flags (Continued)**

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64 -lqkmalloc


631.deepsjeng_s: Same as 623.xalancbmk_s

641.leela_s: Same as 623.xalancbmk_s

Fortran benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4 -nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

### SPEC CPU®2017 Integer Speed Result

**Dell Inc.**

PowerEdge MX740c (Intel Xeon Gold 6212U, 2.40GHz)  

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_int_base</td>
<td>10.2</td>
</tr>
<tr>
<td>SPECspeed®2017_int_peak</td>
<td>10.4</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  

**Test Date:** Aug-2019  
**Hardware Availability:** Apr-2019  
**Software Availability:** Aug-2019

---

You can also download the XML flags sources by saving the following links:


---

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.0.5 on 2019-08-22 19:58:01-0400.  
Report generated on 2019-10-01 14:14:54 by CPU2017 PDF formatter v6255.  
Originally published on 2019-10-01.