Lenovo Global Technology
ThinkSystem SR655
3.20 GHz, AMD EPYC 7262

SPECspeed®2017_int_base = 8.52
SPECspeed®2017_int_peak = 8.78

Hardware

CPU Name: AMD EPYC 7262
Max MHz: 3400
Nominal: 3200
Enabled: 8 cores, 1 chip
Orderable: 1 chip
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 512 KB I+D on chip per core
L3: 128 MB I+D on chip per chip, 16 MB per core
Other: None
Memory: 256 GB (8 x 32 GB 2Rx4 PC4-3200AA-R)
Storage: 1 x 960 GB SATA SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP1 (x86_64)
Kernel 4.12.14-195-default
Compiler: C/C++/Fortran: Version 2.0.0 of AOCC
Parallel: Yes
Firmware: Lenovo BIOS Version CFE103G released Aug-2019
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc: jemalloc memory allocator library version 5.2.0
Power Management: --
## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base</th>
<th></th>
<th></th>
<th></th>
<th>Peak</th>
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<td>15.3</td>
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<tr>
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<td>4.94</td>
<td>291</td>
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<td>648.exchange2_s</td>
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<td>181</td>
<td>16.3</td>
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<td>657.xz_s</td>
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<td>14.2</td>
<td>434</td>
<td>14.2</td>
<td>434</td>
<td>14.2</td>
</tr>
</tbody>
</table>

### Compiler Notes


### Submit Notes

The config file option 'submit' was used. 'numactl' was used to bind copies to the cores. See the configuration file for details.

### Operating System Notes

'ulimit -s unlimited' was used to set environment stack size 'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.: numactl --interleave=all runcpu <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory sync then drop_caches=3 to reset caches before invoking runcpu

dirty_ratio, swappiness, zone_reclaim_mode and drop_caches were all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).
Lenovo Global Technology
ThinkSystem SR655
3.20 GHz, AMD EPYC 7262

**SPEC CPU®2017 Integer Speed Result**

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<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>9017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Lenovo Global Technology</td>
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</tbody>
</table>

**SPECspeed®2017_int_base = 8.52**

**SPECspeed®2017_int_peak = 8.78**

**Operating System Notes (Continued)**

Transparent huge pages set to 'always' for this run (OS default)

**General Notes**

Environment variables set by runcpu before the start of the run:

- `GOMP_CPU_AFFINITY = "0-7"`
- `LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-amd-rome-aocc200/amd_speed_aocc200_rome_A_lib/64"`
- `LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017-1.0.5-amd-rome-aocc200/amd_speed_aocc200_rome_A_lib/32"`
- `MALLOC_CONF = "retain:true"
- `OMP_DYNAMIC = "false"
- `OMP_SCHEDULE = "static"
- `OMP_STACKSIZE = "128M"
- `OMP_THREADLIMIT = "64"

Binaries were compiled on a system with 2p AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flto
jemalloc 5.2.0 is available here:
https://github.com/jemalloc/jemalloc/releases/download/5.2.0/jemalloc-5.2.0.tar.bz2

**Platform Notes**

BIOS settings:
- Operating Mode set to Maximum Performance
- SMT Mode set to Disabled
- EfficiencyModeEn set to Auto
- NUMA nodes per socket set to NPS2
- Sysinfo program /home/cpu2017-1.0.5-amd-rome-aocc200/bin/sysinfo
- Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
- running on linux-01om Mon Aug 26 07:01:25 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : AMD EPYC 7262 8-Core Processor

(Continued on next page)
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Test Date: Aug-2019
Hardware Availability: Aug-2019
Software Availability: Aug-2019

Platform Notes (Continued)

1  "physical id"s (chips)
8  "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 8
physical 0: cores 0 4 8 12 16 20 24 28
From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
Address sizes: 43 bits physical, 48 bits virtual
CPU(s): 8
On-line CPU(s) list: 0-7
Thread(s) per core: 1
Core(s) per socket: 8
Socket(s): 1
NUMA node(s): 2
Vendor ID: AuthenticAMD
CPU family: 23
Model: 49
Model name: AMD EPYC 7262 8-Core Processor
Stepping: 0
CPU MHz: 3200.000
CPU max MHz: 3200.0000
CPU min MHz: 1500.0000
BogoMIPS: 6387.44
Virtualization: AMD-V
L1d cache: 32K
L1i cache: 32K
L2 cache: 512K
L3 cache: 16384K
NUMA node0 CPU(s): 0-3
NUMA node1 CPU(s): 4-7
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl xtopology nonstop_tsc cpuid extd_apicid aperfasperf pni
pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx f16c
rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3nowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_l2 mwaitx cpb
cat_l3 cdp_l3 hw_pstate sme ssbd sev ibrs ibrp stibp vmmcall fsqsbse bni avx2 smep
bmi2 cmq rdr_a rdseed adx smap clflushopt clwb sha_ni xsavemap xsaves xsaveopt xsavec
xgetbv1 xsaves cmq_llc cmq_occup_llc cmq_mbm_total cmq_mbm_local clzero irperf xsaveerpr arat
npt lbv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
pfthreshold avic v_vmsave_vmload vgif umip rdpid overflow_recov succor smca

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Platform Notes (Continued)

/proc/cpuinfo cache data
  cache size : 512 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3
  node 0 size: 128775 MB
  node 0 free: 128411 MB
  node 1 cpus: 4 5 6 7
  node 1 size: 128984 MB
  node 1 free: 128613 MB
  node distances:
    node 0 1
    0: 10 12
    1: 12 10

From /proc/meminfo
  MemTotal: 263946228 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="SLES"
    VERSION="15-SP1"
    VERSION_ID="15.1"
    PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
    ID="sles"
    ID_LIKE="suse"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:15:sp1"

  uname -a:
    Linux linux-01om 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)
    x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full AMD retpoline, IBPB: conditional, IBRS_FW, STIBP: disabled, RSB filling

run-level 3 Aug 26 01:36

SPEC is set to: /home/cpu2017-1.0.5-amd-rome-aocc200

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Software Availability: Aug-2019

**Platform Notes (Continued)**

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda2</td>
<td>xfs</td>
<td>893G</td>
<td>32G</td>
<td>862G</td>
<td>4%</td>
<td>/</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is intended to allow hardware to be accurately determined, but the intent may not be met, as there are frequent changes to hardware, firmware, and the DMTF SMBIOS standard.

BIOS Lenovo CFE103G 08/01/2019  
Memory:  
8x Samsung M393A4K40DB2-CWE 32 kB 2 rank 3200  
8x Unknown Unknown

(End of data from sysinfo program)

**Compiler Version Notes**

```
C       | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
```

AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins  
AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)  
Target: x86_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin

```
C++     | 623.xalancbmk_s(peak)
```

AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins  
AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin

```
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
```

AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins  
AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)  
Target: x86_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin

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Software Availability: Aug-2019

Compiler Version Notes (Continued)

==============================================================================
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<tr>
<th>C++</th>
<th>623.xalancbmk_s (peak)</th>
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AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin
==============================================================================

<table>
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<tr>
<th>C++</th>
<th>620.omnetpp_s (base, peak) 623.xalancbmk_s(base)</th>
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AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin
==============================================================================

<table>
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<th>Fortran</th>
<th>648.exchange2_s (base, peak)</th>
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AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin
==============================================================================

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang
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Base Portability Flags

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
- flto -Wl,-mllvm -Wl,-function-specialize
- Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
- Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
- march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
- fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
- mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
- mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
- fly-function-specialization -z muldefs -DSPEC_OPENMP -fopenmp
- DUSE_OPENMP -fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm
- ljemalloc -lflang

C++ benchmarks:
- flto -Wl,-mllvm -Wl,-function-specialize
- Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
- Wl,-mllvm -Wl,-reduce-array-computations=3
- Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
- mllvm -loop-unswitch-threshold=200000 -mllvm -vector-library=LIBMVEC
- mllvm -unroll-threshold=100 -fly-function-specialization
- mllvm -enable-partial-unswitch -z muldefs -DSPEC_OPENMP -fopenmp
- DUSE_OPENMP -fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm
- ljemalloc -lflang

Fortran benchmarks:
- flto -Wl,-mllvm -Wl,-function-specialize
- Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
- Wl,-mllvm -Wl,-reduce-array-computations=3 -ffast-math
- Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,lsr-in-nested-loop
- Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver2 -funroll-loops
- mrecursive -mllvm -vector-library=LIBMVEC -z muldefs
- mllvm -disable-indvar-simplify -mllvm -unroll-aggressive

(Continued on next page)
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Base Optimization Flags (Continued)

Fortran benchmarks (continued):
-mlirm -unroll-threshold=150 -DSPEC_OPENMP -fopenmp -DUSE_OPENMP
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc
-lflang

Base Other Flags

C benchmarks:
-Wno-return-type

C++ benchmarks:
-Wno-return-type

Fortran benchmarks:
-Wno-return-type

Peak Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Peak Portability Flags

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LINUX -D_FILE_OFFSET_BITS=64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
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Peak Optimization Flags

C benchmarks:

600.perlbench_s: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-fprofile-instr-generate(pass 1)
-fprofile-instr-use(pass 2) -Ofast -march=znver2
-mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -DSPEC_OPENMP -fopenmp
-DUSE_OPENMP -lmvec -lamdlibm -fopenmp=libomp -lomp
-lpthread -ldl -ljemalloc -lflang

602.gcc_s: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -z muldefs -DSPEC_OPENMP
-fopenmp -DUSE_OPENMP -fgnu89-inline -fopenmp=libomp
-lomp -lpthread -ldl -ljemalloc

605.mcf_s: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -DSPEC_OPENMP -fopenmp

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<td><strong>Hardware Availability:</strong> Aug-2019</td>
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<td><strong>Tested by:</strong> Lenovo Global Technology</td>
<td><strong>Software Availability:</strong> Aug-2019</td>
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#### Peak Optimization Flags (Continued)

**605.mcf_s** (continued):
- DUSE_OPENMP  
  -lmvec  
  -lamdlibm  
  -fopenmp=libomp  
  -lomp  
  -lpthread  
  -ldl  
  -ljemalloc  
  -lflang

**625.x264_s**:
- Same as 600.perlbench_s

**657.xz_s**:
- -flto  
  -Wl,-mllvm -Wl,-region-vectorize  
  -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
  -Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
  -march=znver2 -mno-sse4a -fstruct-layout=5  
  -mllvm -vectorize-memory-aggressively  
  -mllvm -function-specialize -mllvm -enable-gvn-hoist  
  -mllvm -unroll-threshold=50 -fremap-arrays  
  -mllvm -vector-library=LIBMVEC  
  -mllvm -reduce-array-computations=3  
  -mllvm -global-vectorize-slp -mllvm -inline-threshold=1000  
  -flv-function-specialization -DSPEC_OPENMP -fopenmp  
  -DUSE_OPENMP -fopenmp=libomp -lomp -lpthread -ldl  
  -lmvec -lamdlibm -ljemalloc -lflang

**C++ benchmarks**:

**620.omnetpp_s**:
- basepeak = yes

**623.xalanbmks**:
- -m32 -flto  
  -Wl,-mllvm -Wl,-region-vectorize  
  -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
  -Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
  -march=znver2 -flv-function-specialization  
  -mllvm -unroll-threshold=100  
  -mllvm -enable-partial-unswitch  
  -mllvm -loop-unswitch-threshold=200000  
  -mllvm -vector-library=LIBMVEC  
  -mllvm -inline-threshold=1000 -DSPEC_OPENMP -fopenmp  
  -DUSE_OPENMP -fopenmp=libomp -lomp -lpthread -ldl  
  -ljemalloc

**631.deepsjeng_s**:
- -flto  
  -Wl,-mllvm -Wl,-region-vectorize  
  -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
  -Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
  -march=znver2 -flv-function-specialization  
  -mllvm -unroll-threshold=100  
  -mllvm -enable-partial-unswitch  
  -mllvm -loop-unswitch-threshold=200000

(Continued on next page)
SPEC CPU®2017 Integer Speed Result
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Lenovo Global Technology
ThinkSystem SR655
3.20 GHz, AMD EPYC 7262

SPECspeed®2017_int_base = 8.52
SPECspeed®2017_int_peak = 8.78

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology
Test Date: Aug-2019
Hardware Availability: Aug-2019
Software Availability: Aug-2019

Peak Optimization Flags (Continued)

631.deepsjeng_s (continued):
-mlvml -vector-library=LIBMVEC
-mlvml -inline-threshold=1000 -DSPEC_OPENMP -fopenmp
-DUSE_OPENMP -fopenmp=libomp -lomp -lpthread -ldl
-1mvec -lamdlibm -ljemalloc -lflang

641.leela_s: basepeak = yes

Fortran benchmarks:
-fflto -Wl,-mllvm -Wl,-function-specialize
-fflto -Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-fflto -Wl,-mllvm -Wl,-reduce-array-computations=3 -ffast-math
-fflto -Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsl-in-nested-loop
-fflto -Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver2 -funroll-loops
-Mrecursive -mllvm -vector-library=LIBMVEC
-mlvml -disable-indvar-simplify -mlvml -unroll-aggressive
-mlvml -unroll-threshold=150 -DSPEC_OPENMP -fopenmp -DUSE_OPENMP
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc
-lflang

Peak Other Flags

C benchmarks:
-Wno-return-type

C++ benchmarks (except as noted below):
-Wno-return-type

623.xalancbmk_s: -Wno-return-type
-L/sppo/dev/cpu2017/amd_speed_aocc200_rome/amd_speed_aocc200_rome_A_lib/32

Fortran benchmarks:
-Wno-return-type

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Rome-A.xml
# SPEC CPU®2017 Integer Speed Result

**Lenovo Global Technology**

ThinkSystem SR655  
3.20 GHz, AMD EPYC 7262

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Tested with SPEC CPU®2017 v1.0.5 on 2019-08-25 19:01:25-0400.  
Originally published on 2019-10-01.