## SPEC CPU®2017 Floating Point Rate Result

Supermicro
SuperServer 5019C-WR (X11SCW-F, Intel Core i3-8100T)

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base</th>
<th>24.1</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_fp_peak</td>
<td>24.6</td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>Spec</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Core i3-8100T</td>
</tr>
<tr>
<td>Max MHz</td>
<td>3100</td>
</tr>
<tr>
<td>Nominal</td>
<td>3100</td>
</tr>
<tr>
<td>Enabled</td>
<td>4 cores, 1 chip</td>
</tr>
<tr>
<td>Orderable</td>
<td>1 chip</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2</td>
<td>256 KB I+D on chip per core</td>
</tr>
<tr>
<td>L3</td>
<td>6 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>64 GB (4 x 16 GB 2Rx8 PC4-2666V-E, running at 2400)</td>
</tr>
<tr>
<td>Storage</td>
<td>1 x 200 GB SATA III SSD</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Spec</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS</td>
<td>SUSE Linux Enterprise Server 12 SP3 (x86_64) Kernel 4.4.114-94.11-default</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 19.0.1.144 of Intel C/C++ Compiler for Linux; Fortran: Version 19.0.1.144 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td>Parallel</td>
<td>No</td>
</tr>
<tr>
<td>Firmware</td>
<td>Version 1.0b released May-2019</td>
</tr>
<tr>
<td>File System</td>
<td>xfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
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<tr>
<td>Power Management</td>
<td>--</td>
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</table>

### Results

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>SPECrate®2017_fp_base</th>
<th>SPECrate®2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>19.9</td>
<td>66.6</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>16.3</td>
<td>66.6</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>16.4</td>
<td>66.6</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>15.0</td>
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</tr>
<tr>
<td>511.povray_r</td>
<td>25.2</td>
<td>66.6</td>
</tr>
<tr>
<td>519.ibm_r</td>
<td>16.3</td>
<td>66.6</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>21.6</td>
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</tr>
<tr>
<td>526.blender_r</td>
<td>55.1</td>
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</tr>
<tr>
<td>527.cam4_r</td>
<td>25.9</td>
<td>66.6</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>55.9</td>
<td>66.6</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>31.7</td>
<td>66.6</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>20.4</td>
<td>66.6</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>12.7</td>
<td>66.6</td>
</tr>
</tbody>
</table>

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Copyright 2017-2019 Standard Performance Evaluation Corporation
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
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<td>501</td>
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<td>500</td>
<td>12.7</td>
</tr>
</tbody>
</table>

**SPECrate®2017_fp_base = 24.1**  
**SPECrate®2017_fp_peak = 24.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:

`LD_LIBRARY_PATH = "~/home/cpu2017/lib/intel64"`

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
SPEC CPU®2017 Floating Point Rate Result

Supermicro
SuperServer 5019C-WR (X11SCW-F, Intel Core i3-8100T)

SPECrate®2017_fp_base = 24.1
SPECrate®2017_fp_peak = 24.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e4589ea9
running on linux-65nv Mon Sep 9 21:24:56 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Core(TM) i3-8100T CPU @ 3.10GHz
1 "physical id"s (chips)
4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 4
physical 0: cores 0 1 2 3

From lscpu:

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 4
On-line CPU(s) list: 0-3
Thread(s) per core: 1
Core(s) per socket: 4
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 158
Model name: Intel(R) Core(TM) i3-8100T CPU @ 3.10GHz
Stepping: 11
CPU MHz: 3100.002
CPU max MHz: 3100.0000
CPU min MHz: 800.0000
BogoMIPS: 6191.99
Virtualization: VT-x
L1d cache: 32K

(Continued on next page)
Supermicro
SuperServer 5019C-R (X11SCW-F, Intel Core i3-8100T)

**SPECrate®2017_fp_base = 24.1**
**SPECrate®2017_fp_peak = 24.6**

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>001176</th>
<th>Test Date:</th>
<th>Sep-2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Supermicro</td>
<td>Hardware Availability:</td>
<td>Nov-2018</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Supermicro</td>
<td>Software Availability:</td>
<td>Nov-2018</td>
</tr>
</tbody>
</table>

**SPEC CPU®2017 Floating Point Rate Result**

**Platform Notes (Continued)**

L1i cache: 32K
L2 cache: 256K
L3 cache: 6144K
NUMA node0 CPU(s): 0-3

Flags: fpu vme de pse mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 sdbg fma
cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch arat epb invpcid_single pln pts dtherm hwp
hwp_notify hwp_act_window hwp_epp intel_pt rsb_ctxsw spec_ctrl retpoline kaiser
tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms
invpcid mxp rdseed adx smap clflushopt xsaveopt xsavec xgetbv1

/pro/cpusinfo cache data
  cache size: 6144 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0 1 2 3
  node 0 size: 64332 MB
  node 0 free: 55538 MB
  node distances:
    node 0
    0: 10

From /proc/meminfo
  MemTotal: 65876956 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 3
    # This file is deprecated and will be removed in a future service pack or release.
    # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP3"
    VERSION_ID="12.3"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp3"

(Continued on next page)
Spec CPU®2017 Floating Point Rate Result

Supermicro
SuperServer 5019C-WR (X11SCW-F, Intel Core i3-8100T) SPECrate®2017_fp_base = 24.1
SPECrate®2017_fp_peak = 24.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Sep-2019
Hardware Availability: Nov-2018
Software Availability: Nov-2018

Platform Notes (Continued)

uname -a:
Linux linux-65nv 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 3 Sep 9 15:48
SPEC is set to: /home/cpu2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 145G 21G 124G 15% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
- BIOS American Megatrends Inc. 1.0b 05/16/2019
- Memory:
  4x Micron 18ADF2G72AZ-2G6H1R 16 GB 2 rank 2667, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C  | 519.lbm_r(base, peak) 538.imagick_r(base, peak)
  | 544.nab_r(base, peak)
==============================================================================

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)
==============================================================================

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Supermicro
SuperServer 5019C-WR (X11SCW-F, Intel Core i3-8100T)

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Compiler Version Notes (Continued)

C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
-------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

C++, C, Fortran | 507.cactuBSSN_r(base, peak)
-------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak)
 | 554.roms_r(base, peak)
-------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
-------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Supermicro
SuperServer 5019C-WR (X11SCW-F, Intel Core i3-8100T)

SPECrate®2017_fp_base = 24.1
SPECrate®2017_fp_peak = 24.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Sep-2019
Hardware Availability: Nov-2018
Software Availability: Nov-2018

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
Supermicro
SuperServer 5019C-WR (X11SCW-F, Intel Core i3-8100T)

SPECrate®2017_fp_base = 24.1
SPECrate®2017_fp_peak = 24.6

Base Optimization Flags (Continued)

C++ benchmarks (continued):
-qopt-mem-layout-trans=4

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64
Supermicro
SuperServer 5019C-WR (X11SCW-F, Intel Core i3-8100T)

SPECrate®2017_fp_base = 24.1
SPECrate®2017_fp_peak = 24.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Sep-2019
Hardware Availability: Nov-2018
Software Availability: Nov-2018

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4
538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4
544.nab_r: Same as 538.imagick_r

C++ benchmarks:
508.namd_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4
510.parest_r: basepeak = yes

Fortran benchmarks:
503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -auto
-nostandard-realloc-lhs -align array32byte
549.fotonik3d_r: Same as 503.bwaves_r
554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:

(Continued on next page)
**Supermicro**
SuperServer 5019C-WR (X11SCW-F, Intel Core i3-8100T)

**SPECrate®2017_fp_base = 24.1**
**SPECrate®2017_fp_peak = 24.6**

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<th>Test Date: Sep-2019</th>
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<tr>
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<td>Software Availability: Nov-2018</td>
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</table>

**Peak Optimization Flags (Continued)**

511.povray_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

526.blender_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

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Tested with SPEC CPU®2017 v1.0.5 on 2019-09-09 09:24:55-0400.
Originally published on 2019-10-01.