### SPEC CPU®2017 Integer Rate Result

**Fujitsu**  
PRIMERGY RX2540 M5, Intel Xeon Gold 6246, 3.30 GHz

**SPECRate®2017_int_base = 182**  
**SPECRate®2017_int_peak = Not Run**

<table>
<thead>
<tr>
<th>Tested by:</th>
<th>Fujitsu</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Fujitsu</td>
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<tr>
<td>CPU2017 License:</td>
<td>19</td>
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<tr>
<td>Test Date:</td>
<td>May-2019</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>May-2019</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Feb-2019</td>
</tr>
</tbody>
</table>

#### Hardware

- **CPU Name:** Intel Xeon Gold 6246  
- **Max MHz:** 4200  
- **Nominal:** 3300  
- **Enabled:** 24 cores, 2 chips, 2 threads/core  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **Cache L2:** 1 MB I+D on chip per core  
- **Cache L3:** 24.75 MB I+D on chip per chip  
- **Memory:** 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R)  
- **Storage:** 1 x SATA M.2 SSD, 480 GB  
- **Other:** None

#### Software

- **OS:** SUSE Linux Enterprise Server 15  
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++ Compiler Build 20181018 for Linux; Fortran: Version 19.0.1.144 of Intel Fortran Compiler Build 20181018 for Linux  
- **Firmware:** Fujitsu BIOS Version V5.0.0.14 R1.8.0 for D3384-B1x. Released Jun-2019 tested as V5.0.0.14 R1.2.0 for D3384-B1x Feb-2019  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** Not Applicable  
- **Other:** None  
- **Power Management:** --
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Results Table

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</tbody>
</table>

SPECrade®2017_int_base = 182
SPECrade®2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Kernel Boot Parameter set with : nohz_full=1-111

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/Benchmark/speccpu2017-1.0.5/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

(Continued on next page)
General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Patrol Scrub = Disabled
WR CRC feature Control = Disabled
DCU Ip Prefetcher = Disabled
DCU Streamer Prefetcher = Disabled
Stale AtoS = Enable
Fan Control = Full
Sysinfo program /home/Benchmark/speccpu2017-1.0.5/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on RX2540M5 Tue May 28 11:12:11 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6246 CPU @ 3.30GHz
  2 "physical id"s (chips)
  48 "processors"
core, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 12
  siblings : 24
  physical 0: cores 0 2 4 8 9 10 17 18 19 20 25 27
  physical 1: cores 0 1 2 4 8 9 10 11 17 18 19 27

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 2
Core(s) per socket: 12
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6246 CPU @ 3.30GHz
Stepping: 7

(Continued on next page)
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Test Sponsor: Fujitsu
Tested by: Fujitsu

CPU MHz: 3300.000
CPU max MHz: 4200.0000
CPU min MHz: 1200.0000
BogoMIPS: 6600.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 25344K
NUMA node0 CPU(s): 0,1,3,4,6,10,24,25,27,28,30,34
NUMA node1 CPU(s): 2,5,7-9,11,26,29,31-33,35
NUMA node2 CPU(s): 12-14,16,17,20,36-38,40,41,44
NUMA node3 CPU(s): 15,18,19,21-23,39,42,43,45-47

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xapic msr dtes64 monitor ds_cpl vmx
smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave f16c rdrand lahf_lm abah_set mtrr hypep pni cpuid_fault epb cat_l3 cdp_l3
invpcid_single ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vni flxpriority
pt vpid fsgsbase tsc_adjust blklld hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a
avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
xsaveopt xsavec xsavec xsavec xsavec xsavec xsavec xsavec xsavec xsavec xsavec xsavec xsavec

/platform/cpusinfo cache data

cache size : 25344 KB

From numactl -h WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 3 4 6 10 24 25 27 28 30 34
node 0 size: 191969 MB
node 0 free: 191579 MB
node 1 cpus: 2 5 7 8 9 11 26 29 31 32 33 35
node 1 size: 193533 MB
node 1 free: 193281 MB
node 2 cpus: 12 13 14 16 17 20 23 24 25 27 28 30 34
node 2 size: 193504 MB
node 2 free: 193257 MB
node 3 cpus: 15 18 19 21 22 23 29 32 34 40 41 44
node 3 size: 193321 MB
node 3 free: 193072 MB
node distances:
node 0 1 2 3
0: 10 11 21 21

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Platform Notes (Continued)

1:  11  10  21  21
2:  21  21  10  11
3:  21  21  11  10

From /proc/meminfo
MemTotal:       790864360 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
os-release:
  NAME="SLES"
  VERSION="15"
  VERSION_ID="15"
  PRETTY_NAME="SUSE Linux Enterprise Server 15"
  ID="sles"
  ID_LIKE="suse"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
  Linux RX2540M5 4.12.14-25.28-default #1 SMP Wed Jan 16 20:00:47 UTC 2019 (dd6077c)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 May 28 11:10

SPEC is set to: /home/Benchmark/speccpu2017-1.0.5
  Filesystem     Type  Size  Used Avail Use% Mounted on
  /dev/sda5      xfs   191G   57G  134G  30% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS FUJITSU // American Megatrends Inc. V5.0.0.14 R1.2.0 for D3384-B1x
  02/28/2019
  Memory:
    24x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933, configured at 2934

(End of data from sysinfo program)
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Hardware Availability: May-2019
Software Availability: Feb-2019

Compiler Version Notes

==============================================================================
| C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base)  
|         | 525.x264_r(base) 557.xz_r(base)       |
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
| C++     | 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)  
|         | 541.leela_r(base)     |
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
| Fortran | 548.exchange2_r(base) |
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64

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**Base Portability Flags (Continued)**

- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
- 525.x264_r: -DSPEC_LP64
- 531.deepsjeng_r: -DSPEC_LP64
- 541.leela_r: -DSPEC_LP64
- 548.exchange2_r: -DSPEC_LP64
- 557.xz_r: -DSPEC_LP64

**Base Optimization Flags**

**C benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4
- -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
- -lqkmalloc

**C++ benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4
- -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
- -lqkmalloc

**Fortran benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
- -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
- -lqkmalloc

The flags files that were used to format this result can be browsed at:

You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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