## SPEC CPU®2017 Floating Point Speed Result

**Tyrone Systems**  
(Test Sponsor: Netweb)  
**DIT400TR-28RL**  
(2.20 GHz, Intel Xeon Silver 4210)  

<table>
<thead>
<tr>
<th>Test Sponsor:</th>
<th>Netweb</th>
<th>Test Date:</th>
<th>Oct-2019</th>
<th>Hardware Availability:</th>
<th>Sep-2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU2017 License:</td>
<td>006042</td>
<td>Test Sponsor:</td>
<td>Netweb</td>
<td>Hardware Availability:</td>
<td>Aug-2019</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Netweb</td>
<td>Software Availability:</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>Threads</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>603.bwaves_s</td>
<td>20</td>
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<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>20</td>
<td>67.8</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>20</td>
<td>68.0</td>
<td>73.9</td>
<td></td>
<td></td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>20</td>
<td>76.4</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>20</td>
<td>46.3</td>
<td>61.9</td>
<td></td>
<td></td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>40</td>
<td>55.1</td>
<td>50.6</td>
<td></td>
<td></td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>20</td>
<td>58.6</td>
<td>58.6</td>
<td></td>
<td></td>
</tr>
<tr>
<td>644.nab_s</td>
<td>40</td>
<td>106</td>
<td>123</td>
<td></td>
<td></td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>20</td>
<td>66.6</td>
<td>65.2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>654.roms_s</td>
<td>20</td>
<td>72.1</td>
<td>72.3</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### SPECspeed®2017_fp_base = 80.9

### SPECspeed®2017_fp_peak = 83.9

#### Hardware

- **CPU Name:** Intel Xeon Silver 4210  
- **Max MHz:** 3200  
- **Nominal:** 2200  
- **Enabled:** 20 cores, 2 chips, 2 threads/core  
- **Orderable:** 1, 2 chip(s)  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 13.75 MB I+D on chip per chip  
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933P-R, running at 2400)  
- **Storage:** 1 x 480 GB SSD  
- **Other:** None

#### Software

- **OS:** CentOS Linux release 7.7.1908 (Core)  
- **Compiler:** C/C++: Version 19.0.4.243 of Intel C/C++ Compiler Build 20190416 for Linux; Fortran: Version 19.0.4.243 of Intel Fortran Compiler Build 20190416 for Linux  
- **Parallel:** Yes  
- **Firmware:** Version V8.101 released Aug-2019  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** None  
- **Power Management:** None
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>20</td>
<td>169</td>
<td>350</td>
<td>168</td>
<td>350</td>
<td>169</td>
<td>349</td>
<td>20</td>
<td>169</td>
<td>349</td>
<td>169</td>
<td>350</td>
<td>169</td>
<td>350</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>20</td>
<td>191</td>
<td>87.5</td>
<td>188</td>
<td>88.6</td>
<td>189</td>
<td>88.4</td>
<td>20</td>
<td>189</td>
<td>88.4</td>
<td>190</td>
<td>87.8</td>
<td>191</td>
<td>87.4</td>
</tr>
<tr>
<td>619.lbm_s</td>
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<td>68.6</td>
<td>76.1</td>
<td>68.8</td>
<td>76.6</td>
<td>68.4</td>
<td>20</td>
<td>76.9</td>
<td>68.1</td>
<td>76.6</td>
<td>68.3</td>
<td>76.6</td>
<td>68.4</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>20</td>
<td>182</td>
<td>72.8</td>
<td>179</td>
<td>73.9</td>
<td>179</td>
<td>74.0</td>
<td>20</td>
<td>173</td>
<td>76.4</td>
<td>174</td>
<td>76.2</td>
<td>172</td>
<td>77.1</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>20</td>
<td>191</td>
<td>46.3</td>
<td>192</td>
<td>46.3</td>
<td>191</td>
<td>46.3</td>
<td>40</td>
<td>143</td>
<td>62.0</td>
<td>143</td>
<td>61.9</td>
<td>143</td>
<td>61.8</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>20</td>
<td>214</td>
<td>55.4</td>
<td>216</td>
<td>54.9</td>
<td>216</td>
<td>55.1</td>
<td>40</td>
<td>236</td>
<td>50.4</td>
<td>234</td>
<td>50.7</td>
<td>235</td>
<td>50.6</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>20</td>
<td>246</td>
<td>58.7</td>
<td>246</td>
<td>58.6</td>
<td>246</td>
<td>58.6</td>
<td>20</td>
<td>247</td>
<td>58.4</td>
<td>246</td>
<td>58.6</td>
<td>246</td>
<td>58.6</td>
</tr>
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<td>106</td>
<td>165</td>
<td>106</td>
<td>165</td>
<td>106</td>
<td>40</td>
<td>142</td>
<td>123</td>
<td>143</td>
<td>122</td>
<td>143</td>
<td>123</td>
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<td>138</td>
<td>65.9</td>
<td>136</td>
<td>66.8</td>
<td>137</td>
<td>66.6</td>
<td>20</td>
<td>136</td>
<td>66.9</td>
<td>138</td>
<td>66.2</td>
<td>140</td>
<td>65.1</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>20</td>
<td>218</td>
<td>72.1</td>
<td>218</td>
<td>72.3</td>
<td>218</td>
<td>72.1</td>
<td>20</td>
<td>218</td>
<td>72.2</td>
<td>217</td>
<td>72.4</td>
<td>218</td>
<td>72.3</td>
</tr>
</tbody>
</table>

SPECspeed®2017_fp_base = 80.9
SPECspeed®2017_fp_peak = 83.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Compiler Notes

SPEC has learned that this result, which used an evaluation compiler, was submitted contrary to the compiler license terms.

Intel has granted a one-time waiver for this result.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

- `KMP_AFFINITY = "granularity=fine,compact,1,0"
- `LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64"
- `OMP_STACKSIZE = "192M"

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3>/proc/sys/vm/drop_caches
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
Tyrone Systems
(2.20 GHz, Intel Xeon Silver 4210)

CPU2017 License: 006042
Test Sponsor: Netweb
Tested by: Netweb

SPECspeed®2017_fp_base = 80.9
SPECspeed®2017_fp_peak = 83.9

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7ed4b8e6e46a485a0011
running on NODE2 Thu Oct 10 02:02:20 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4210 CPU @ 2.20GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 10
  siblings : 20
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian
  CPU(s): 40
  On-line CPU(s) list: 0-39
  Thread(s) per core: 2
  Core(s) per socket: 10
  Socket(s): 2
  NUMA node(s): 2
  Vendor ID: GenuineIntel
  CPU family: 6
  Model: 85
  Model name: Intel(R) Xeon(R) Silver 4210 CPU @ 2.20GHz
  Stepping: 7
  CPU MHz: 1001.037
  CPU max MHz: 3200.000
  CPU min MHz: 1000.000
  BogoMIPS: 4400.00

(Continued on next page)
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(2.20 GHz, Intel Xeon Silver 4210)

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**SPECspeed®2017_fp_base = 80.9**

**SPECspeed®2017_fp_peak = 83.9**

---

**CPU2017 License:** 006042  
**Test Sponsor:** Netweb  
**Tested by:** Netweb

---

**Platform Notes (Continued)**

- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 1024K
- L3 cache: 14080K
- NUMA node0 CPU(s): 0-9, 20-29
- NUMA node1 CPU(s): 10-19, 30-39
- Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
- /proc/cpuinfo cache data
  - cache size: 14080 KB

---

```
From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
    available: 2 nodes (0-1)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 9 20 21 22 23 24 25 26 27 28 29
    node 0 size: 195229 MB
    node 0 free: 166562 MB
    node 1 cpus: 10 11 12 13 14 15 16 17 18 19 30 31 32 33 34 35 36 37 38 39
    node 1 size: 196608 MB
    node 1 free: 168811 MB
    node distances:
      node   0   1
      0:  10  21
      1:  21  10
```

---

```
From /proc/meminfo
    MemTotal: 394864496 kB
    HugePages_Total: 0
    Hugepagesize: 2048 kB
```

---

```
From /etc/*release* /etc/*version*
    centos-release: CentOS Linux release 7.7.1908 (Core)
    centos-release-upstream: Derived from Red Hat Enterprise Linux 7.7 (Source)
    os-release: NAME="CentOS Linux"
```

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(Continued on next page)
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SPECspeed®2017_fp_base = 80.9
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CPU2017 License: 006042
Test Sponsor: Netweb
Tested by: Netweb

Platform Notes (Continued)

VERSION="7 (Core)"
ID="centos"
ID_LIKE="rhel fedora"
VERSION_ID="7"
PRETTY_NAME="CentOS Linux 7 (Core)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:centos:centos:7"
redhat-release: CentOS Linux release 7.7.1908 (Core)
system-release: CentOS Linux release 7.7.1908 (Core)
system-release-cpe: cpe:/o:centos:centos:7

uname -a:
Linux NODE2 3.10.0-1062.el7.x86_64 #1 SMP Wed Aug 7 18:08:02 UTC 2019 x86_64 x86_64
x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-3620 (L1 Terminal Fault):        Not affected
Microarchitectural Data Sampling:         Not affected
CVE-2017-5754 (Meltdown):                Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
                                          via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):        Mitigation: Load fences, __user pointer
                                          sanitization
CVE-2017-5715 (Spectre variant 2):        Mitigation: Full retpoline, IBPB

run-level 3 Oct 8 10:00

SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/centos-home xfs 392G 122G 271G 32% /home

From /sys/devices/virtual/dmi/id
BIOS: American Megatrends Inc. V8.101 08/02/2019
Vendor: Tyrone Systems
Product: TP12XH-L2I
Serial: empty

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
12x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)
Tyrone Systems  
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DIT400TR-28RL  
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CPU2017 License: 006042  
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Tested by: Netweb

Compiler Version Notes

==============================================================================
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak)  
| 644.nab_s(base, peak)  
==============================================================================
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.243 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
icc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.

==============================================================================
C++, C, Fortran | 607.cactuBSSN_s(base, peak)  
==============================================================================
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.243 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
icc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.

==============================================================================
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)  
| 654.roms_s(base, peak)  
==============================================================================
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.243 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
ifort: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.

==============================================================================
Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak)  
| 628.pop2_s(base, peak)  
==============================================================================
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,  
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(Continued on next page)
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<tr>
<th>SPECspeed®2017_fp_peak</th>
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</tr>
</thead>
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<tr>
<td>SPECspeed®2017_fp_base</td>
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</tr>
</tbody>
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CPU2017 License: 006042
Test Sponsor: Netweb
Tested by: Netweb

Compiler Version Notes (Continued)

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icc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp

(Continued on next page)
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| Tested by: Netweb | Software Availability: Aug-2019 |

**Base Optimization Flags (Continued)**

Fortran benchmarks (continued):
- nostandard-realloc-lhs

Benchmarks using both Fortran and C:
- xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
- ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
- nostandard-realloc-lhs

Benchmarks using Fortran, C, and C++:
- xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
- ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
- nostandard-realloc-lhs

**Peak Compiler Invocation**

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

**Peak Portability Flags**

Same as Base Portability Flags

**Peak Optimization Flags**

C benchmarks:
xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:

(Continued on next page)
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Test Date: Oct-2019
Hardware Availability: Sep-2019
Software Availability: Aug-2019

Peak Optimization Flags (Continued)

603.bwaves_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
649.fotonik3d_s: Same as 603.bwaves_s
654.roms_s: -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs

Benchmarks using both Fortran and C:
621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
627.cam4_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

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