## Lenovo Global Technology

ThinkSystem SN550  
(2.10 GHz, Intel Xeon Gold 5218T)

<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS: SUSE Linux Enterprise Server 15 (x86_64)</td>
<td>CPU Name: Intel Xeon Gold 5218T</td>
</tr>
<tr>
<td>Compiler: C/C++: Version 19.0.4.227 of Intel</td>
<td>Nominal: 2100</td>
</tr>
<tr>
<td>Compiler for Linux; Fortran: Version 19.0.4.227 of Intel Fortran</td>
<td>Enabled: 32 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>No</td>
<td>Orderable: 1,2 chips</td>
</tr>
<tr>
<td>Lenovo BIOS Version IVE142E 2.30 released Aug-2019 tested as IVE141E 2.30 Jul-2019</td>
<td>Cache L1: 32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>File System: xfs</td>
<td>L2: 1 MB I+D on chip per core</td>
</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
<td>L3: 22 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Base Pointers: 64-bit</td>
<td>Other: None</td>
</tr>
<tr>
<td>Peak Pointers: Not Applicable</td>
<td>Power Management: --</td>
</tr>
</tbody>
</table>

### SPEC CPU®2017 Floating Point Rate Result

<table>
<thead>
<tr>
<th>Copy Number</th>
<th>Benchmark</th>
<th>Result</th>
</tr>
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<tbody>
<tr>
<td>64</td>
<td>503.bwaves_r</td>
<td>139</td>
</tr>
<tr>
<td>64</td>
<td>507.cactuBSSN_r</td>
<td>122</td>
</tr>
<tr>
<td>64</td>
<td>508.namd_r</td>
<td>104</td>
</tr>
<tr>
<td>64</td>
<td>510.parest_r</td>
<td>183</td>
</tr>
<tr>
<td>64</td>
<td>511.povray_r</td>
<td>104</td>
</tr>
<tr>
<td>64</td>
<td>519.lbm_r</td>
<td>186</td>
</tr>
<tr>
<td>64</td>
<td>521.wrf_r</td>
<td>183</td>
</tr>
<tr>
<td>64</td>
<td>526.blender_r</td>
<td>177</td>
</tr>
<tr>
<td>64</td>
<td>527.cam4_r</td>
<td>361</td>
</tr>
<tr>
<td>64</td>
<td>538.imagick_r</td>
<td>272</td>
</tr>
<tr>
<td>64</td>
<td>544.nab_r</td>
<td>148</td>
</tr>
<tr>
<td>64</td>
<td>549.fotonik3d_r</td>
<td>83.0</td>
</tr>
<tr>
<td>64</td>
<td>554.roms_r</td>
<td>542</td>
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**SPECRATE®2017_FP_BASE = 172**  
**SPECRATE®2017_FP_PEAK = Not Run**
Lenovo Global Technology

ThinkSystem SN550
(2.10 GHz, Intel Xeon Gold 5218T)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology
Test Date: Sep-2019
Hardware Availability: Jul-2019
Software Availability: May-2019

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
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<td>1397</td>
<td>460</td>
<td>1396</td>
<td>460</td>
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<td>507.cactuBSSN_r</td>
<td>64</td>
<td>585</td>
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<td>139</td>
<td>584</td>
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<tr>
<td>508.namd_r</td>
<td>64</td>
<td>498</td>
<td>122</td>
<td>496</td>
<td>123</td>
<td>499</td>
<td>122</td>
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<tr>
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<tr>
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<td>815</td>
<td>183</td>
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<td>104</td>
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<tr>
<td>521.wrf_r</td>
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<td>767</td>
<td>187</td>
<td>780</td>
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<td>183</td>
<td>532</td>
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<td>533</td>
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<tr>
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<td>177</td>
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<td>364</td>
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</tr>
<tr>
<td>544.nab_r</td>
<td>64</td>
<td>396</td>
<td>272</td>
<td>399</td>
<td>270</td>
<td>396</td>
<td>272</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>64</td>
<td>1683</td>
<td>148</td>
<td>1682</td>
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<td>1682</td>
<td>148</td>
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<tr>
<td>554.roms_r</td>
<td>64</td>
<td>1226</td>
<td>83.0</td>
<td>1225</td>
<td>83.0</td>
<td>1231</td>
<td>82.6</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0u4/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
Lenovo Global Technology
ThinkSystem SN550
(2.10 GHz, Intel Xeon Gold 5218T)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

SPECrate®2017_fp_base = 172
SPECrate®2017_fp_peak = Not Run

Test Date: Sep-2019
Hardware Availability: Jul-2019
Software Availability: May-2019

General Notes (Continued)
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

Platform Notes
BIOS configuration:
Choose Operating Mode set to Maximum Performance
Trusted Execution Technology set to Enable
SNC set to Enable
CPU Frequency Limits set to Restrict Maximum Frequency
Workload Configuration set to I/O Sensitive
Sysinfo program /home/cpu2017-1.0.5-ic19.0u4/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-4brr Wed Sep 18 00:26:02 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5218T CPU @ 2.10GHz
  2 "physical id"s (chips)
  64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 16
  siblings : 32
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Thread(s) per core: 2
Core(s) per socket: 16
Socket(s): 2
NUMA node(s): 4

(Continued on next page)
**SPEC CPU®2017 Floating Point Rate Result**

**Lenovo Global Technology**

ThinkSystem SN550
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<thead>
<tr>
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<th>172</th>
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<tbody>
<tr>
<td>SPECrate®2017_fp_peak</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Platform Notes (Continued)**

Vendor ID: GenuineIntel  
CPU family: 6  
Model: 85  
Model name: Intel(R) Xeon(R) Gold 5218T CPU @ 2.10GHz  
Stepping: 7  
CPU MHz: 2100.000  
CPU max MHz: 3800.0000  
CPU min MHz: 1000.0000  
BogoMIPS: 4200.00  
Virtualization: VT-x  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 1024K  
L3 cache: 22528K  
NUMA node0 CPU(s): 0-3, 8-11, 32-35, 40-43  
NUMA node1 CPU(s): 4-7, 12-15, 36-39, 44-47  
NUMA node2 CPU(s): 16-19, 24-27, 50-53, 56-59  
NUMA node3 CPU(s): 20-23, 28-31, 52-55, 60-63  
Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperf perfctr pdpe32 tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_pinn mba ibrs ibpb stibp tpr_shadow vmmx flexpriority ept psp fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local dtherm ida arat pln pts pku ospke avx512_vnni flush_lid arch_capabilities

From numactl --hardware  
WARNING: a numactl 'node' might or might not correspond to a physical chip.

<table>
<thead>
<tr>
<th>available: 4 nodes (0-3)</th>
</tr>
</thead>
<tbody>
<tr>
<td>node 0 cpus: 0 1 2 3 8 9 10 11 32 33 34 35 40 41 42 43</td>
</tr>
<tr>
<td>node 0 size: 96337 MB</td>
</tr>
<tr>
<td>node 0 free: 90098 MB</td>
</tr>
<tr>
<td>node 1 cpus: 4 5 6 7 12 13 14 15 36 37 38 39 44 45 46 47</td>
</tr>
<tr>
<td>node 1 size: 96754 MB</td>
</tr>
<tr>
<td>node 1 free: 96360 MB</td>
</tr>
<tr>
<td>node 2 cpus: 16 17 18 19 24 25 26 27 48 49 50 51 56 57 58 59</td>
</tr>
<tr>
<td>node 2 size: 96754 MB</td>
</tr>
<tr>
<td>node 2 free: 96555 MB</td>
</tr>
<tr>
<td>node 3 cpus: 20 21 22 23 28 29 30 31 52 53 54 55 60 61 62 63</td>
</tr>
<tr>
<td>node 3 size: 96752 MB</td>
</tr>
</tbody>
</table>

(Continued on next page)
### Lenovo Global Technology

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<table>
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</tr>
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<tbody>
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</tr>
</tbody>
</table>

**Platform Notes (Continued)**

```plaintext
node 3 free: 96558 MB
node distances:
node 0 1 2 3
  0: 10 11 21 21
  1: 11 10 21 21
  2: 21 21 10 11
  3: 21 21 11 10

From /proc/meminfo
MemTotal: 395877452 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  os-release:
  NAME="SLES"
  VERSION="15"
  VERSION_ID="15"
  PRETTY_NAME="SUSE Linux Enterprise Server 15"
  ID="sles"
  ID_LIKE="suse"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 Sep 18 00:24

SPEC is set to: /home/cpu2017-1.0.5-ic19.0u4
  Filesystem Type Size Used Avail Use% Mounted on
  /dev/sdb3     xfs  891G 76G 816G  9% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS Lenovo -[IVE141E-2.30]- 07/02/2019
  Memory:
```
(Continued on next page)
Lenovo Global Technology
ThinkSystem SN550
(2.10 GHz, Intel Xeon Gold 5218T)

SPEC CPU®2017 Floating Point Rate Result
Copyright 2017-2019 Standard Performance Evaluation Corporation

Lenovo Global Technology
ThinkSystem SN550
(2.10 GHz, Intel Xeon Gold 5218T)

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CPU2017 License: 9017
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Test Date: Sep-2019
Hardware Availability: Jul-2019
Software Availability: May-2019

Platform Notes (Continued)

24x Samsung M393A2K43CB2-CVF 16 GB 2 rank 2933, configured at 2666
(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C               | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Compiler Version Notes (Continued)

==============================================================================
C++             | 508.namd_r(base) 510.parest_r(base)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Compiler Version Notes (Continued)

==============================================================================
C++, C          | 511.povray_r(base) 526.blender_r(base)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Compiler Version Notes (Continued)

==============================================================================
C++, C, Fortran | 507.cactuBSSN_r(base)
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Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.4.227 Build 20190416
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Compiler Version Notes (Continued)

==============================================================================
Fortran         | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
Fortran, C      | 521.wrf_r(base) 527.cam4_r(base)
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Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
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------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

503.bwaves_r: -DSPEC_LP64

(Continued on next page)
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Base Portability Flags (Continued)

507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte
**Lenovo Global Technology**

**ThinkSystem SN550 (2.10 GHz, Intel Xeon Gold 5218T)**

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The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-F.html](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-F.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-F.xml](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-F.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.0.5 on 2019-09-17 12:26:01-0400.  
Originally published on 2019-10-29.