## SPEC CPU®2017 Floating Point Speed Result

### Hardware

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon Gold 6248</td>
</tr>
<tr>
<td>Max MHz</td>
<td>3900</td>
</tr>
<tr>
<td>Nominal</td>
<td>2500</td>
</tr>
<tr>
<td>Enabled</td>
<td>40 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable</td>
<td>1, 2 (chip)</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3</td>
<td>27.5 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>384 GB (12 x 32 GB 2Rx4 PC4-2933P-R)</td>
</tr>
<tr>
<td>Storage</td>
<td>1 x 480 GB SSD</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
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</thead>
<tbody>
<tr>
<td>OS</td>
<td>CentOS Linux release 7.7.1908 (Core)</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 19.0.4.243 of Intel C/C++</td>
</tr>
<tr>
<td>Compiler Build</td>
<td>20190416 for Linux</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
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### Test Information

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<thead>
<tr>
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<tbody>
<tr>
<td>Test Date</td>
<td>Oct-2019</td>
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<tr>
<td>Hardware Availability</td>
<td>Sep-2019</td>
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<tr>
<td>Software Availability</td>
<td>Aug-2019</td>
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</table>

### Tyrone Systems

<table>
<thead>
<tr>
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<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor</td>
<td>Netweb Pte Ltd</td>
</tr>
<tr>
<td>Hardware</td>
<td>DIT400TR-28RL (2.50 GHz, Intel Xeon Gold 6248)</td>
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</table>

### Test Result

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
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<tbody>
<tr>
<td>SPECspeed®2017_fp_base</td>
<td>132</td>
</tr>
<tr>
<td>SPECspeed®2017_fp_peak</td>
<td>137</td>
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>40</td>
<td>124</td>
<td>476</td>
<td>128</td>
<td>462</td>
<td>126</td>
<td>467</td>
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<tr>
<td>607.cactuBSSN_s</td>
<td>40</td>
<td>114</td>
<td>146</td>
<td>115</td>
<td>145</td>
<td>115</td>
<td>145</td>
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<tr>
<td>619.lbm_s</td>
<td>40</td>
<td>58.2</td>
<td>89.9</td>
<td>55.6</td>
<td>94.2</td>
<td>55.7</td>
<td>94.1</td>
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<tr>
<td>621.wrf_s</td>
<td>40</td>
<td>109</td>
<td>122</td>
<td>108</td>
<td>122</td>
<td>109</td>
<td>122</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>40</td>
<td>91.8</td>
<td>96.6</td>
<td>91.3</td>
<td>97.1</td>
<td>91.4</td>
<td>96.9</td>
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<tr>
<td>628.pop2_s</td>
<td>40</td>
<td>175</td>
<td>67.2</td>
<td>175</td>
<td>67.7</td>
<td>176</td>
<td>67.6</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>40</td>
<td>120</td>
<td>120</td>
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<td>121</td>
<td>120</td>
<td>120</td>
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<tr>
<td>644.nab_s</td>
<td>40</td>
<td>76.7</td>
<td>228</td>
<td>76.7</td>
<td>228</td>
<td>76.9</td>
<td>227</td>
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<tr>
<td>649.fotonik3d_s</td>
<td>40</td>
<td>108</td>
<td>84.4</td>
<td>108</td>
<td>84.3</td>
<td>108</td>
<td>84.7</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>40</td>
<td>120</td>
<td>131</td>
<td>120</td>
<td>131</td>
<td>119</td>
<td>132</td>
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</table>

Compilers:

SPECspeed®2017_fp_base = 132
SPECspeed®2017_fp_peak = 137

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes
SPEC has learned that this result, which used an evaluation compiler, was submitted contrary to the compiler license terms.
Intel has granted a one-time waiver for this result.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64"
OMP_STACKSIZE = "192M"

General Notes
Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
DIT400TR-28RL
(2.50 GHz, Intel Xeon Gold 6248)

SPECspeed®2017_fp_base = 132
SPECspeed®2017_fp_peak = 137

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7ed1be6e46a485a0011
running on NODE6 Thu Oct 10 10:43:37 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6248 CPU @ 2.50GHz
  2 "physical id"s (chips)
  80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 20
siblings : 40
physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

From lscpu:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                80
On-line CPU(s) list:   0-79
Thread(s) per core:    2
Core(s) per socket:    20
Socket(s):             2
NUMA node(s):          2
Vendor ID:             GenuineIntel
CPU family:            6
Model:                 85
Model name:            Intel(R) Xeon(R) Gold 6248 CPU @ 2.50GHz
Stepping:              7
CPU MHz:               1000.061
CPU max MHz:           3900.0000
CPU min MHz:           1000.0000
BogoMIPS:              5000.00

(Continued on next page)
SPEC CPU®2017 Floating Point Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
DIT400TR-28RL
(2.50 GHz, Intel Xeon Gold 6248)

| SPECspeed®2017_fp_base | 132 |
| SPECspeed®2017_fp_peak | 137 |

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

| Test Date: | Oct-2019 |
| Hardware Availability: | Sep-2019 |
| Software Availability: | Aug-2019 |

Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 28160K
NUMA node0 CPU(s): 0-19,40-59
NUMA node1 CPU(s): 20-39,60-79
Flags: fpu vme de pse mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl pge mca cmov

Platform Notes (Continued)

/proc/cpuinfo cache data
  cache size : 28160 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 40 41 42 43 44 45 46 47
  node 0 size: 195228 MB
  node 0 free: 114589 MB
  node 1 cpus: 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 60 61 62 63 64
  node 1 size: 196608 MB
  node 1 free: 125117 MB
  node distances:
    node   0   1
    0:  10  21
    1:  21  10

From /proc/meminfo
  MemTotal: 394669116 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  centos-release: CentOS Linux release 7.7.1908 (Core)
  centos-release-upstream: Derived from Red Hat Enterprise Linux 7.7 (Source)

(Continued on next page)
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DIT400TR-28RL
(2.50 GHz, Intel Xeon Gold 6248)

SPECspeed®2017_fp_base = 132
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CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

Test Date: Oct-2019
Hardware Availability: Sep-2019
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Platform Notes (Continued)

os-release:
  NAME="CentOS Linux"
  VERSION="7 (Core)"
  ID="centos"
  ID_LIKE="rhel fedora"
  VERSION_ID="7"
  PRETTY_NAME="CentOS Linux 7 (Core)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:centos:centos:7"
redhat-release: CentOS Linux release 7.7.1908 (Core)
system-release: CentOS Linux release 7.7.1908 (Core)
system-release-cpe: cpe:/o:centos:centos:7

uname -a:
  Linux NODE6 3.10.0-1062.el7.x86_64 #1 SMP Wed Aug 7 18:08:02 UTC 2019 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full retpoline, IBPB

run-level 3 Oct 7 11:31
SPEC is set to: /home/cpu2017
  Filesystem Type Size Used Avail Use% Mounted on
  /dev/mapper/centos-home xfs 392G 182G 211G 47% /home

From /sys/devices/virtual/dmi/id
  BIOS: American Megatrends Inc. V8.101 08/02/2019
  Vendor: Tyrone Systems
  Product: TP12XH-L2I
  Serial: empty

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
  12x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

(Continued on next page)
SPEC CPU®2017 Floating Point Speed Result

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(2.50 GHz, Intel Xeon Gold 6248)

SPECspeed®2017_fp_base = 132
SPECspeed®2017_fp_peak = 137

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<tr>
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<tbody>
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<td>Netweb</td>
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<tr>
<td>Tested by:</td>
<td>Netweb</td>
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<tr>
<td>Test Date:</td>
<td>Oct-2019</td>
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<td>Sep-2019</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Aug-2019</td>
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Platform Notes (Continued)

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
<table>
<thead>
<tr>
<th>C</th>
<th>619.ibm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)</th>
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<tbody>
<tr>
<td>Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,</td>
<td></td>
</tr>
<tr>
<td>Version 19.0.4.243 Build 20190416</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2019 Intel Corporation. All rights reserved.</td>
<td></td>
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<tr>
<td>icc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.</td>
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|==============================================================================
<table>
<thead>
<tr>
<th>C++, C, Fortran</th>
<th>607.cactuBSSN_s(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,</td>
<td></td>
</tr>
<tr>
<td>Version 19.0.4.243 Build 20190416</td>
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<table>
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<tr>
<th>Fortran</th>
<th>603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)</th>
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<tr>
<td>Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)</td>
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<tr>
<td>64, Version 19.0.4.243 Build 20190416</td>
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<tr>
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<table>
<thead>
<tr>
<th>Fortran, C</th>
<th>621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)</th>
</tr>
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<tr>
<td>Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)</td>
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(2.50 GHz,Intel Xeon Gold 6248)

SPECspeed®2017_fp_base = 132
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CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

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icc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -03 -no-prec-div -qopt-prefetch

(Continued on next page)
**SPEC CPU®2017 Floating Point Speed Result**

Tyrone Systems  
(Test Sponsor: Netweb Pte Ltd)  
DIT400TR-28RL  
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**Tested by:** Netweb  

**Test Date:** Oct-2019  
**Hardware Availability:** Sep-2019  
**Software Availability:** Aug-2019

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**Base Optimization Flags (Continued)**

C benchmarks (continued):
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs

---

**Peak Compiler Invocation**

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

---

**Peak Portability Flags**

Same as Base Portability Flags

---

**Peak Optimization Flags**

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

(Continued on next page)
Peak Optimization Flags (Continued)

C benchmarks (continued):
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:

603.bwaves_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=4
-qopenmp -nostandard-realloc-lhs

649.fotonik3d_s: Same as 603.bwaves_s

654.roms_s: -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
-qopenmp -nostandard-realloc-lhs

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs

627.cam4_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
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**SPECspeed®2017_fp_base = 132**  
**SPECspeed®2017_fp_peak = 137**

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**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
DIT400TR-28RL  
(2.50 GHz, Intel Xeon Gold 6248)

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